

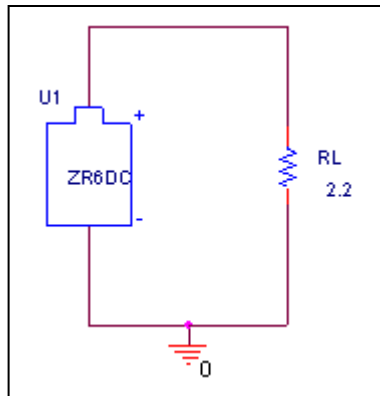
Device Modeling Report

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REMARK: $RL = 2.2(\Omega)$

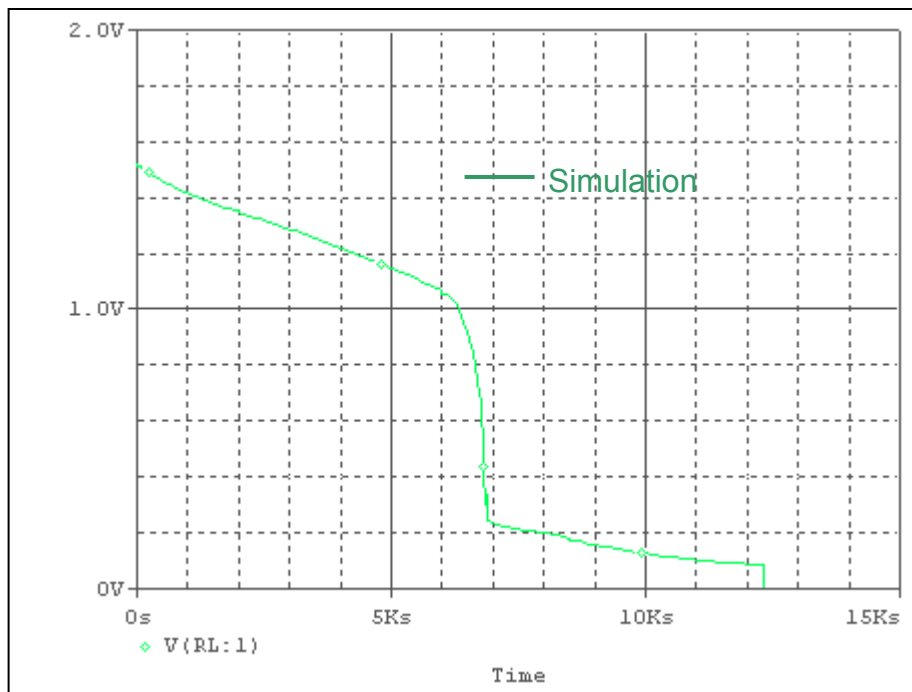


Bee Technologies Inc.

Evaluation circuit



Simulation result



Comparison Table

Time (kSec)	Vdc(V) Measurement	Vdc(V) Simulation	%Error
0.07	1.520941	1.5089	0.792
0.5	1.460279	1.4586	0.115
1	1.412444	1.412	0.031
1.5	1.379251	1.3776	0.120
2	1.348422	1.3481	0.024
2.5	1.317035	1.3166	0.033
3	1.284926	1.2848	0.010
3.5	1.25297	1.2534	-0.034
4	1.219569	1.2195	0.006
4.5	1.184385	1.1829	0.125
5	1.147668	1.1459	0.154
5.5	1.107855	1.1069	0.086
6	1.062679	1.0608	0.177
6.5	0.9077153	0.915299	-0.835
7	0.2356646	0.236686	-0.433
7.5	0.2131835	0.212551	0.297
8	0.2010245	0.200809	0.107
8.5	0.1786146	0.177384	0.689
9	0.1568601	0.156098	0.486
9.5	0.140452	0.140241	0.150
10	0.1253962	0.125675	-0.222
10.5	0.113303	0.11327	0.029
11	0.1034566	0.103206	0.242
11.5	0.09590364	0.0974944	-1.659
12	0.0887766	0.089023	-0.278
12.33	0.08439408	0.041005	51.412

Comparison Graph

