

Device Modeling Report

COMPONENTS: Power MOSFET (Standard)
PART NUMBER: 2SK3728-01MR
MANUFACTURER: Fuji Electric Co., Ltd.
REMARK: Body Diode (Standard)



POWER MOSFET MODEL

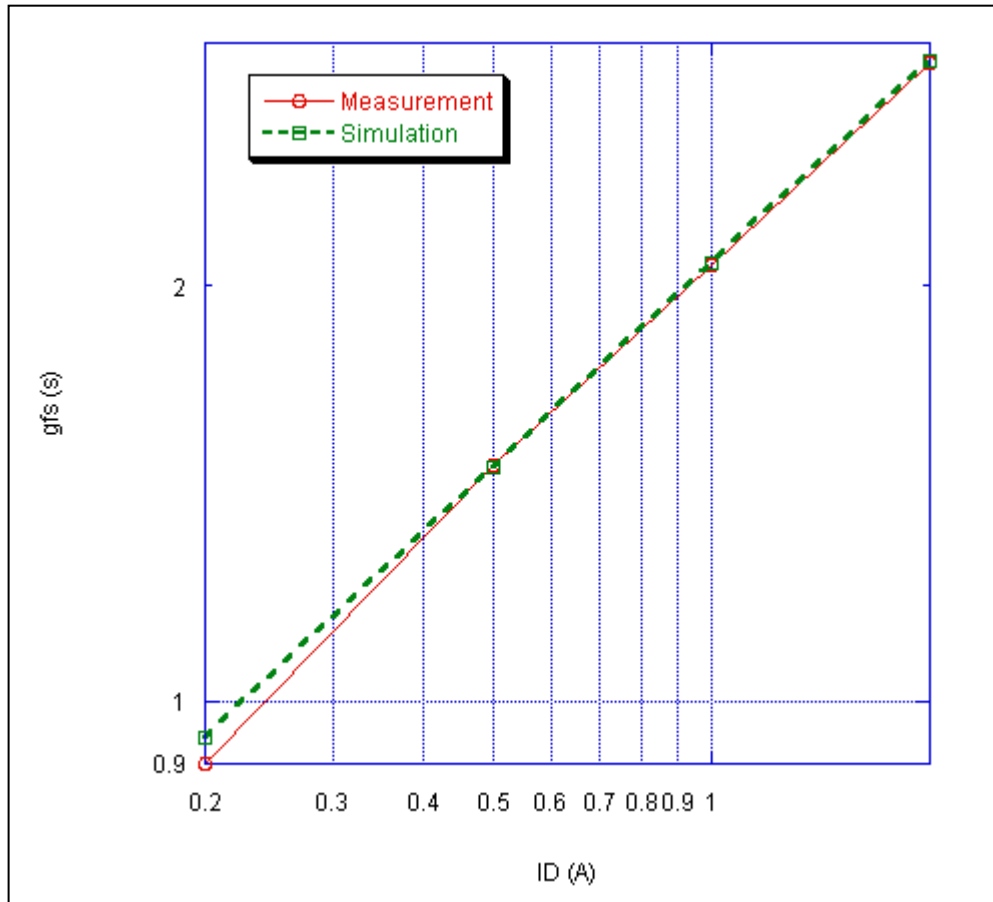
Pspice model parameter	Model description
LEVEL	
L	Channel Length
W	Channel Width
KP	Transconductance
RS	Source Ohmic Resistance
RD	Ohmic Drain Resistance
VTO	Zero-bias Threshold Voltage
RDS	Drain-Source Shunt Resistance
TOX	Gate Oxide Thickness
CGSO	Zero-bias Gate-Source Capacitance
CGDO	Zero-bias Gate-Drain Capacitance
CBD	Zero-bias Bulk-Drain Junction Capacitance
MJ	Bulk Junction Grading Coefficient
PB	Bulk Junction Potential
FC	Bulk Junction Forward-bias Capacitance Coefficient
RG	Gate Ohmic Resistance
IS	Bulk Junction Saturation Current
N	Bulk Junction Emission Coefficient
RB	Bulk Series Resistance
PHI	Surface Inversion Potential
GAMMA	Body-effect Parameter
DELTA	Width effect on Threshold Voltage
ETA	Static Feedback on Threshold Voltage
THETA	Modility Modulation
KAPPA	Saturation Field Factor
VMAX	Maximum Drift Velocity of Carriers
XJ	Metallurgical Junction Depth
UO	Surface Mobility

Body Diode Model

Pspice model parameter	Model description
IS	Saturation Current
N	Emission Coefficient
RS	Series Resistance
IKF	High-injection Knee Current
CJO	Zero-bias Junction Capacitance
M	Junction Grading Coefficient
VJ	Junction Potential
ISR	Recombination Current Saturation Value
BV	Reverse Breakdown Voltage(a positive value)
IBV	Reverse Breakdown Current(a positive value)
TT	Transit Time

Transconductance Characteristic

Circuit Simulation Result

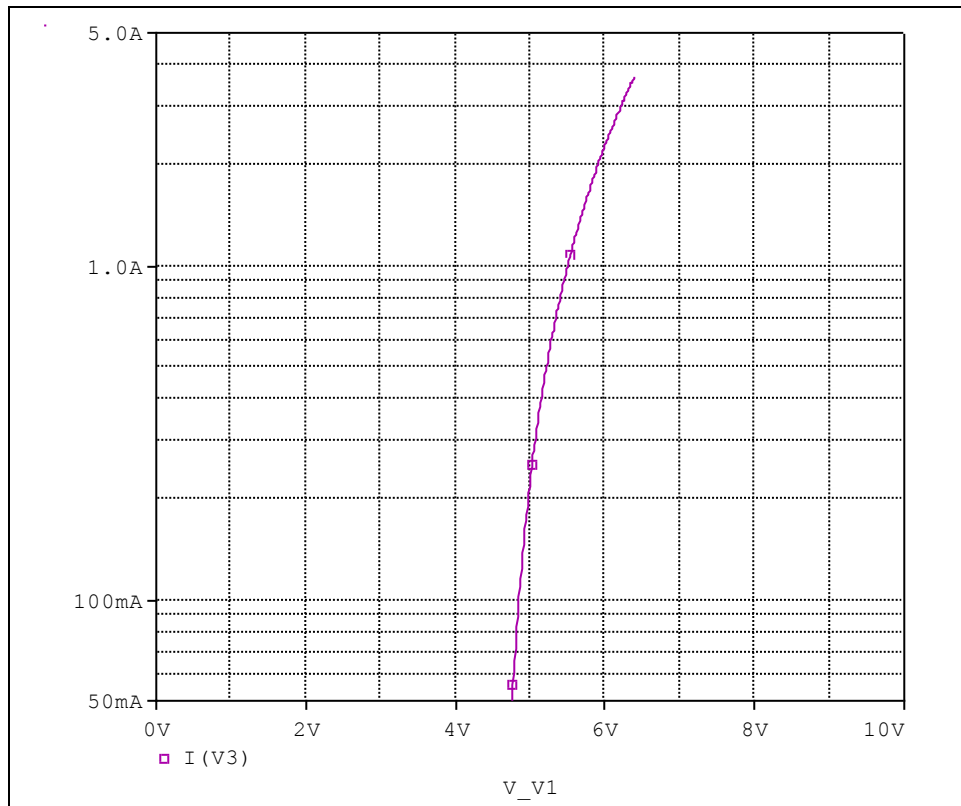


Comparison table

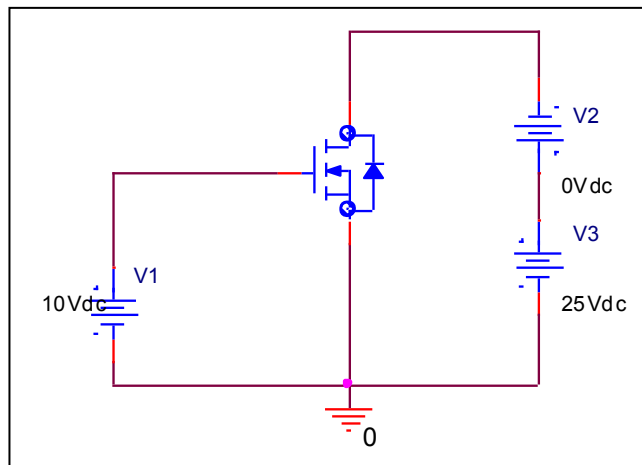
I_D (A)	V_{GS} (V)		Error (%)
	Measurement	Simulation	
0.200	0.900	0.940	4.444
0.500	1.480	1.478	-0.135
1.000	2.070	2.077	0.338
2.000	2.900	2.912	0.414

V_{gs}-I_d Characteristic

Circuit Simulation result

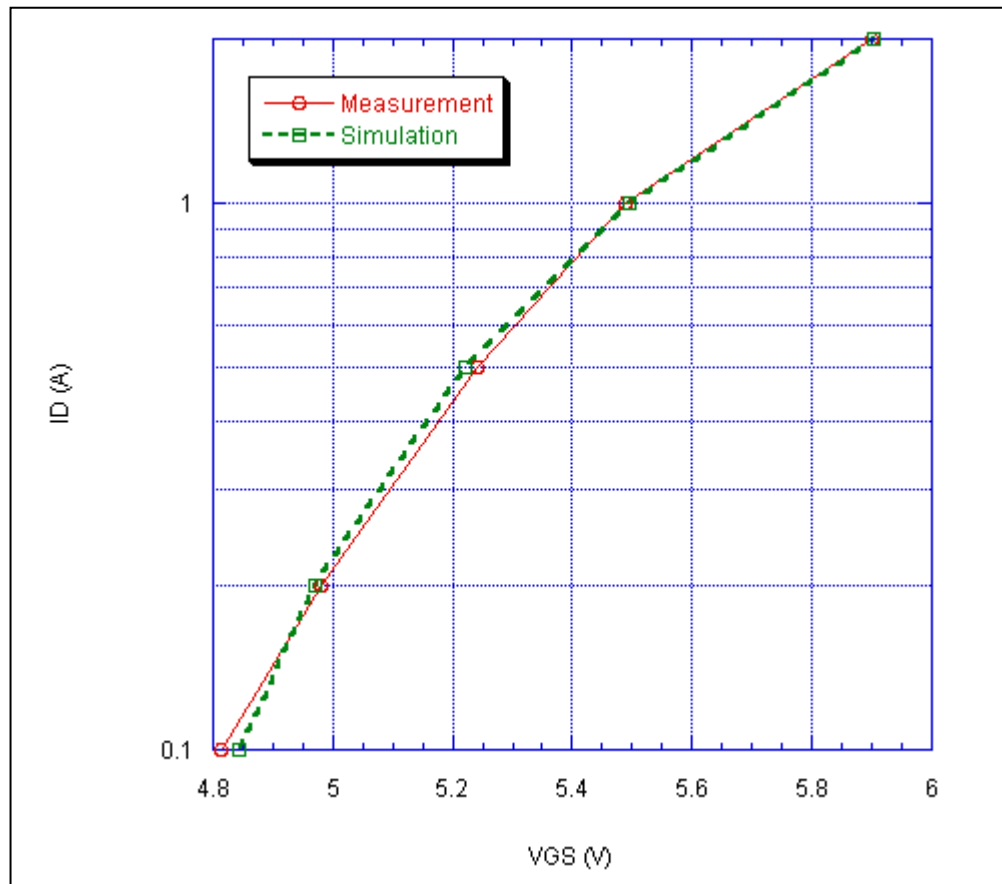


Evaluation circuit



Comparison Graph

Circuit Simulation Result

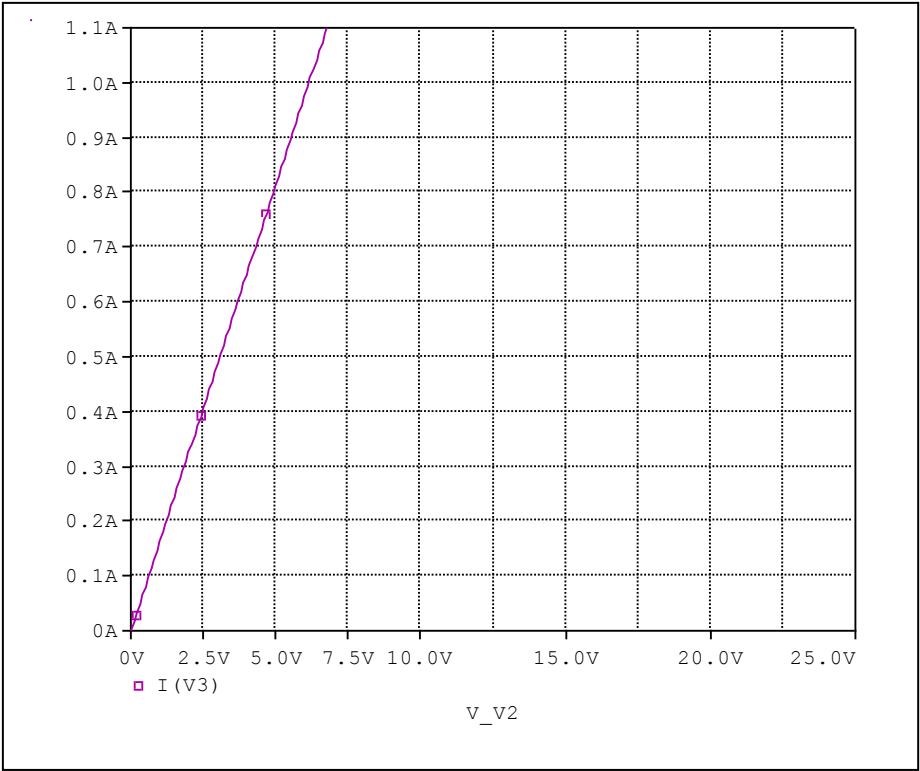


Simulation Result

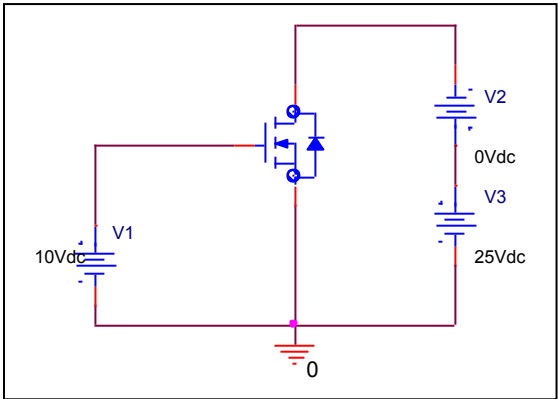
I_D (A)	V_{GS} (V)		Error (%)
	Measurement	Simulation	
0.100	4.815	4.845	0.623
0.200	4.980	4.972	-0.159
0.500	5.240	5.222	-0.351
1.000	5.490	5.495	0.087
2.000	5.900	5.904	0.066

Id-Rds(on) Characteristic

Circuit Simulation result



Evaluation circuit

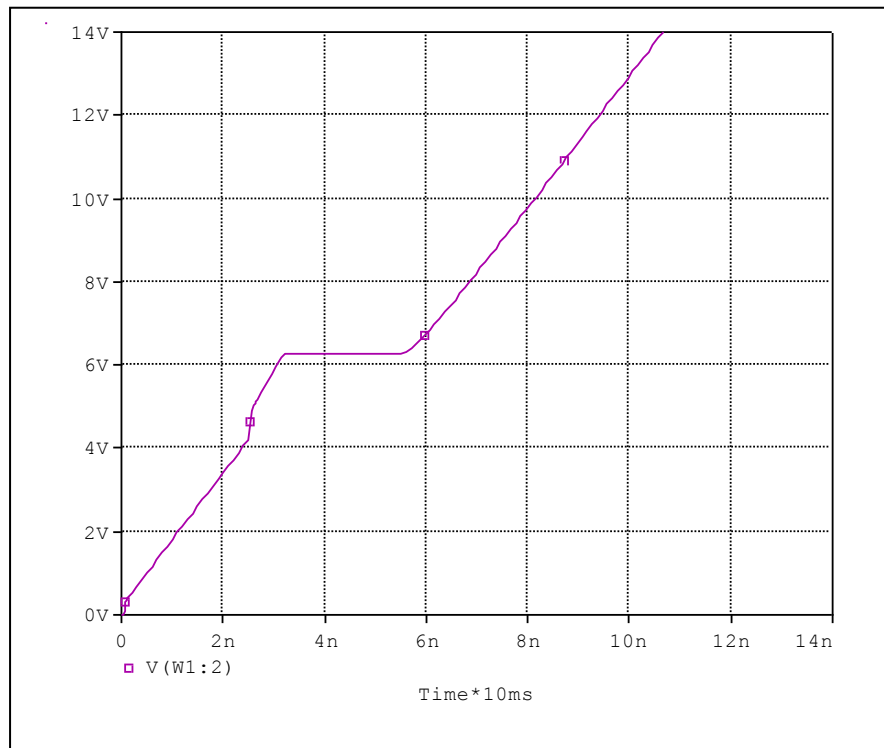


Simulation Result

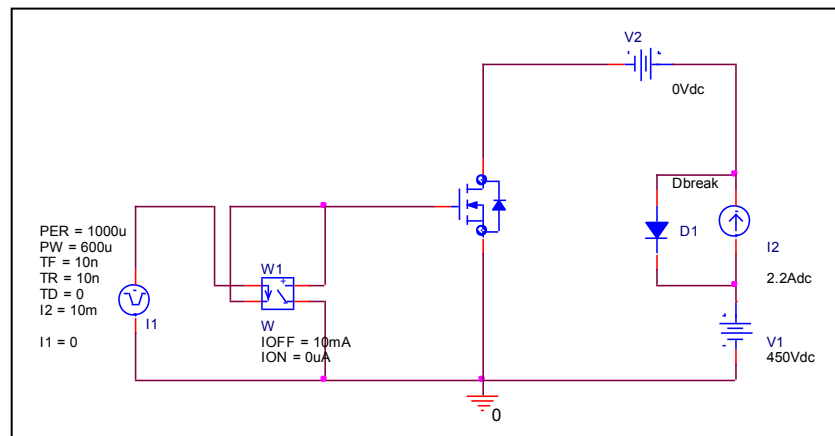
$I_D=1.1, V_{GS}=10V$	Measurement		Simulation		Error (%)
$R_{DS} (on)$	6.15	Ω	6.15	m Ω	0.00

Gate Charge Characteristic

Circuit Simulation result



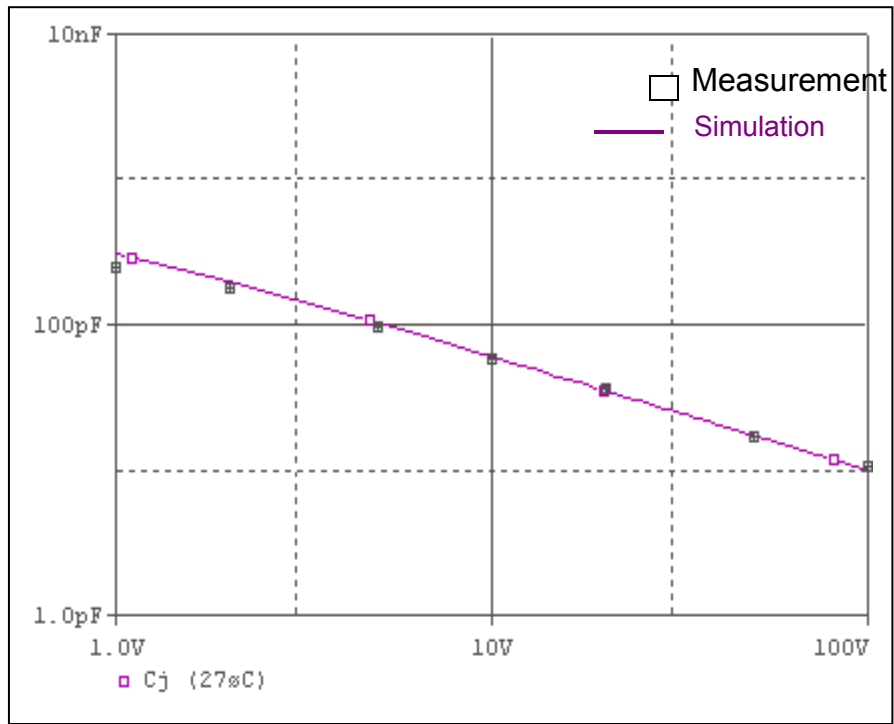
Evaluation circuit



Simulation Result

$V_{DD}=450V$ $I_D=2.2A$	Measurement		Simulation		Error (%)
Qgs	3.40	nC	3.40	nC	0.44
Qgd	2.20	nC	2.21	nC	0.40

Capacitance Characteristic (Vds vs. Cbd)

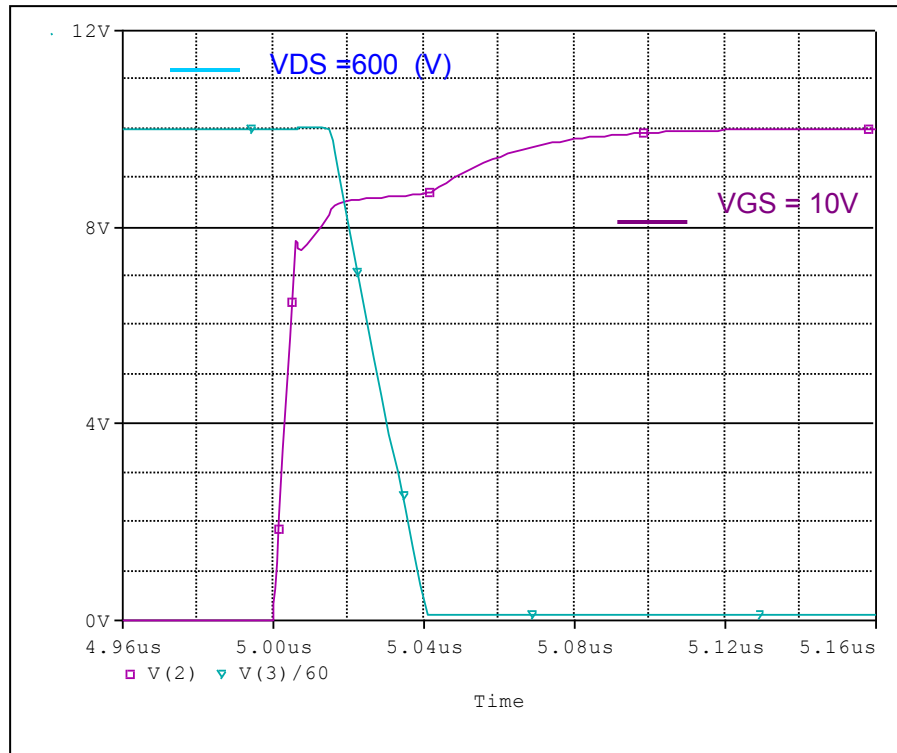


Simulation Result

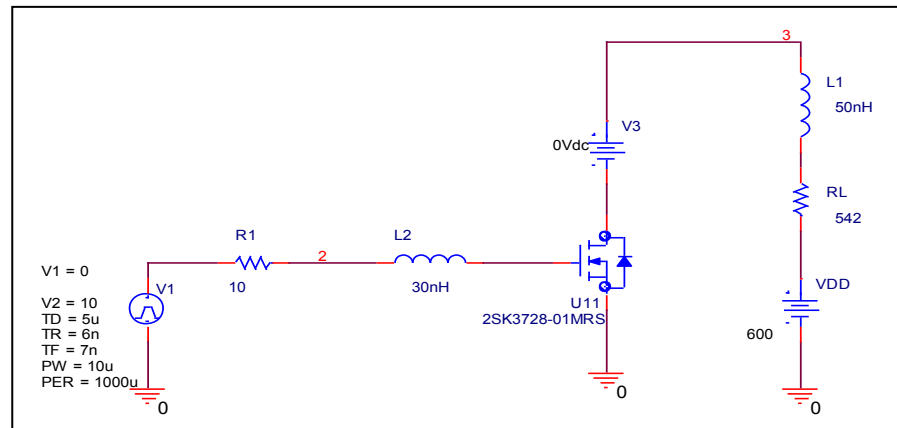
V _{DS} (V)	Cbd(nF)		Error(%)
	Measurement	Simulation	
5.000	100.000	101.250	1.250
10.000	59.500	60.000	0.840
20.000	37.700	35.900	-4.775
50.000	17.700	17.200	-2.825
100.000	10.900	10.370	-4.862

Switching Time Characteristic

Circuit Simulation result



Evaluation circuit

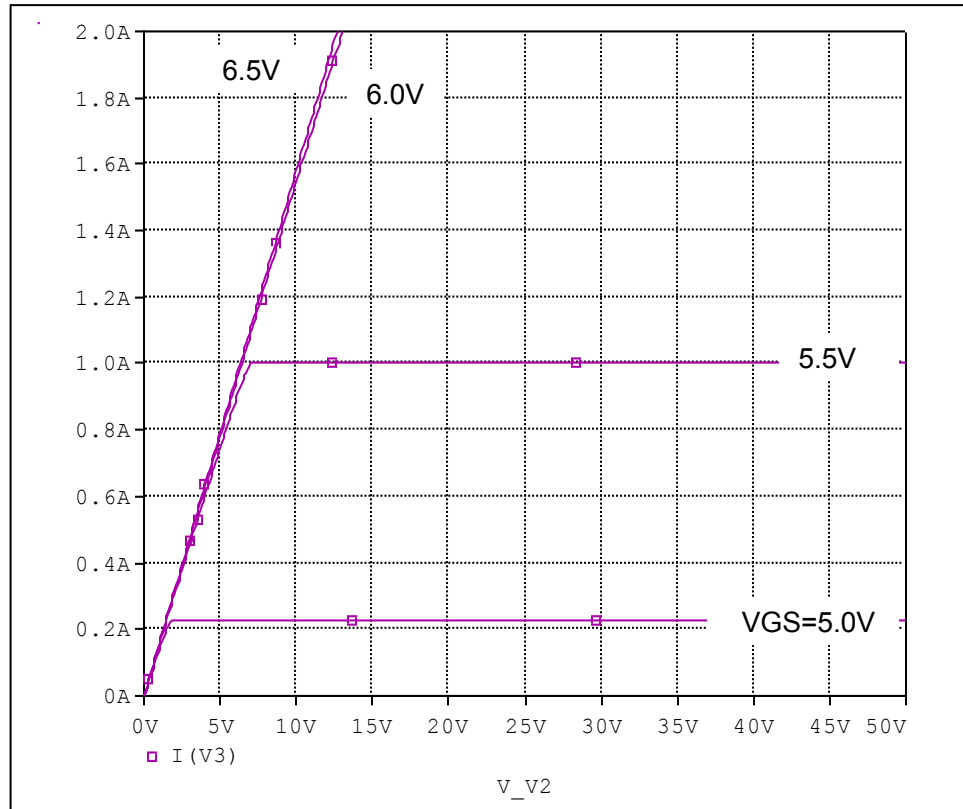


Simulation Result

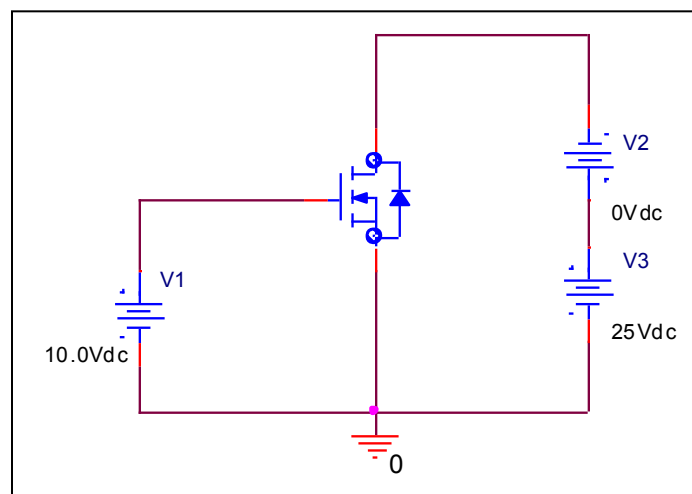
$I_D=1.1A$, $V_{DD}=600V$ $V_{GS}=0/10V$	Measurement		Simulation		Error(%)
t_d (on)	17.00	ns	17.00	ns	0.00

Output Characteristic

Circuit Simulation result

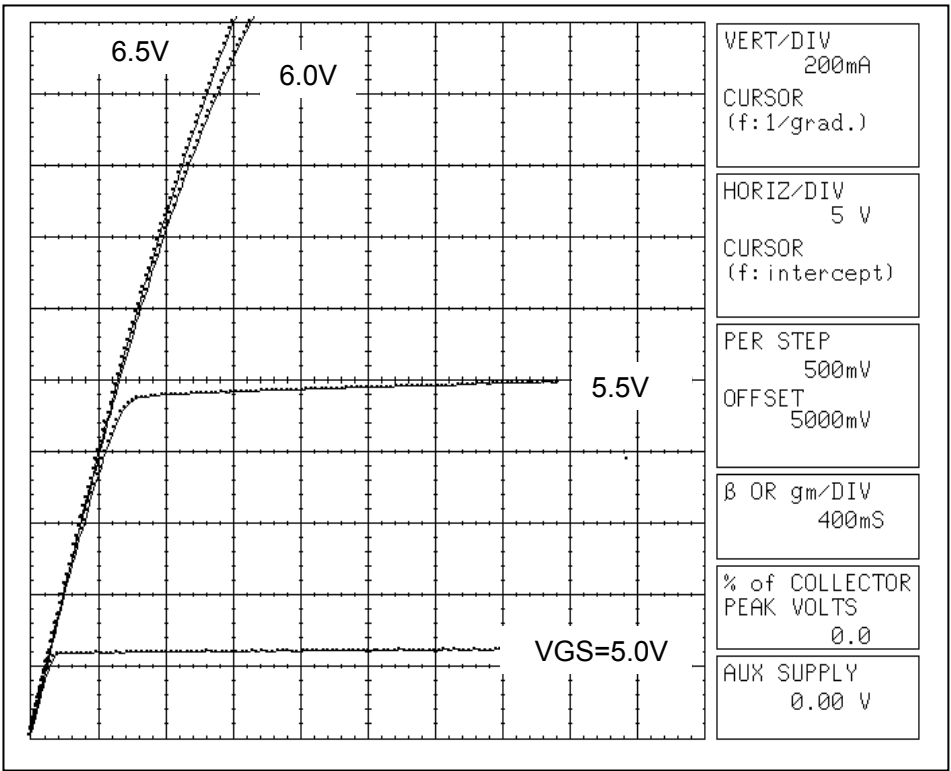


Evaluation circuit



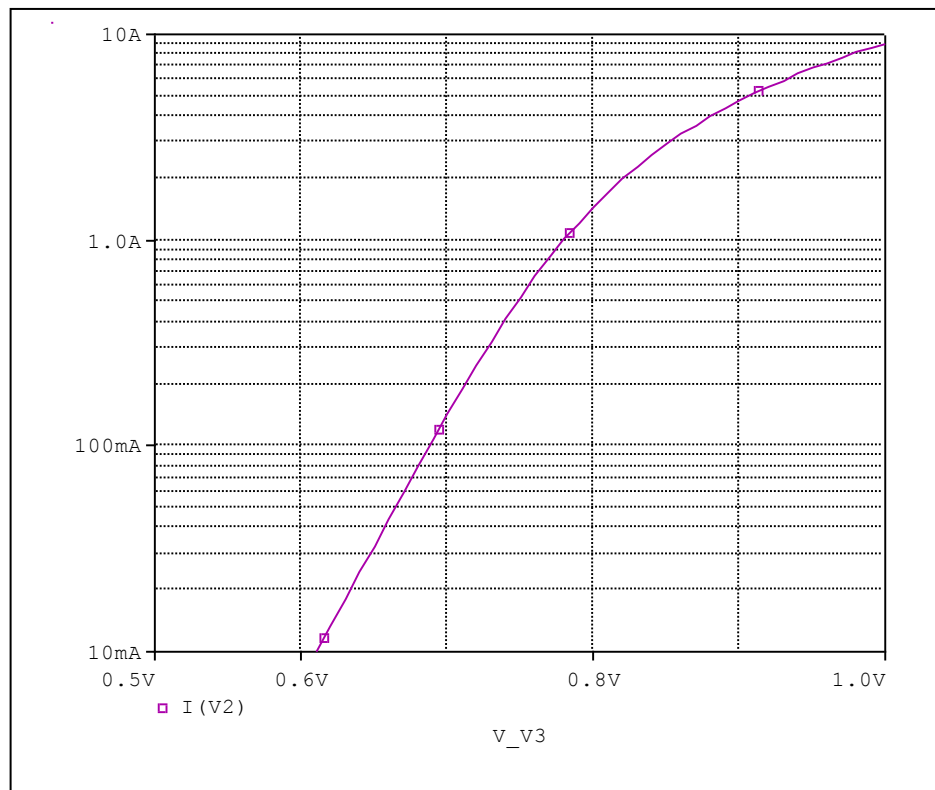
Output Characteristic

Reference

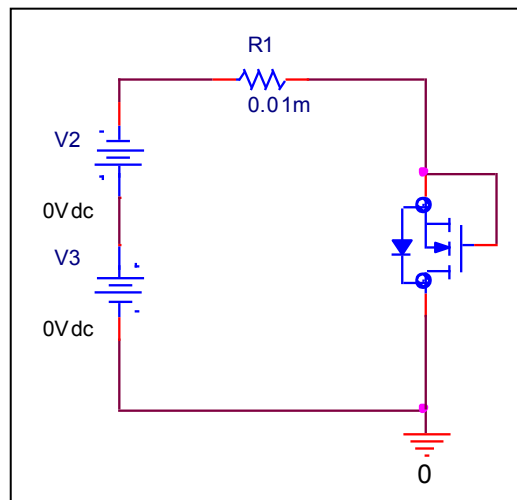


Forward Current Characteristic of Reverse Diode

Circuit Simulation Result

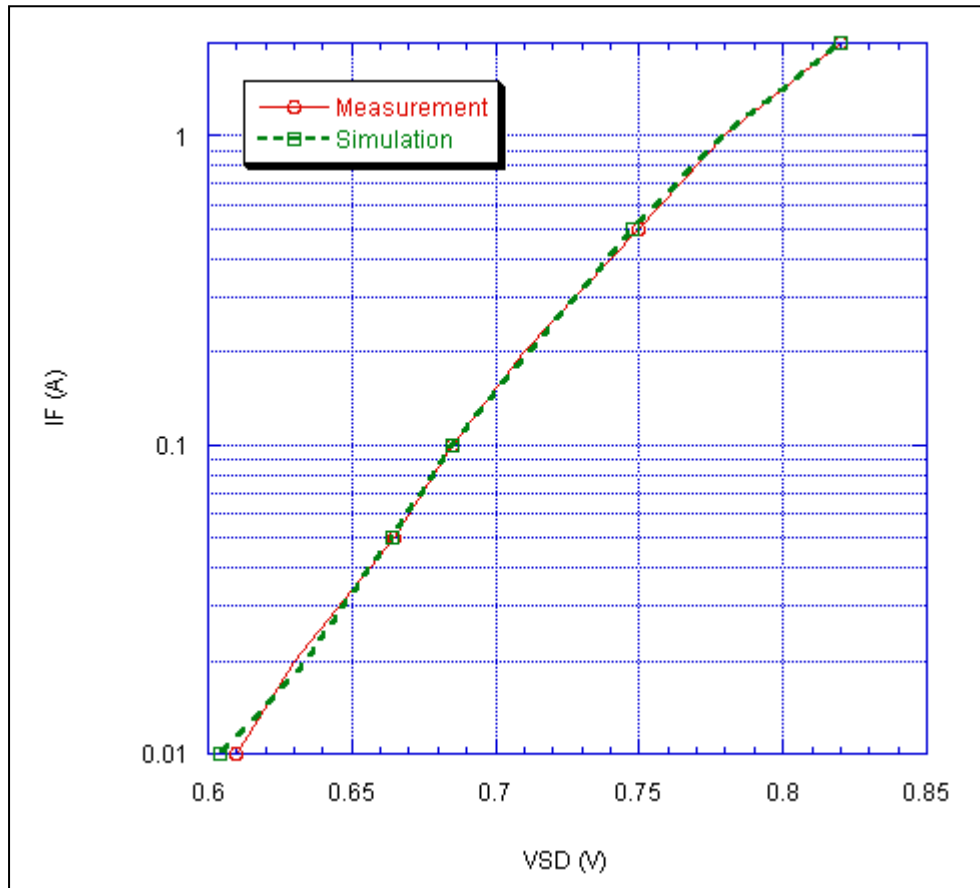


Evaluation Circuit



Comparison Graph

Circuit Simulation Result

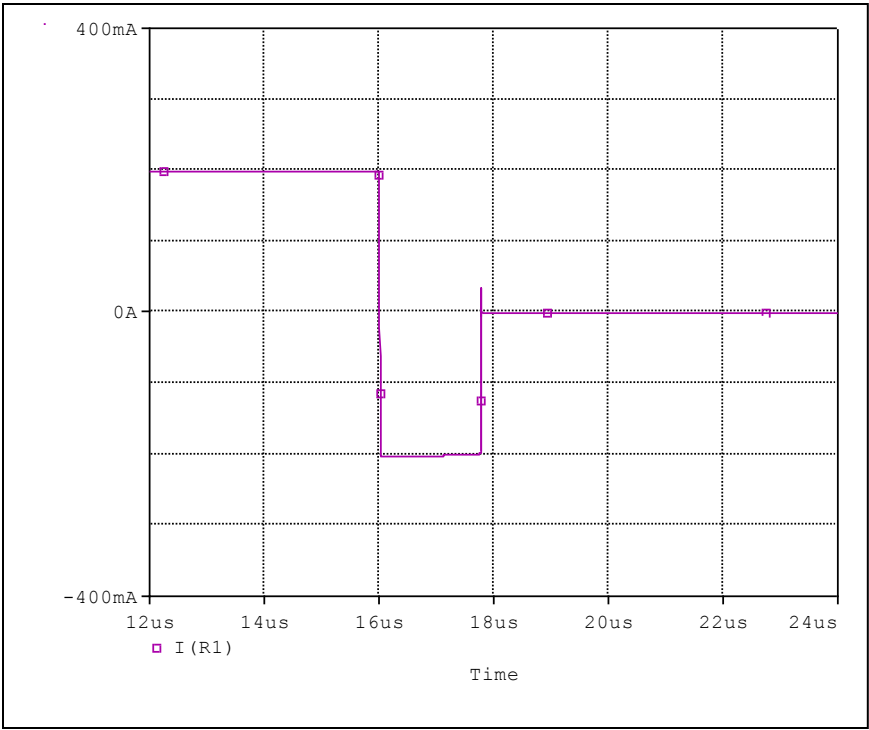


Simulation Result

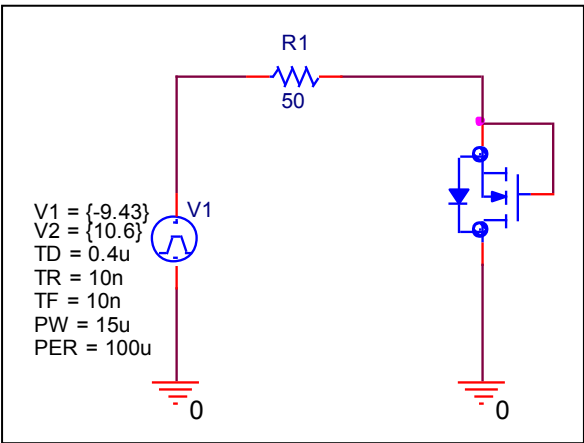
Ifwd(A)	Vfwd(V) Measurement	Vfwd(V) Simulation	%Error
0.010	0.610	0.604	-1.1
0.020	0.630	0.634	0.6
0.050	0.665	0.664	-0.2
0.100	0.685	0.685	0.0
0.200	0.710	0.712	0.3
0.500	0.750	0.748	-0.3
1.000	0.780	0.780	0.0
2.000	0.820	0.820	0.0

Reverse Recovery Characteristic

Circuit Simulation Result



Evaluation Circuit

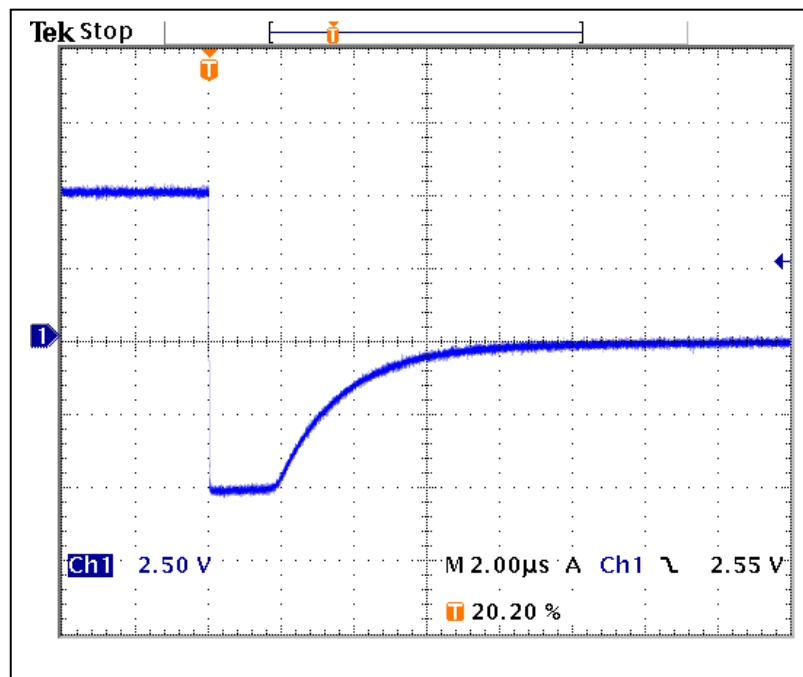


Compare Measurement vs. Simulation

	Measurement		Simulation		Error (%)
trj	1.750	us	1.755	ns	0.286

Reverse Recovery Characteristic

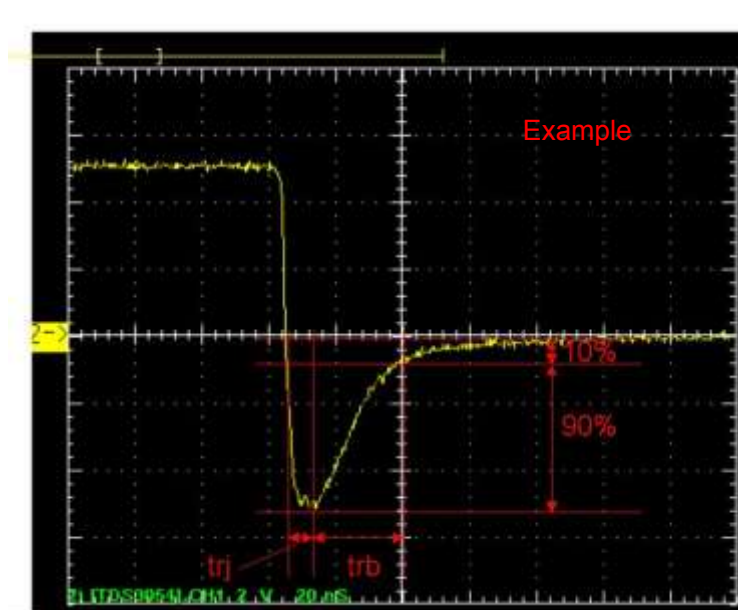
Reference



$T_{rj}=1.75(\mu s)$

$T_{rb}=4.25(\mu s)$

Conditions: $I_{fwd}=I_{rev}=0.2(A)$, $R_I=50$



Relation between t_{rj} and t_{rb}