

Device Modeling Report

COMPONENTS: Power MOSFET (Model parameters)
PART NUMBER: 2SJ493
MANUFACTURER: NEC
REMARK: P Channel Model
Body Diode (Model parameters) / ESD Protection Diode



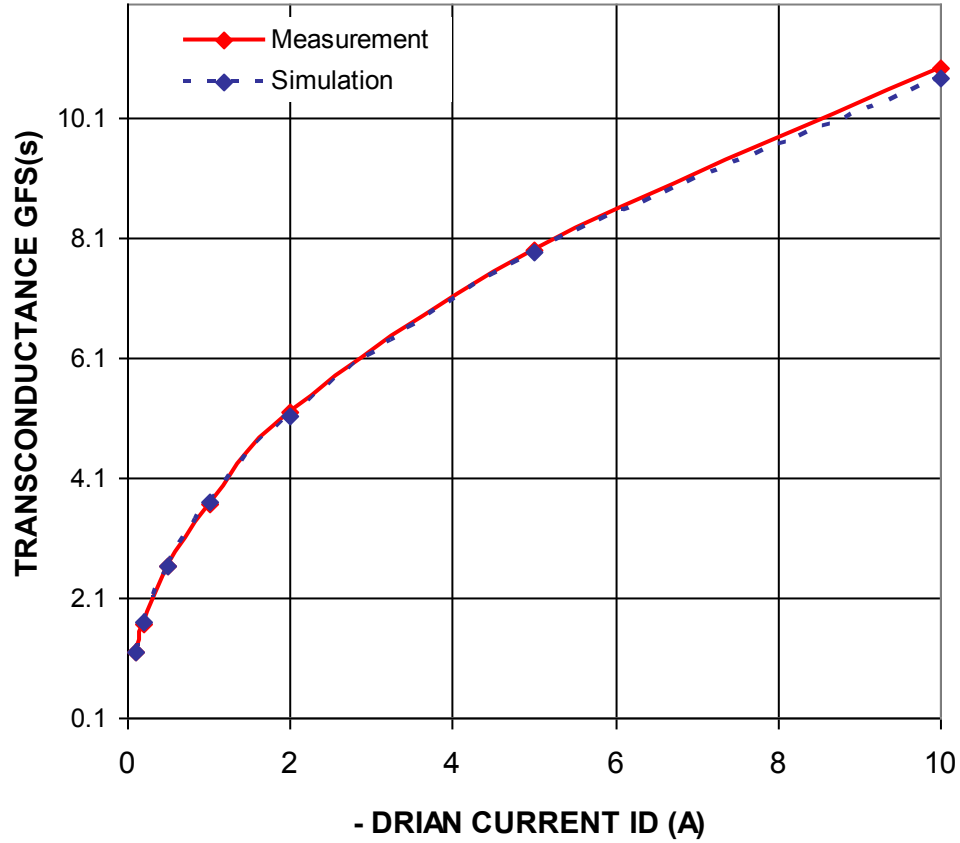
Bee Technologies Inc.

MOSFET MODEL

Pspice model parameter	Model description
LEVEL	
L	Channel Length
W	Channel Width
KP	Transconductance
RS	Source Ohmic Resistance
RD	Ohmic Drain Resistance
VTO	Zero-bias Threshold Voltage
RDS	Drain-Source Shunt Resistance
TOX	Gate Oxide Thickness
CGSO	Zero-bias Gate-Source Capacitance
CGDO	Zero-bias Gate-Drain Capacitance
CBD	Zero-bias Bulk-Drain Junction Capacitance
MJ	Bulk Junction Grading Coefficient
PB	Bulk Junction Potential
FC	Bulk Junction Forward-bias Capacitance Coefficient
RG	Gate Ohmic Resistance
IS	Bulk Junction Saturation Current
N	Bulk Junction Emission Coefficient
RB	Bulk Series Resistance
PHI	Surface Inversion Potential
GAMMA	Body-effect Parameter
DELTA	Width effect on Threshold Voltage
ETA	Static Feedback on Threshold Voltage
THETA	Modility Modulation
KAPPA	Saturation Field Factor
VMAX	Maximum Drift Velocity of Carriers
XJ	Metallurgical Junction Depth
UO	Surface Mobility

Transconductance Characteristic

Circuit Simulation Result

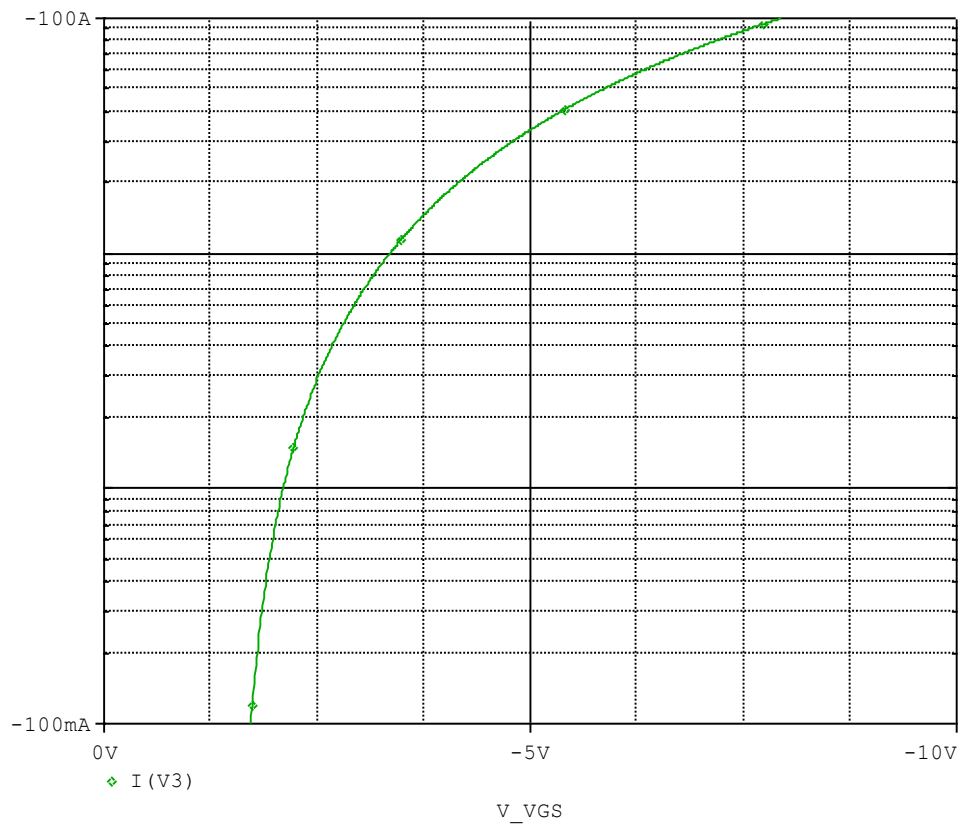


Comparison table

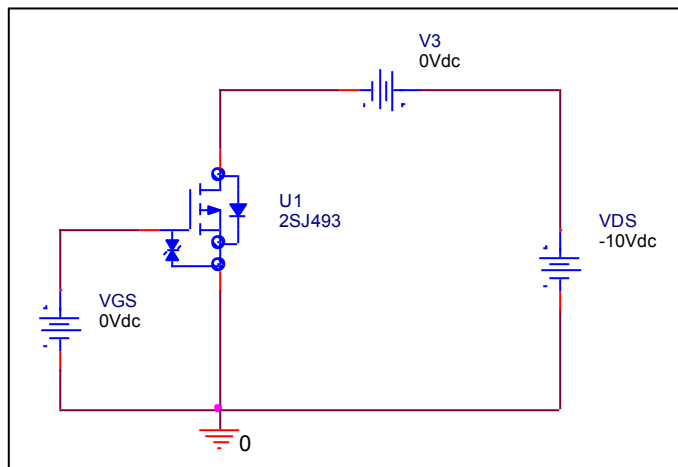
Id(A)	gfs		Error(%)
	Measurement	Simulation	
0.1	1.200	1.190	-0.794
0.2	1.681	1.689	0.484
0.5	2.632	2.650	0.700
1	3.676	3.689	0.335
2	5.210	5.128	-1.570
5	7.900	7.862	-0.486
10	10.922	10.753	-1.550

V_{gs}-I_d Characteristic

Circuit Simulation result

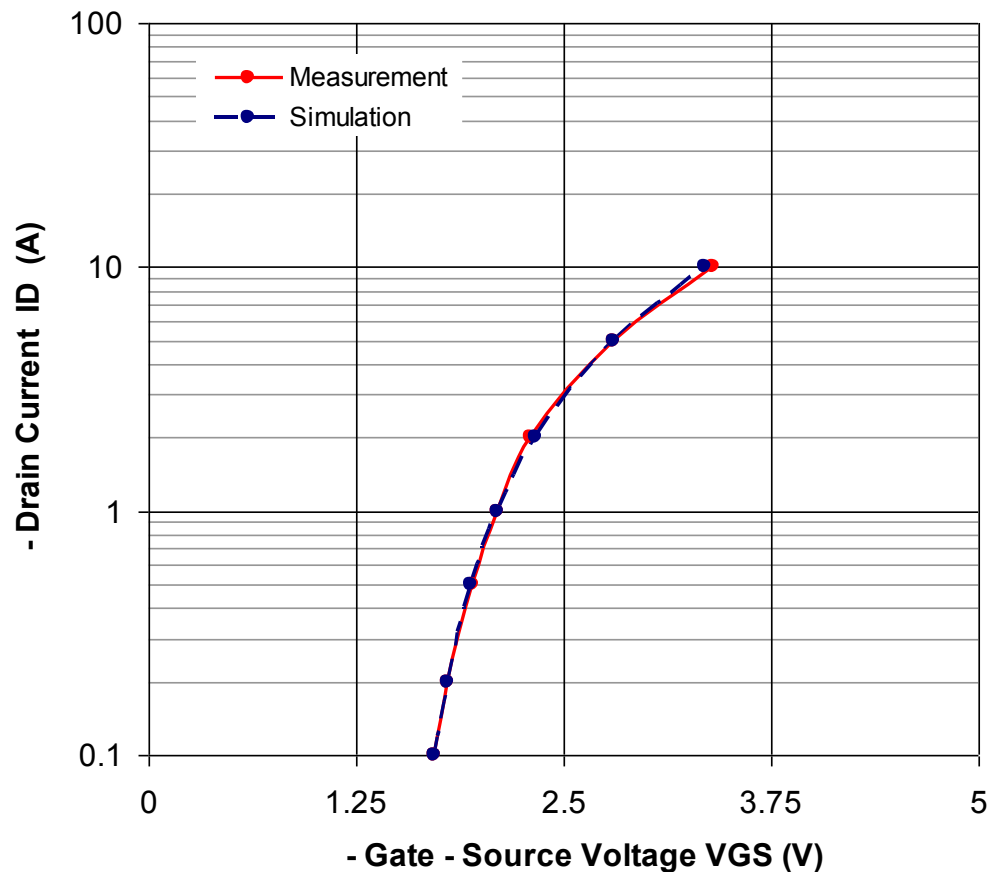


Evaluation circuit



Comparison Graph

Circuit Simulation Result

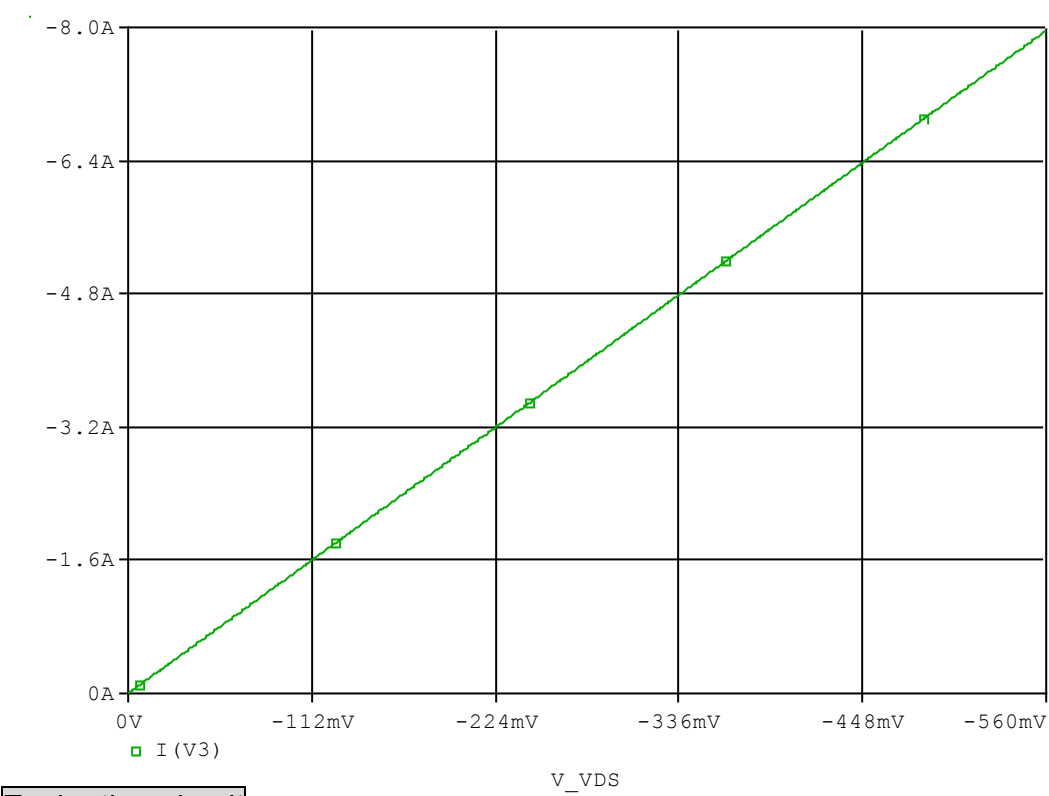


Simulation Result

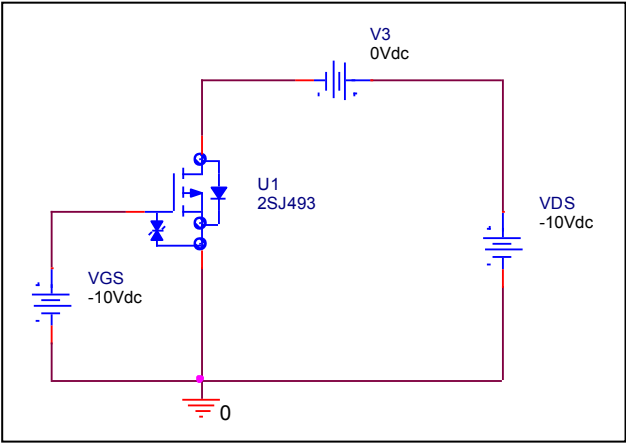
ID(A)	VGS(V)		Error (%)
	Measurement	Simulation	
-0.1	1.720	1.724	0.221
-0.2	1.800	1.795	-0.272
-0.5	1.950	1.938	-0.641
-1	2.100	2.100	-0.019
-2	2.300	2.332	1.387
-5	2.800	2.803	0.114
-10	3.400	3.351	-1.447

Rds(on) Characteristic

Circuit Simulation result



Evaluation circuit

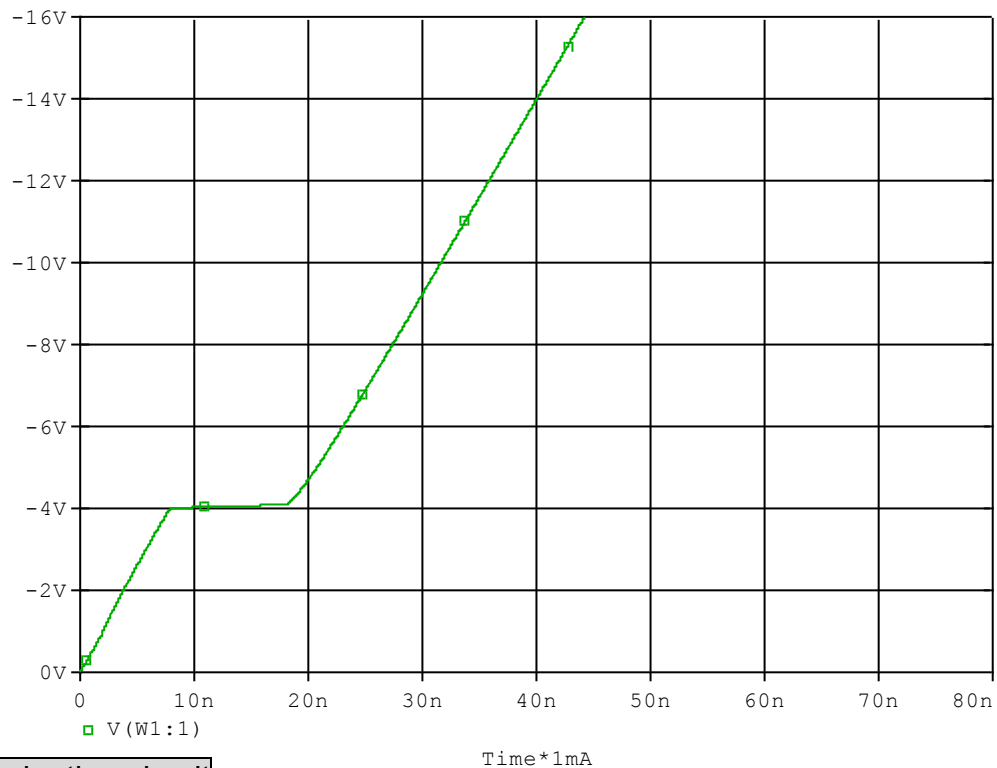


Simulation Result

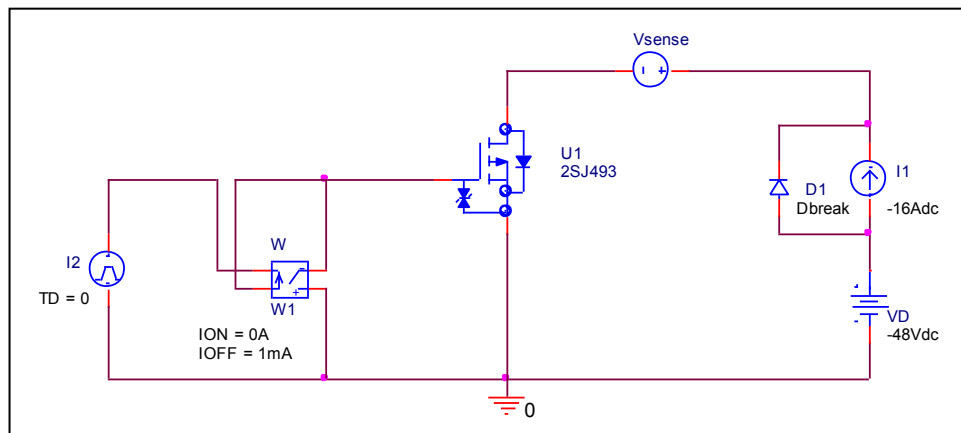
$I_D = -8A, V_{GS} = -10V$		Measurement	Simulation	Error (%)
$R_{DS(on)}$	mΩ	70.00	70.00	0.00

Gate Charge Characteristic

Circuit Simulation result



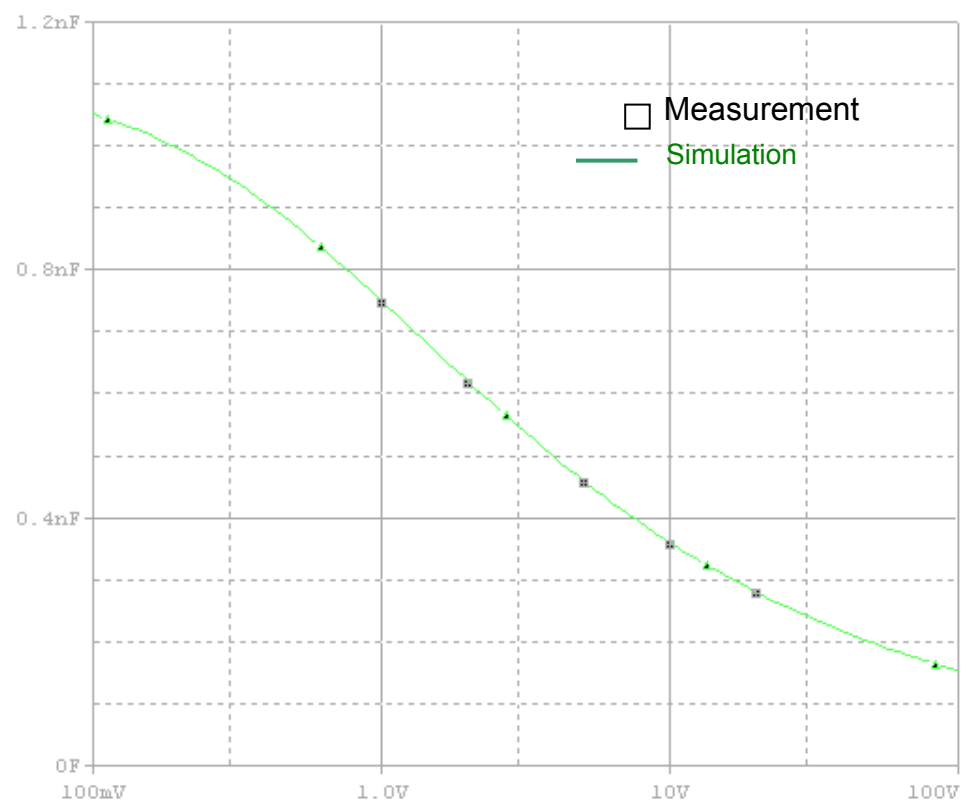
Evaluation circuit



Simulation Result

$V_{DD} = -48V, I_D = -16A$ $V_{GS} = -10V$		Measurement	Simulation	Error (%)
Qgs	nc	8.00	8.06	0.71
Qgd	nc	10.00	10.07	0.70
Qg	nc	42.00	31.59	-24.79

Capacitance Characteristic

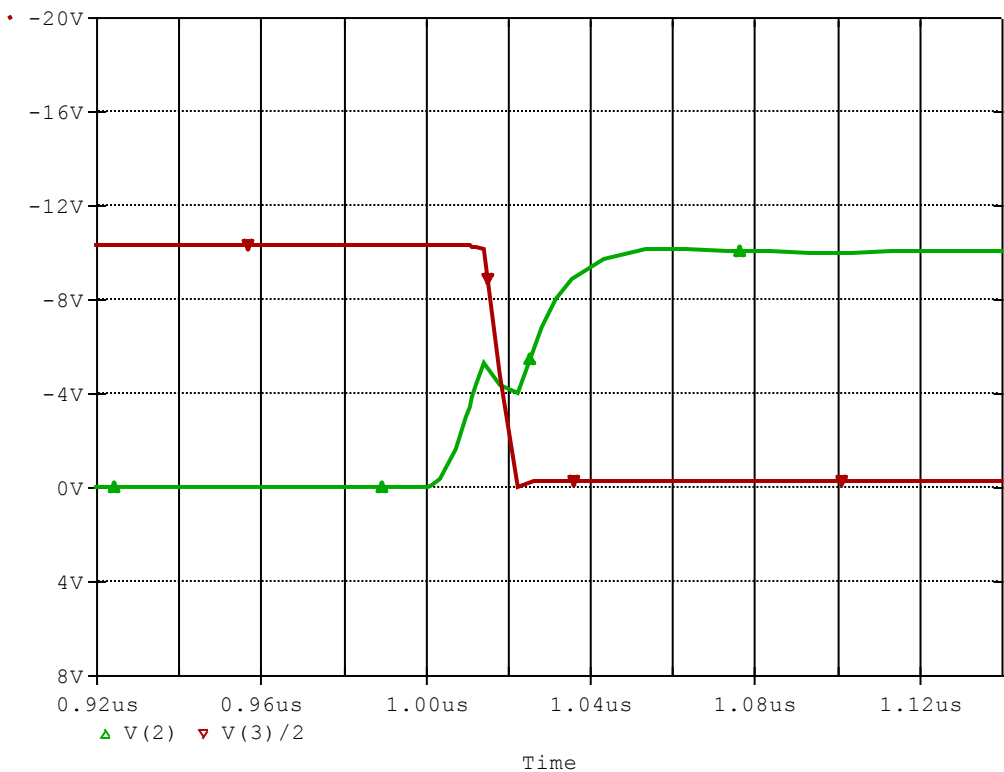


Simulation Result

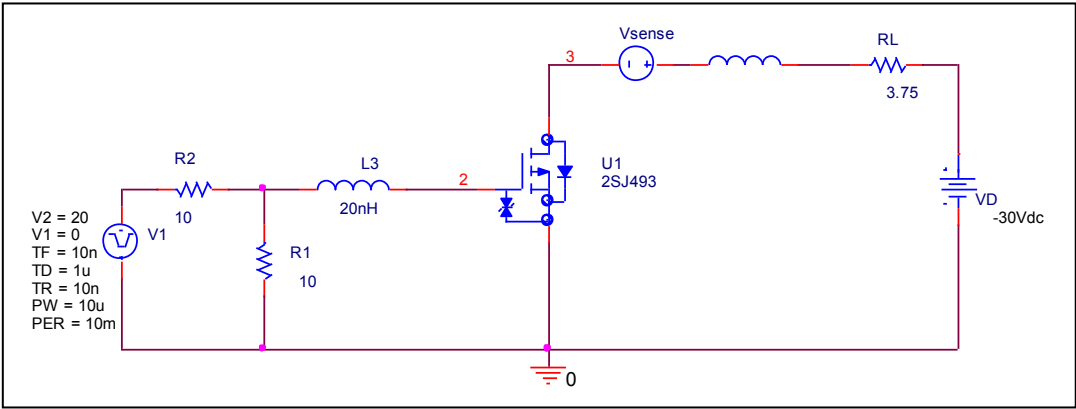
V _{DS} (V)	Cbd(pF)		Error(%)
	Measurement	Simulation	
-1	750.00	749.24	-0.10
-2	620.00	621.55	0.25
-5	460.00	455.88	-0.90
-10	360.00	358.54	-0.41
-20	280.00	275.44	-1.63

Switching Time Characteristic

Circuit Simulation result



Evaluation circuit

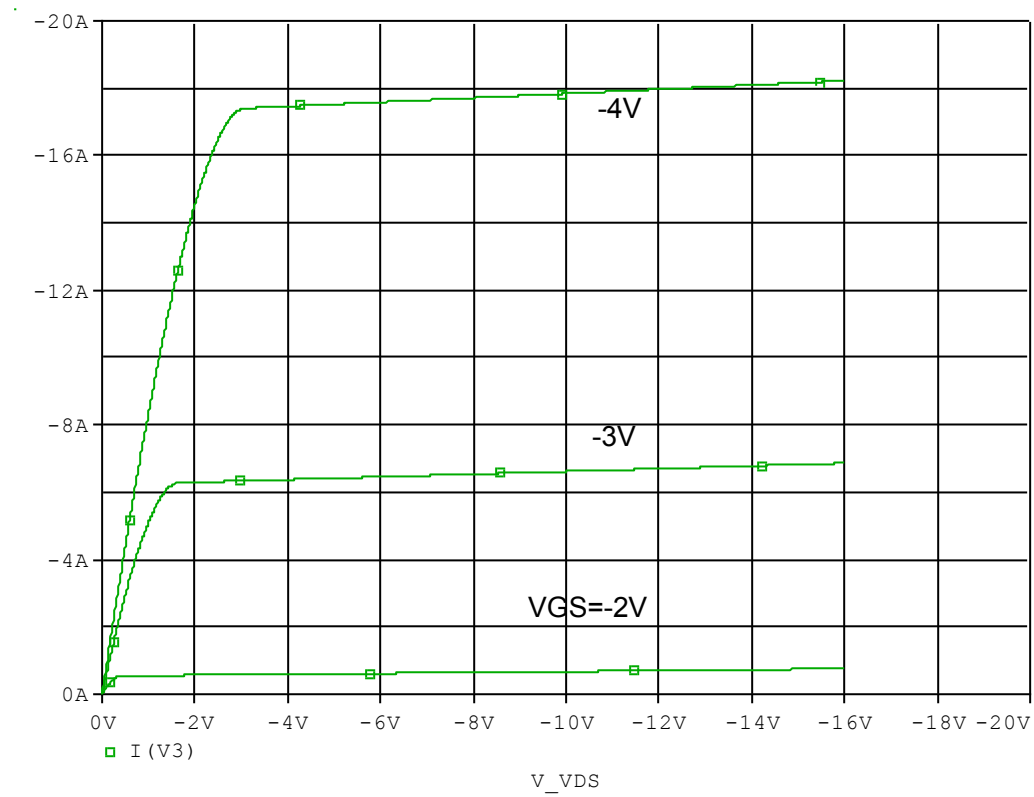


Simulation Result

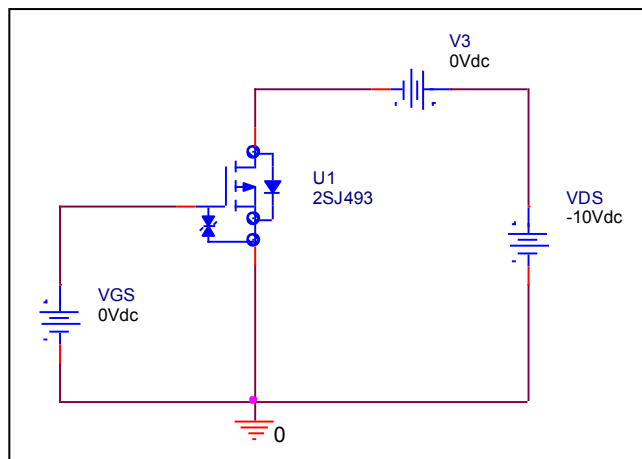
$I_D = -8A$, $V_{DD} = -30V$ $V_{GS} = 0/-10V$		Measurement	Simulation	Error(%)
td (on)	ns	15.00	15.045	0.30

Output Characteristic

Circuit Simulation result

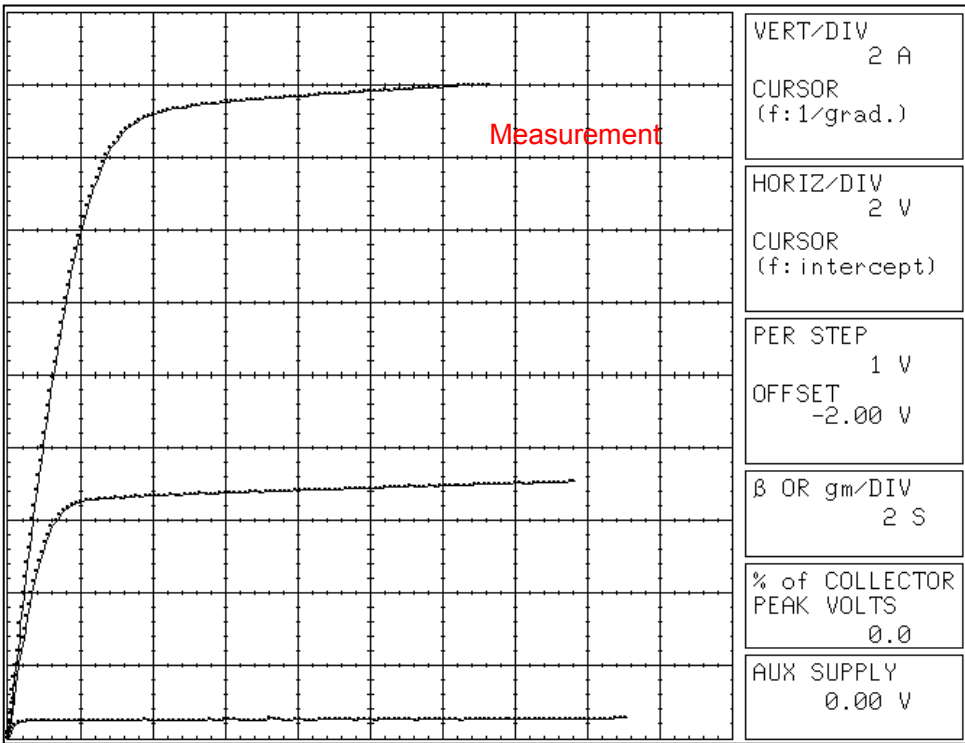


Evaluation circuit



Output Characteristic

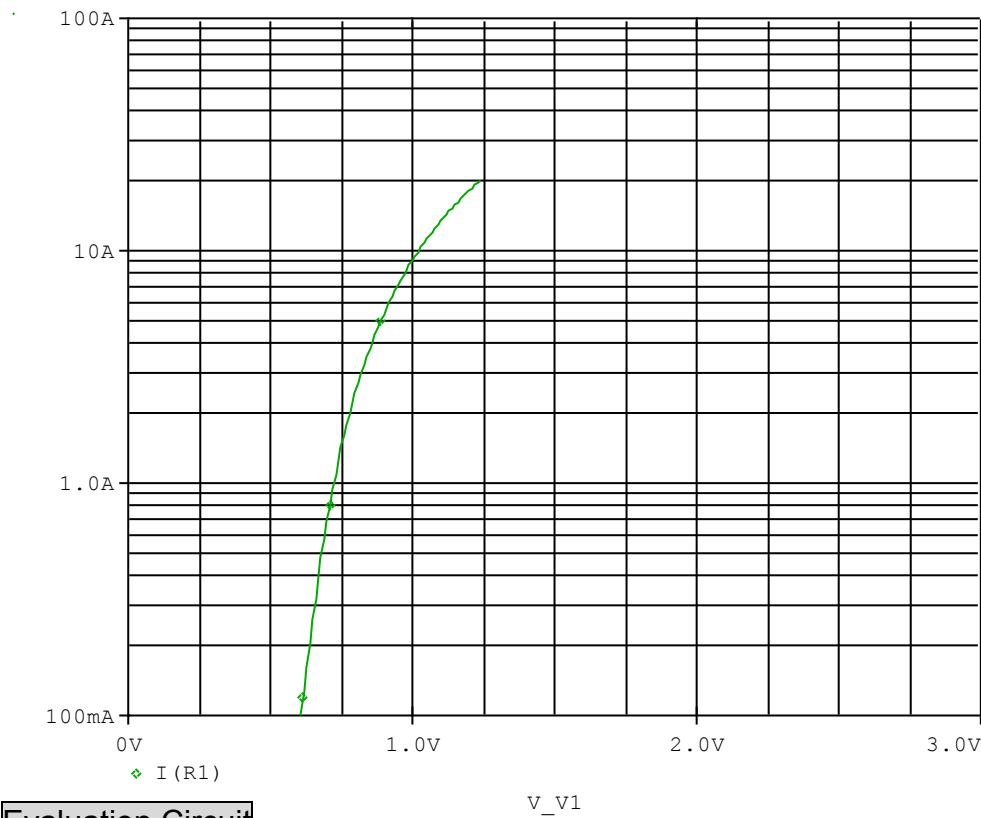
Reference



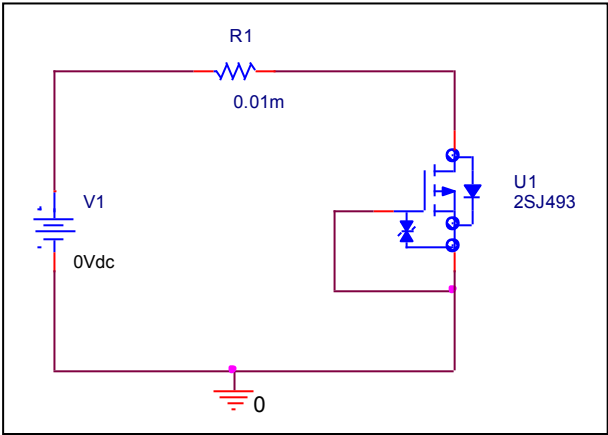
BODY DIODE

Forward Current Characteristic

Circuit Simulation Result

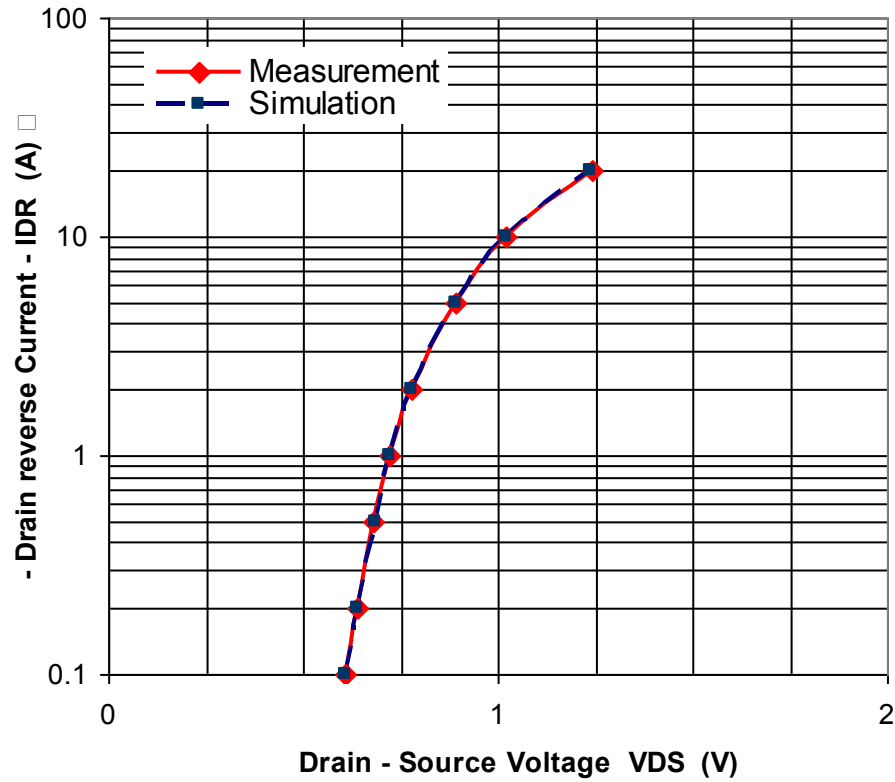


Evaluation Circuit



Comparison Graph

Circuit Simulation Result

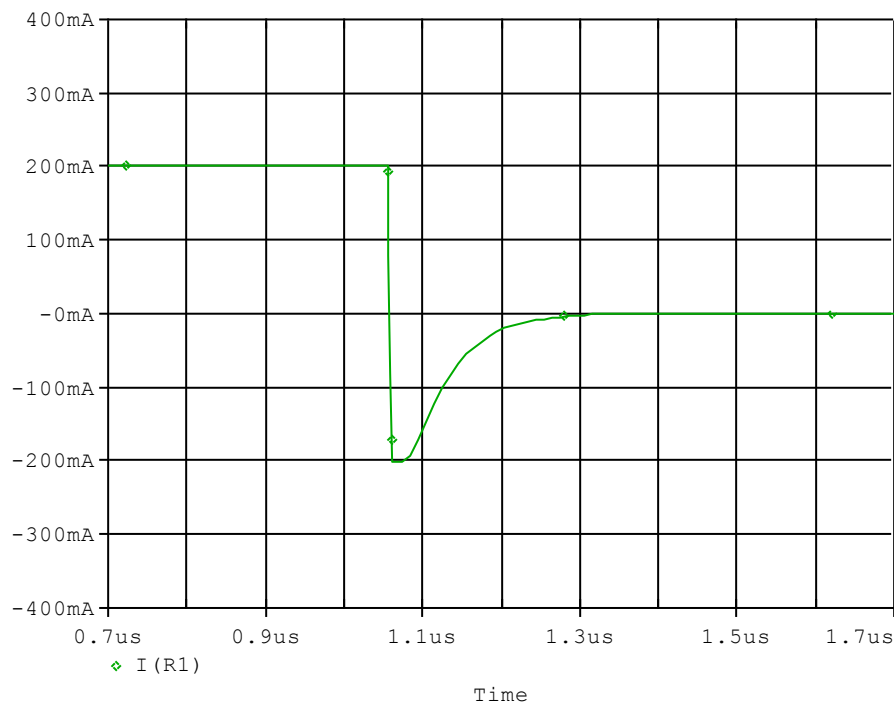


Simulation Result

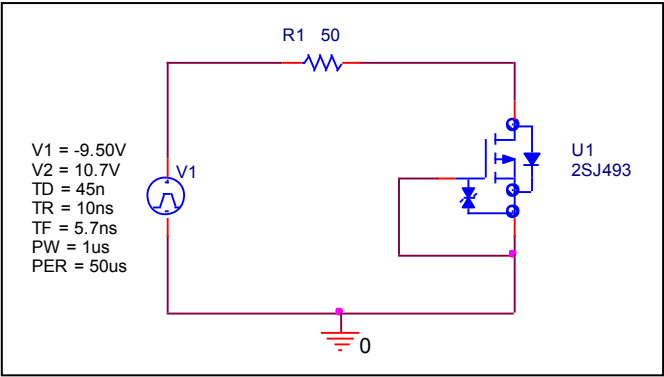
IDR(A)	VDS(V)		%Error
	Measurement	Simulation	
-0.1	0.610	0.609	-0.131
-0.2	0.640	0.638	-0.344
-0.5	0.680	0.683	0.441
-1	0.720	0.723	0.417
-2	0.780	0.779	-0.141
-5	0.890	0.890	-0.056
-10	1.020	1.022	0.176
-20	1.240	1.238	-0.194

Reverse Recovery Characteristic

Circuit Simulation Result



Evaluation Circuit

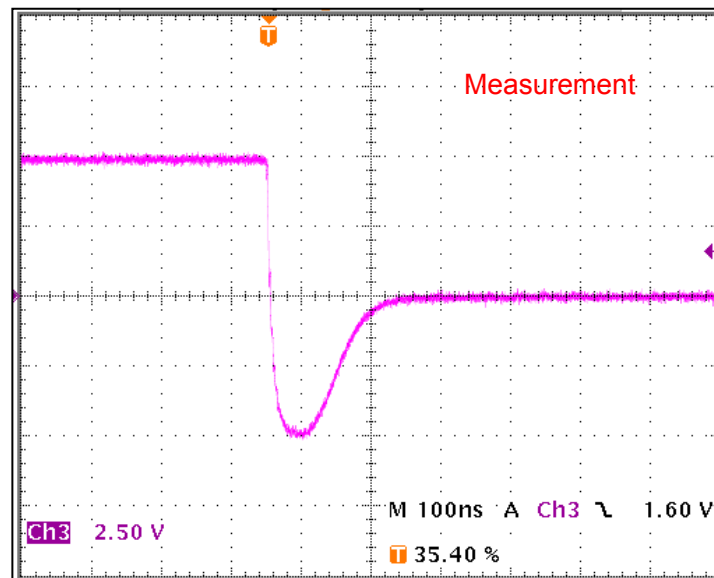


Compare Measurement vs. Simulation

		Measurement	Simulation	Error (%)
trr	ns	146.00	145.72	-0.19

Reverse Recovery Characteristic

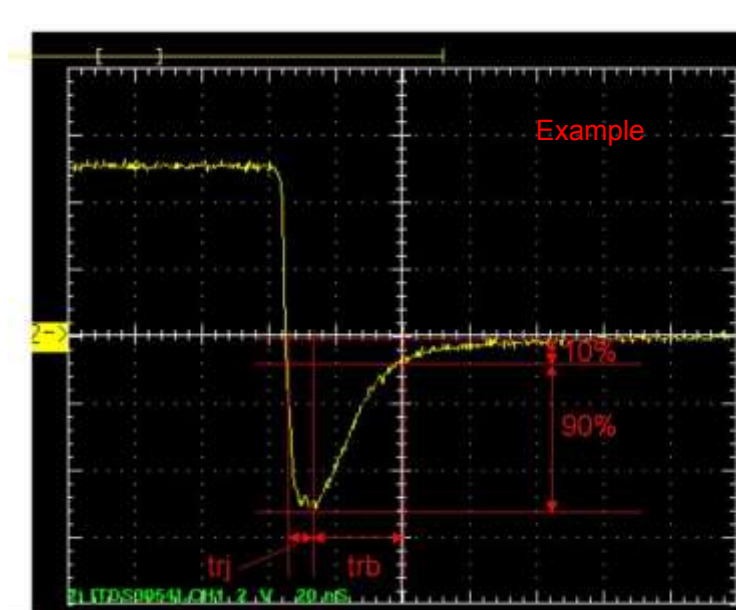
Reference



$T_{rr}=64.00(\text{ns})$

$T_{rb}=82.00(\text{ns})$

Conditions: $I_{fwd}=I_{rev}=0.2(\text{A})$, $R_I=50$

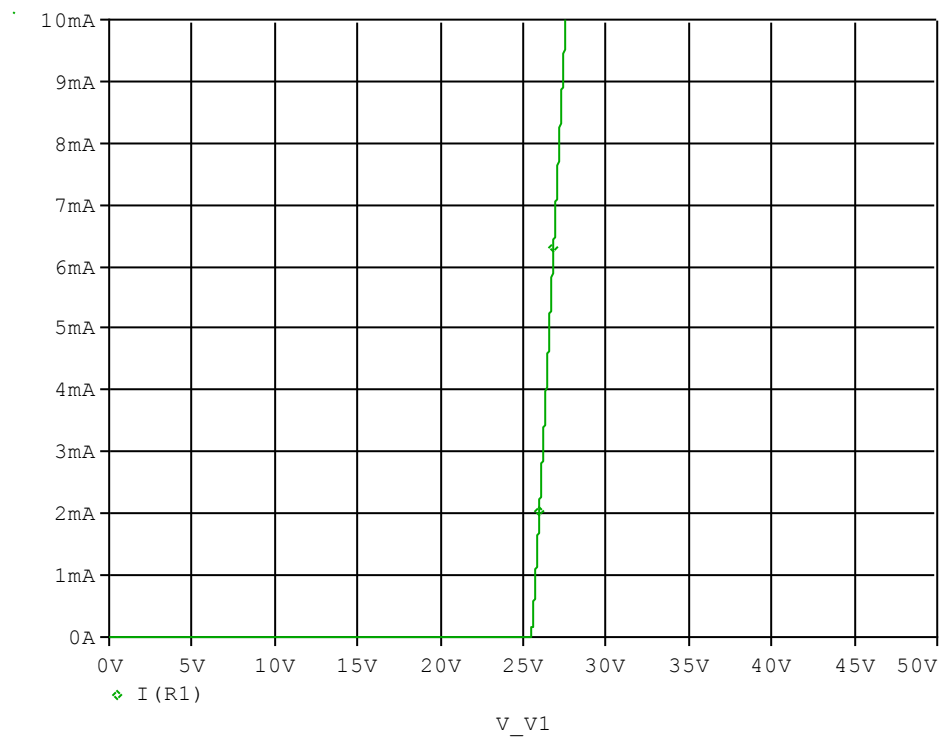


Relation between t_{rr} and t_{rb}

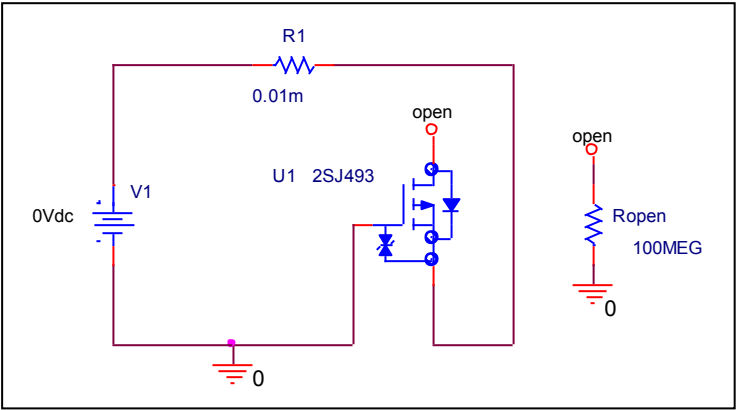
ESD PROTECTION DIODE

Zener Voltage Characteristic

Circuit Simulation Result



Evaluation Circuit



Zener Voltage Characteristic

Reference

