

Device Modeling Report

COMPONENTS: Power MOSFET (Professional)
PART NUMBER: 2SJ495
MANUFACTURER: NEC
Body Diode (Professional) / ESD Protection Diode



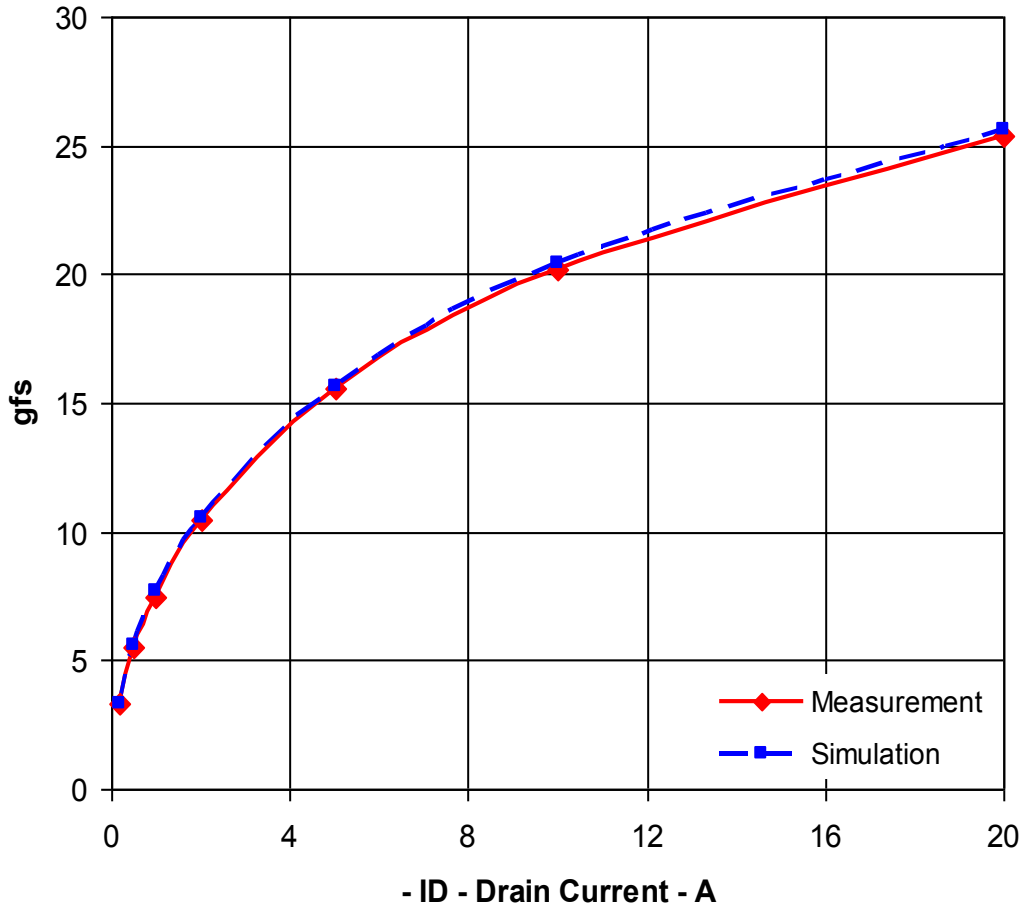
Bee Technologies Inc.

MOSFET MODEL

Pspice model parameter	Model description
LEVEL	
L	Channel Length
W	Channel Width
KP	Transconductance
RS	Source Ohmic Resistance
RD	Ohmic Drain Resistance
VTO	Zero-bias Threshold Voltage
RDS	Drain-Source Shunt Resistance
TOX	Gate Oxide Thickness
CGSO	Zero-bias Gate-Source Capacitance
CGDO	Zero-bias Gate-Drain Capacitance
CBD	Zero-bias Bulk-Drain Junction Capacitance
MJ	Bulk Junction Grading Coefficient
PB	Bulk Junction Potential
FC	Bulk Junction Forward-bias Capacitance Coefficient
RG	Gate Ohmic Resistance
IS	Bulk Junction Saturation Current
N	Bulk Junction Emission Coefficient
RB	Bulk Series Resistance
PHI	Surface Inversion Potential
GAMMA	Body-effect Parameter
DELTA	Width effect on Threshold Voltage
ETA	Static Feedback on Threshold Voltage
THETA	Modility Modulation
KAPPA	Saturation Field Factor
VMAX	Maximum Drift Velocity of Carriers
XJ	Metallurgical Junction Depth
UO	Surface Mobility

Transconductance Characteristic

Circuit Simulation Result

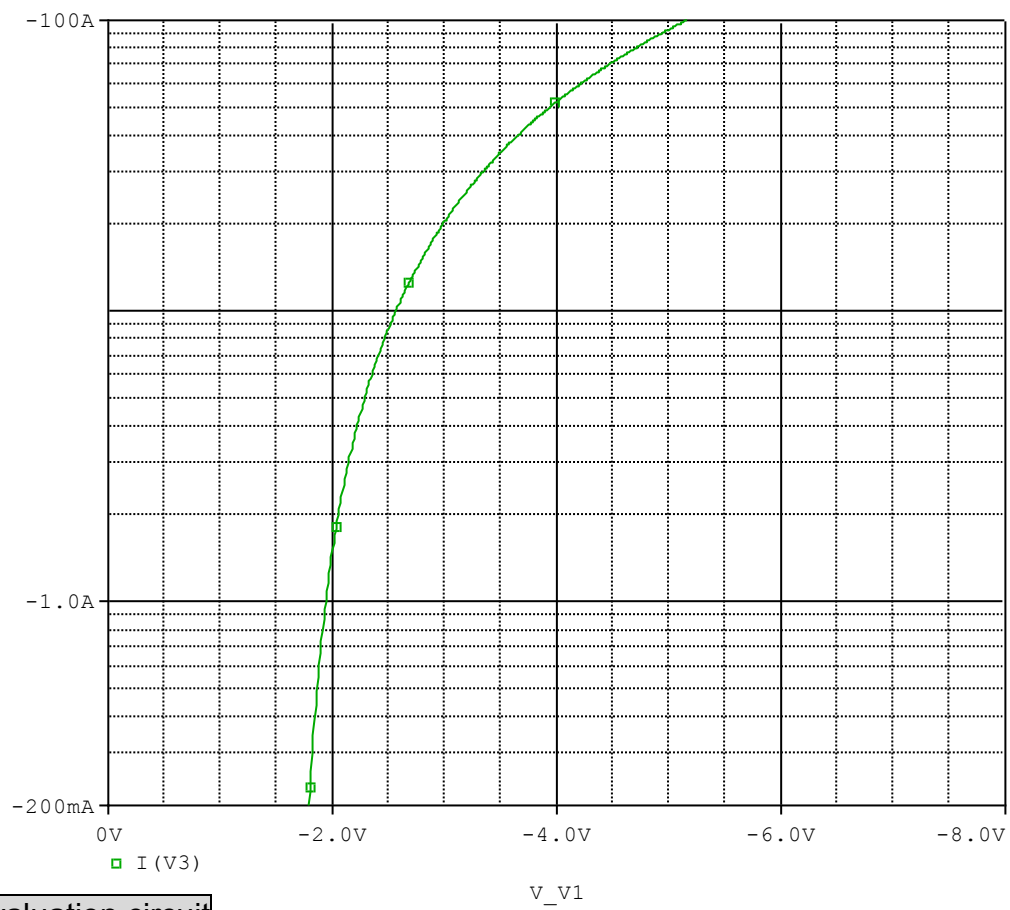


Comparison table

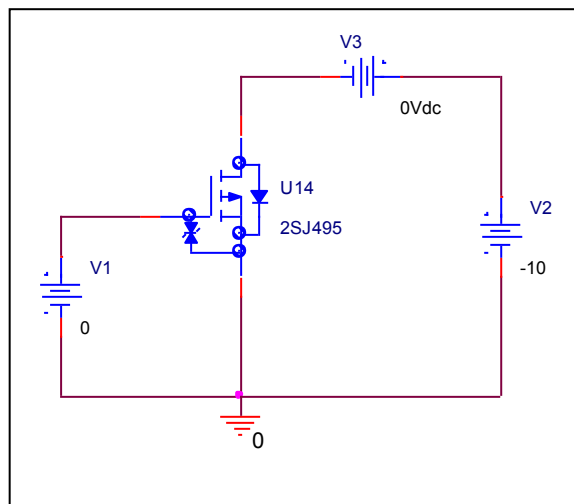
Id(A)	gfs		Error(%)
	Measurement	Simulation	
-0.2	3.330	3.333	0.090
-0.5	5.550	5.556	0.108
-1	7.500	7.692	2.560
-2	10.500	10.526	0.248
-5	15.550	15.625	0.482
-10	20.200	20.408	1.030
-20	25.400	25.641	0.949

V_{gs}-I_d Characteristic

Circuit Simulation result

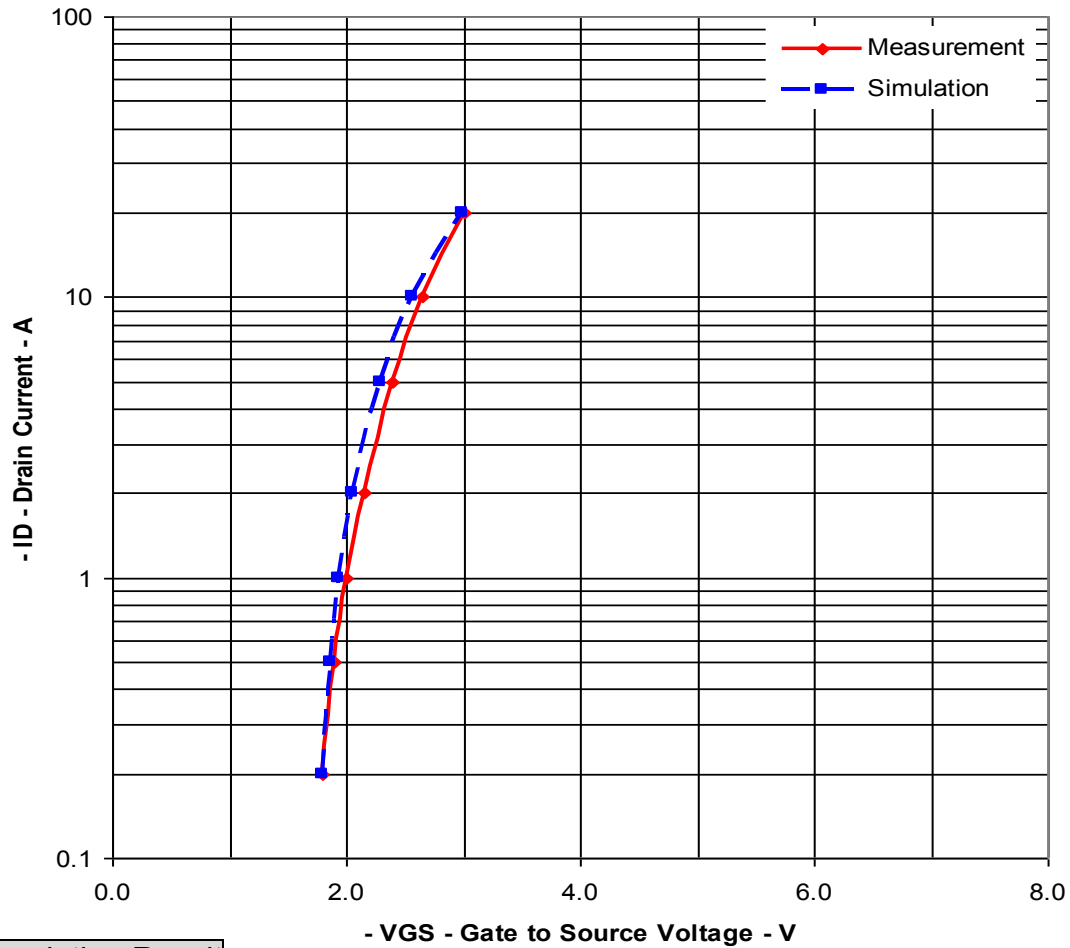


Evaluation circuit



Comparison Graph

Circuit Simulation Result

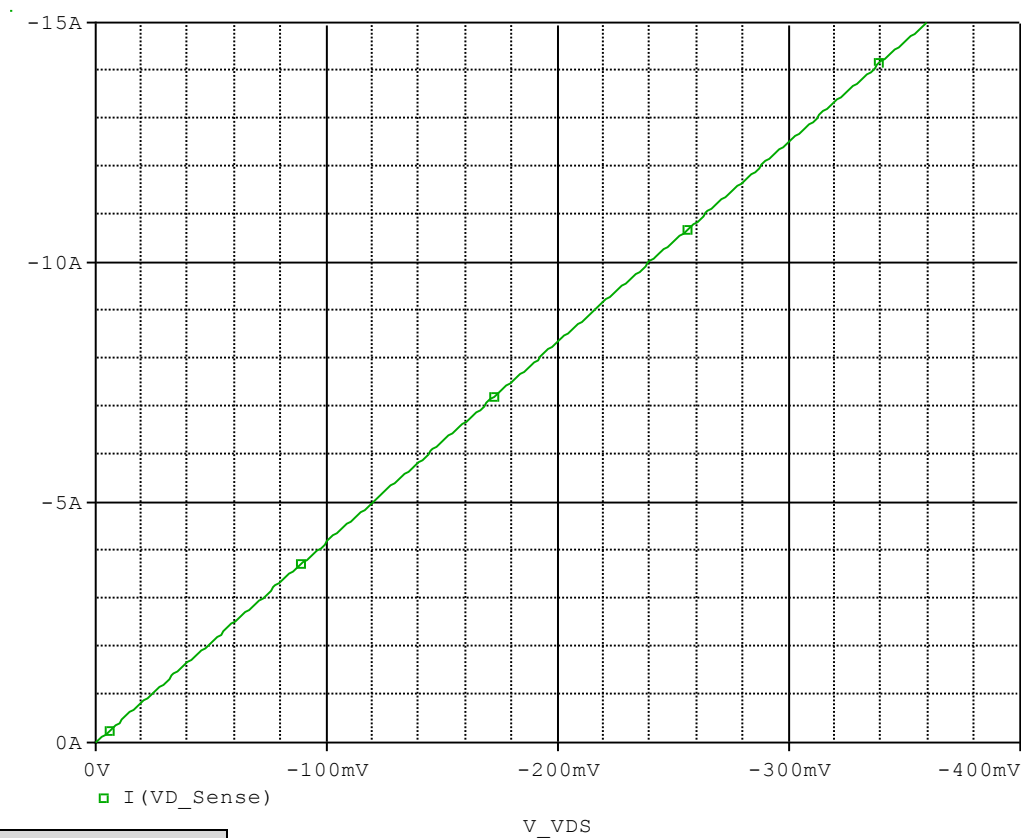


Simulation Result

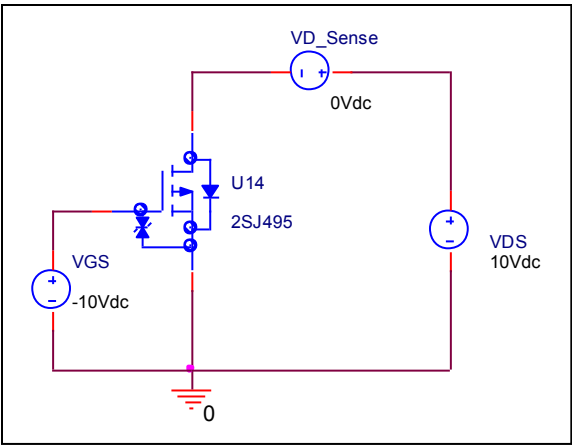
$I_D(A)$	$V_{GS}(V)$		Error (%)
	Measurement	Simulation	
-0.2	-1.800	-1.793	-0.389
-0.5	-1.900	-1.861	-2.053
-1	-2.000	-1.939	-3.050
-2	-2.150	-2.049	-4.698
-5	-2.400	-2.283	-4.875
-10	-2.650	-2.564	-3.245
-20	-3.000	-2.993	-0.233

Rds(on) Characteristic

Circuit Simulation result



Evaluation circuit

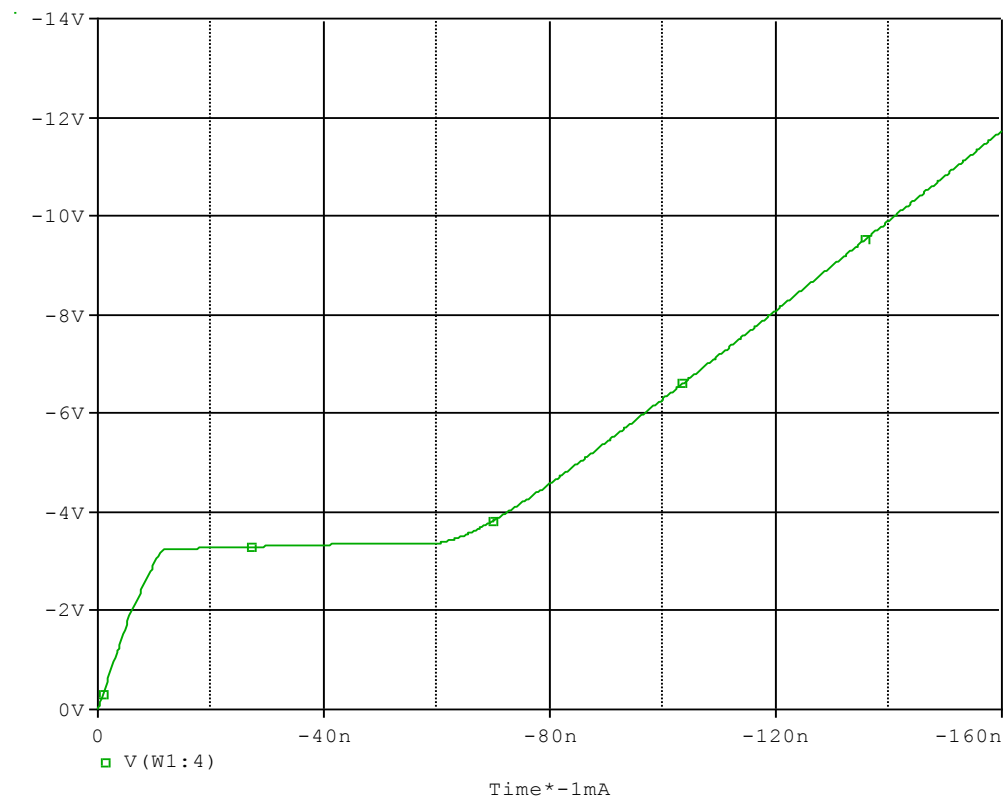


Simulation Result

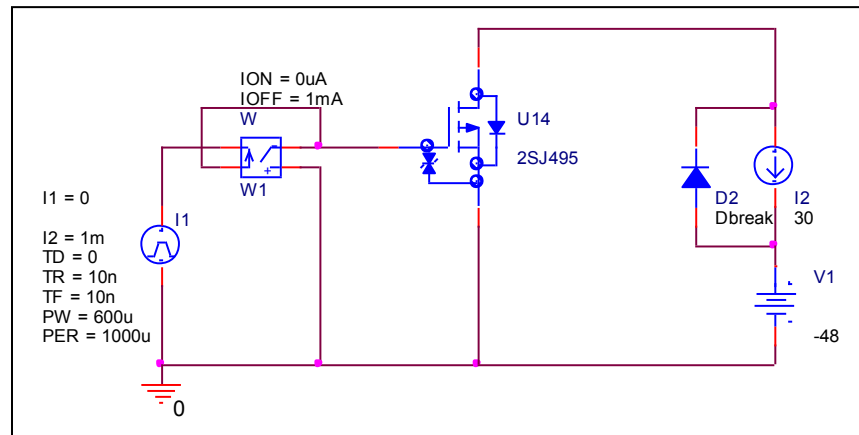
$I_D = -15A, V_{GS} = -10V$	Measurement	Simulation	Error (%)
$R_{DS(on)} \Omega$	0.024	0.024	0

Gate Charge Characteristic

Circuit Simulation result



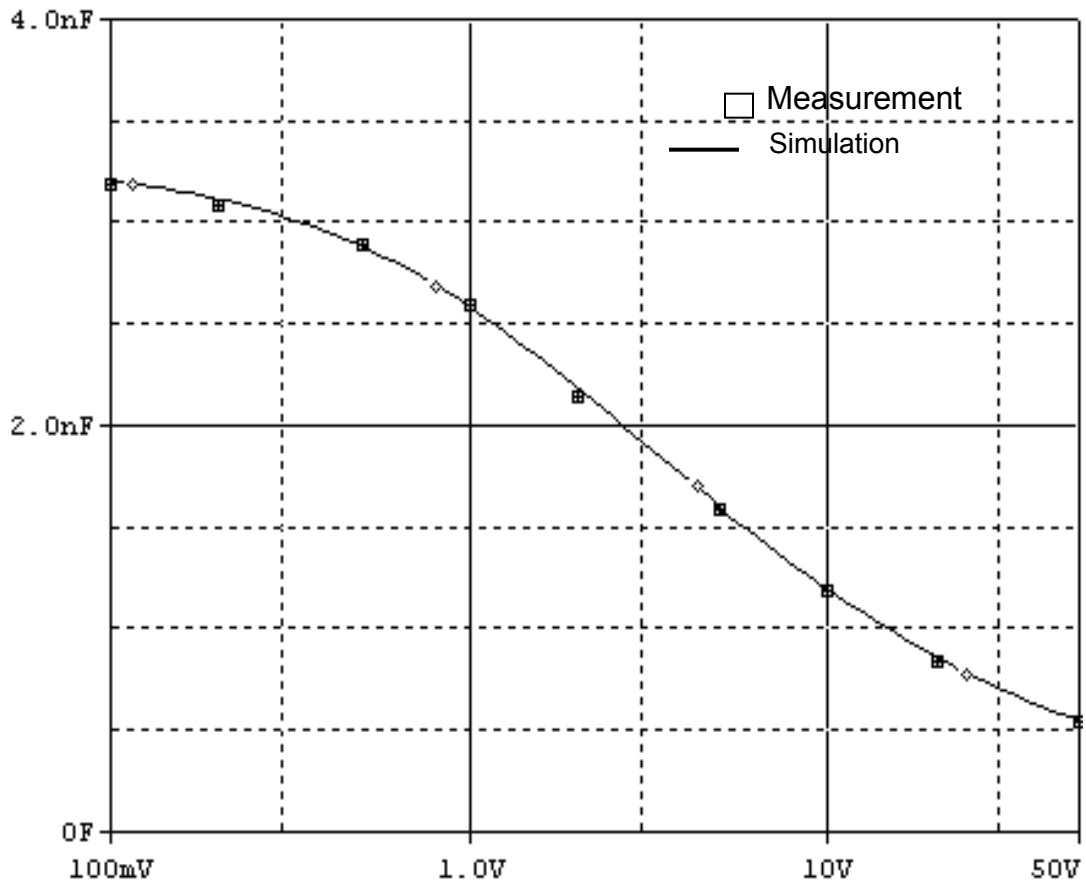
Evaluation circuit



Simulation Result

$V_{DD} = -48V, I_D = -30A$ $V_{GS} = -10V$	Measurement	Simulation	Error (%)
Qgs (nC)	12.000	12.069	0.575
Qgd(nC)	46.000	46.079	0.172
Qg(nC)	140.000	141.111	0.794

Capacitance Characteristic

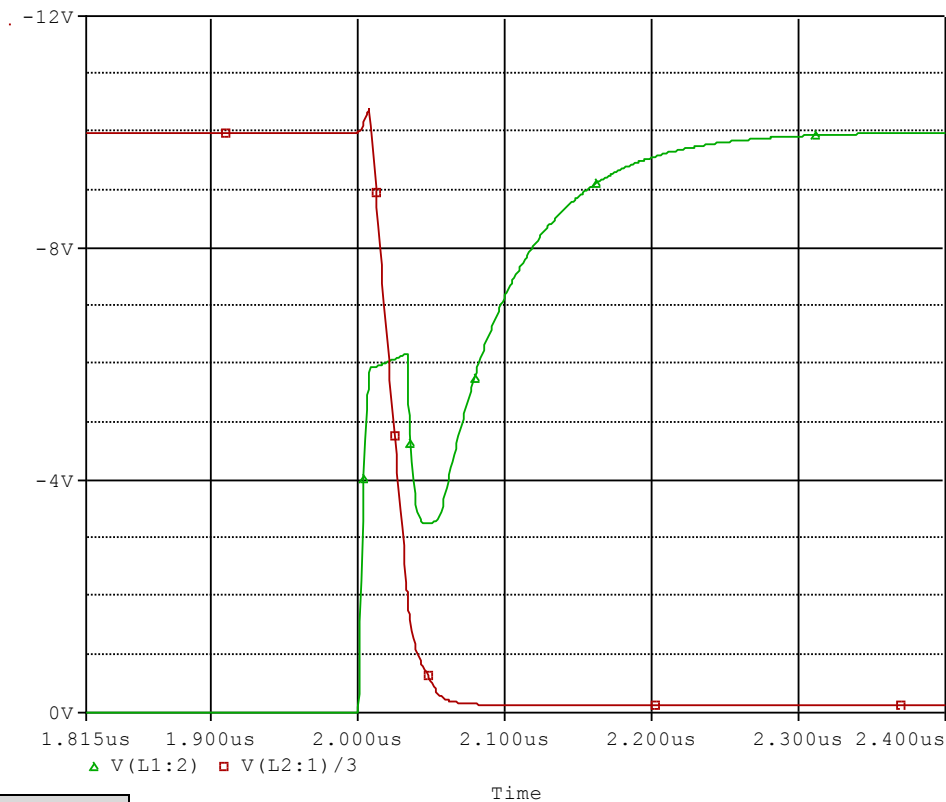


Simulation Result

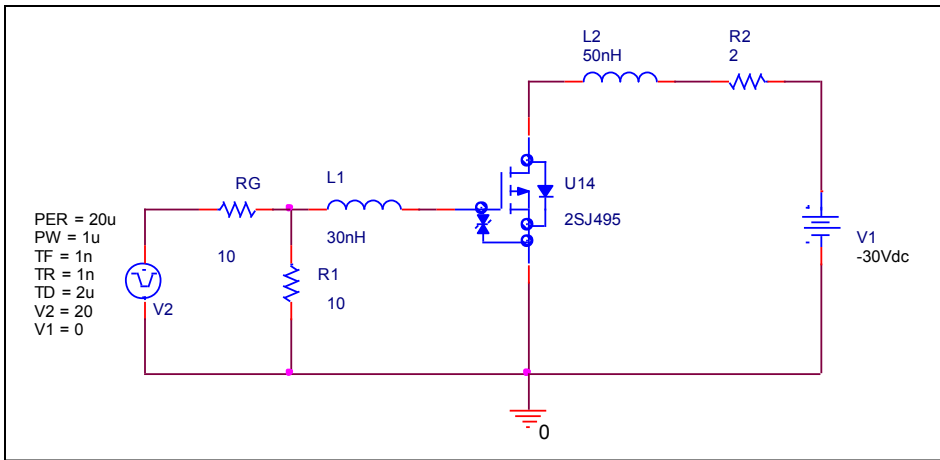
V _{DS} (V)	C _{bd} (pF)		Error(%)
	Measurement	Simulation	
0.1	3200	3210	0.313
0.2	3100	3115	0.484
0.5	2900	2900	0.000
1	2600	2600	0.000
2	2150	2155	0.233
5	1600	1600	0.000
10	1200	1200	0.000
20	850	847	-0.353
50	550	550	0.000

Switching Time Characteristic

Circuit Simulation result



Evaluation circuit

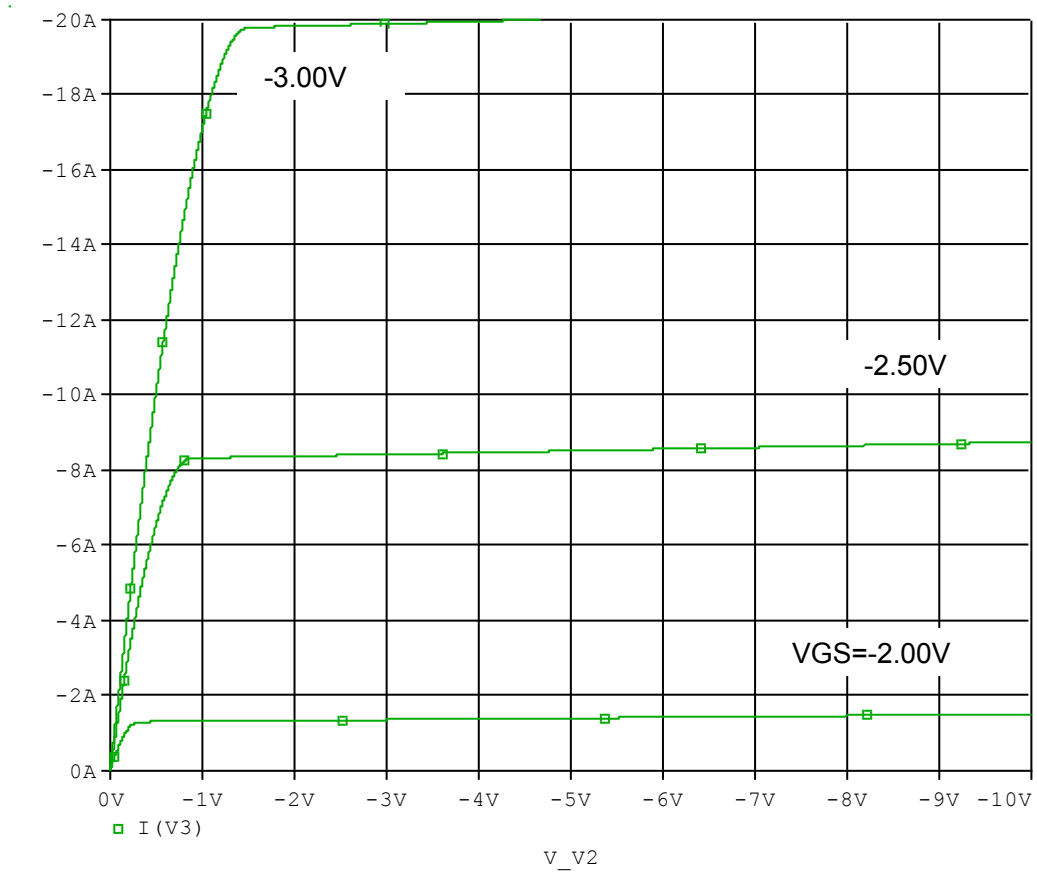


Simulation Result

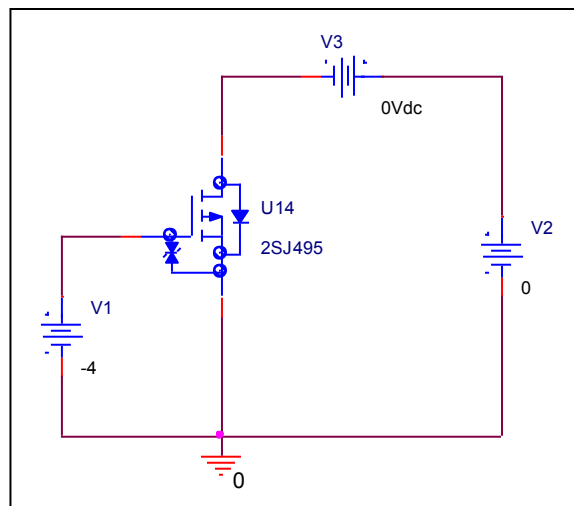
$I_D = -15A, V_{DD} = -30V$ $V_{GS} = 0/-10V$	Measurement	Simulation	Error(%)
Ton(ns)	40.000	39.653	-0.868

Output Characteristic

Circuit Simulation result

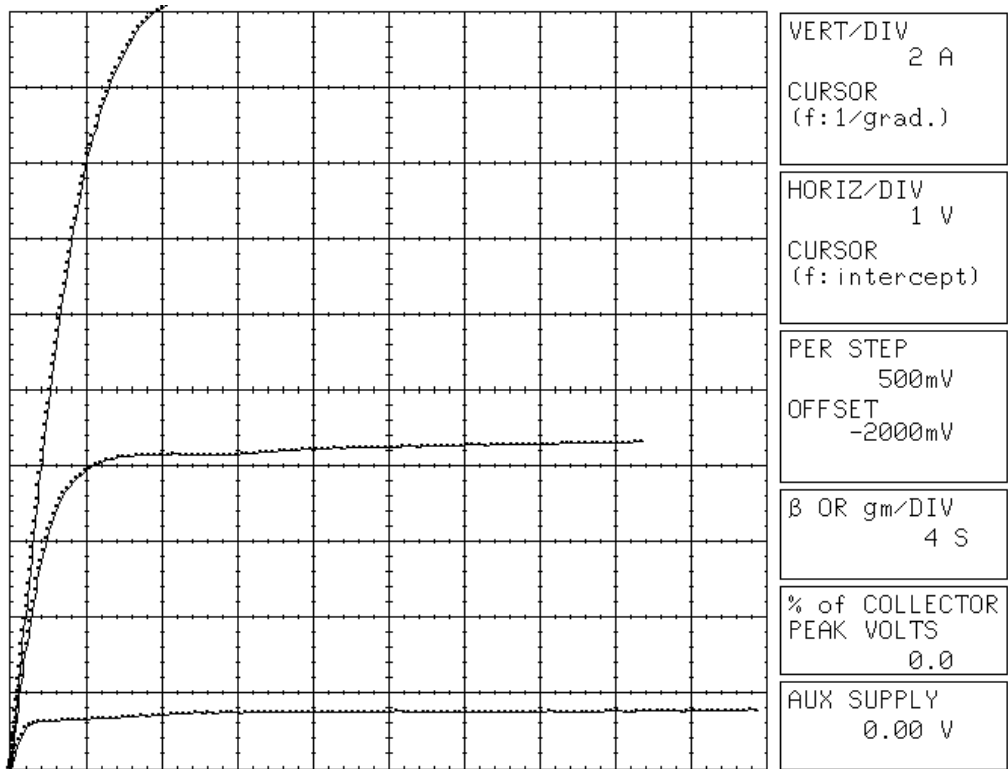


Evaluation circuit



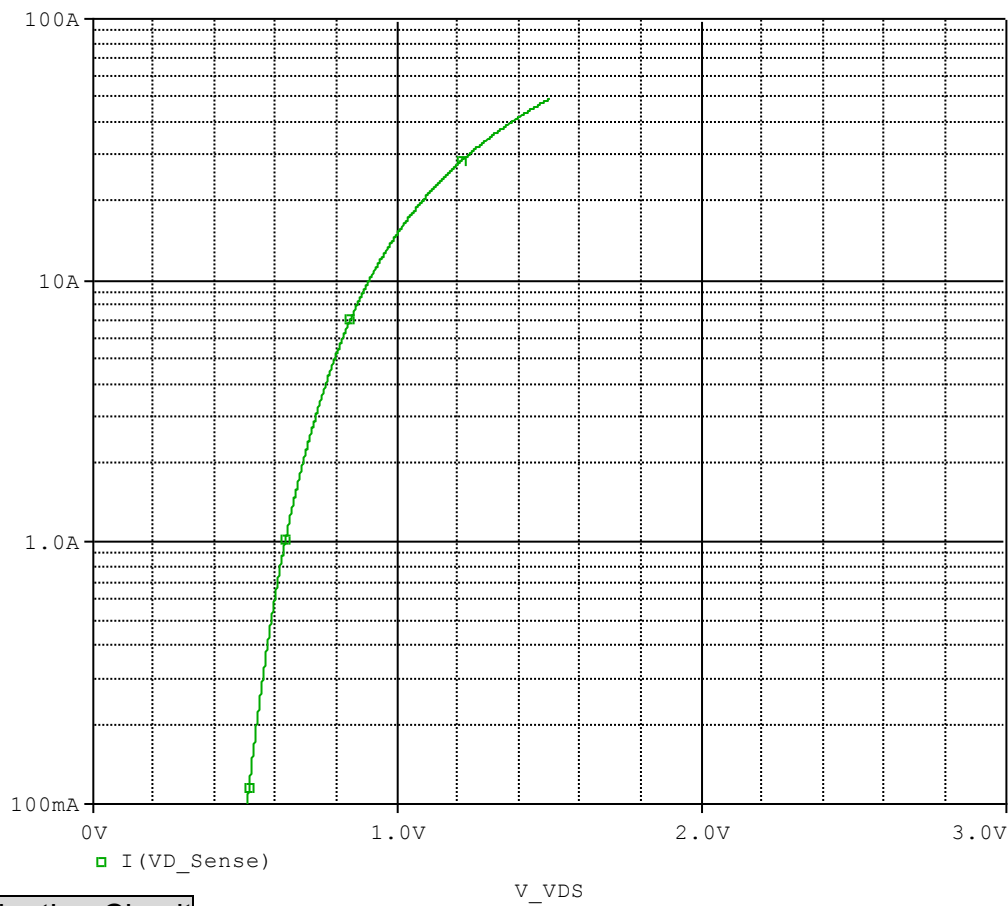
Output Characteristic

Reference

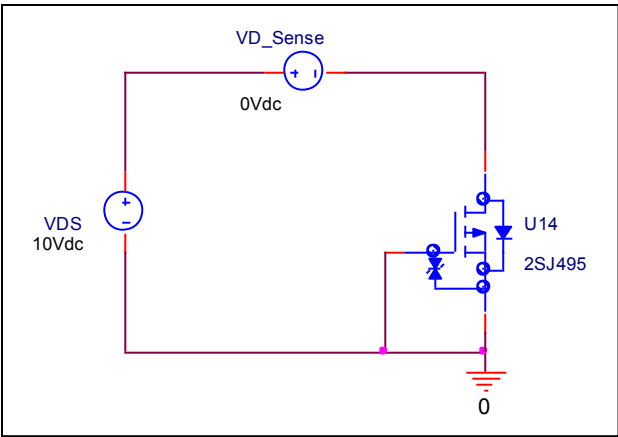


Forward Current Characteristic

Circuit Simulation Result

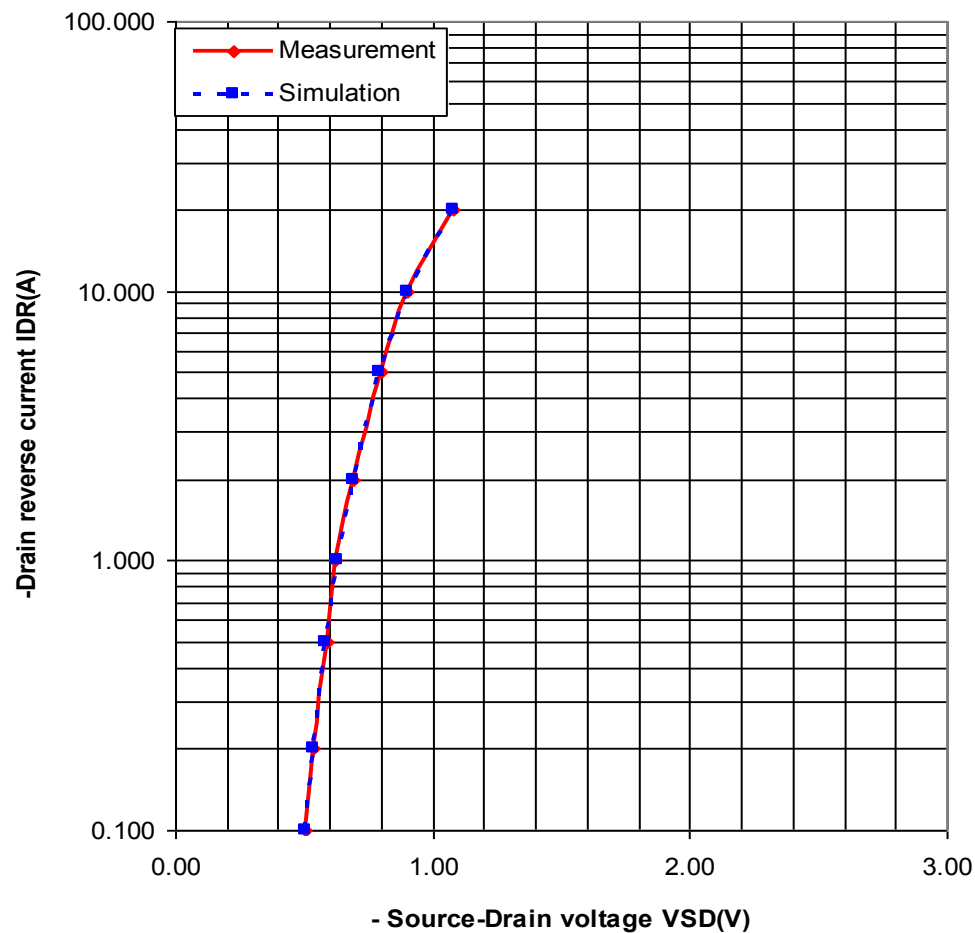


Evaluation Circuit



Comparison Graph

Circuit Simulation Result

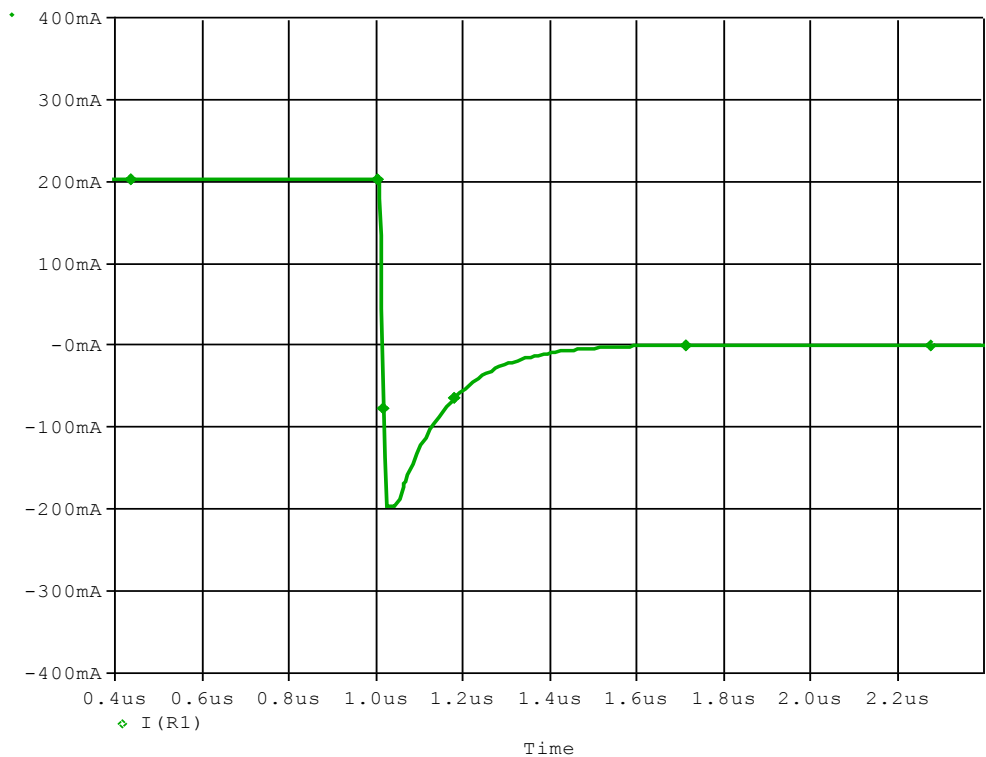


Simulation Result

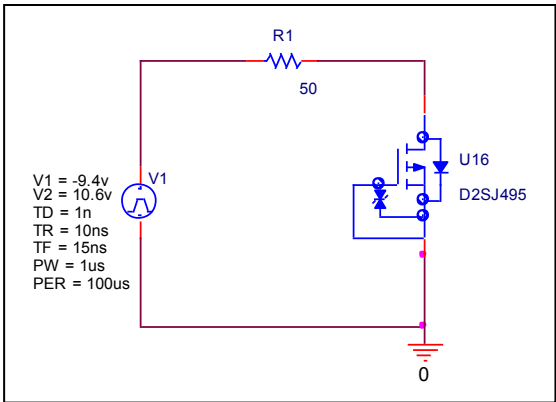
IDR(A)	VDS(V) Measurement	VDS(V) Simulation	%Error
-0.5	-0.590	-0.585	-0.847
-1	-0.620	-0.630	1.613
-2	-0.690	-0.690	0.000
-5	-0.800	-0.792	-1.000
-10	-0.900	-0.901	0.111
-20	-1.080	-1.079	-0.093

Reverse Recovery Characteristic

Circuit Simulation Result



Evaluation Circuit

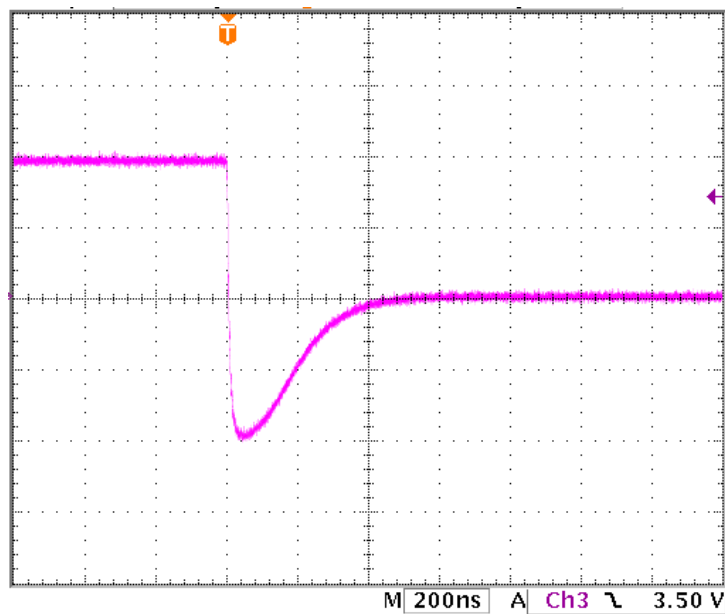


Compare Measurement vs. Simulation

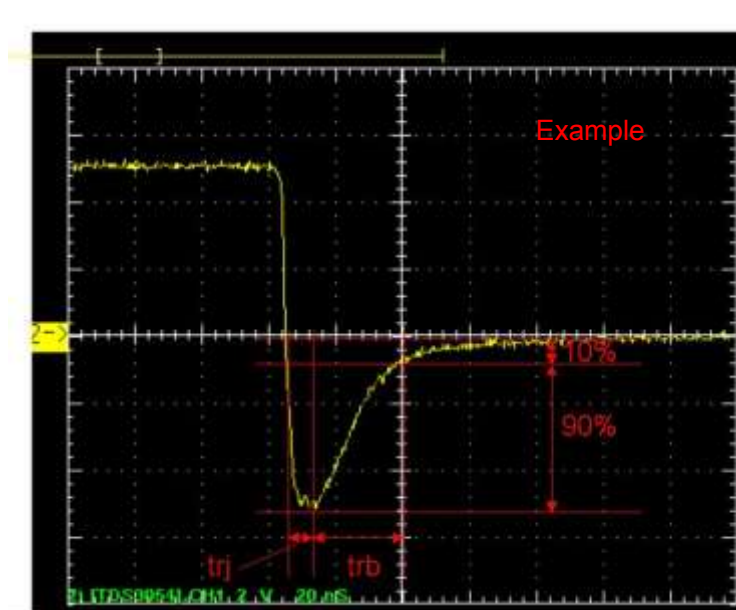
	Measurement	Simulation	Error (%)
trj (ns)	29.000	29.360	1.241
Trb (ns)	353.000	353.478	0.135
Trr (ns)	382.000	382.838	0.219

Reverse Recovery Characteristic

Reference



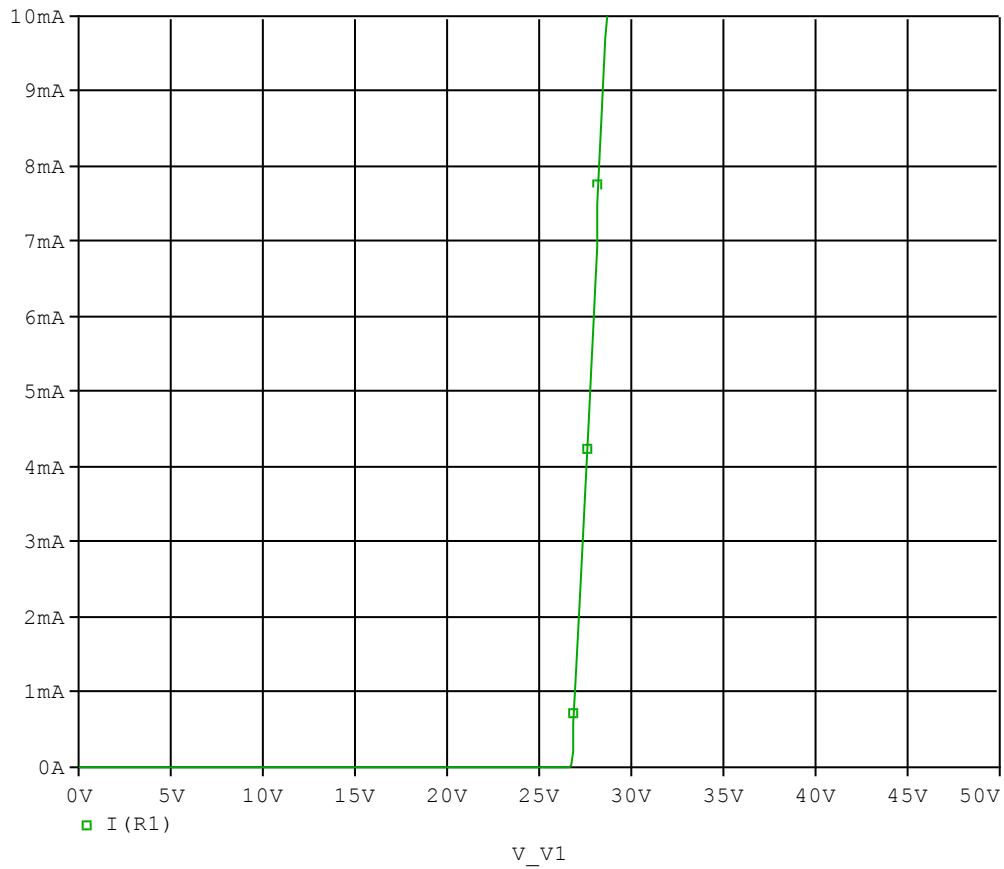
Trj=29.000(ns)
Trb=353.000(ns)
Conditions: Ifwd=Irev=0.2(A), RI=50



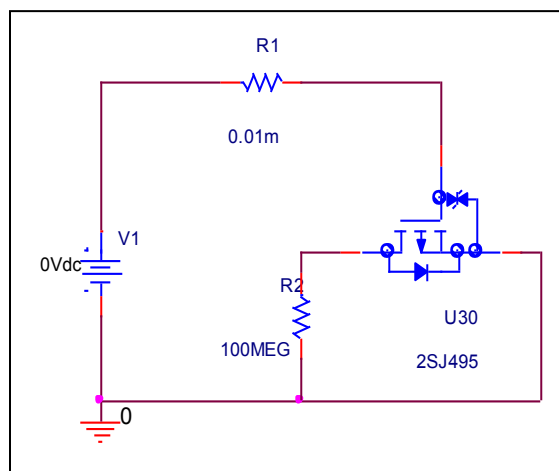
Relation between trj and trb

Zener Voltage Characteristic

Circuit Simulation Result



Evaluation Circuit



Zener Voltage Characteristic

Reference

