Device Modeling Report

COMPONENTS: MOSFET (Model Parameter)
PART NUMBER: 2SJ0536
MANUFACTURER: Panasonic
Body Diode (Model Parameter) / ESD Protection Diode

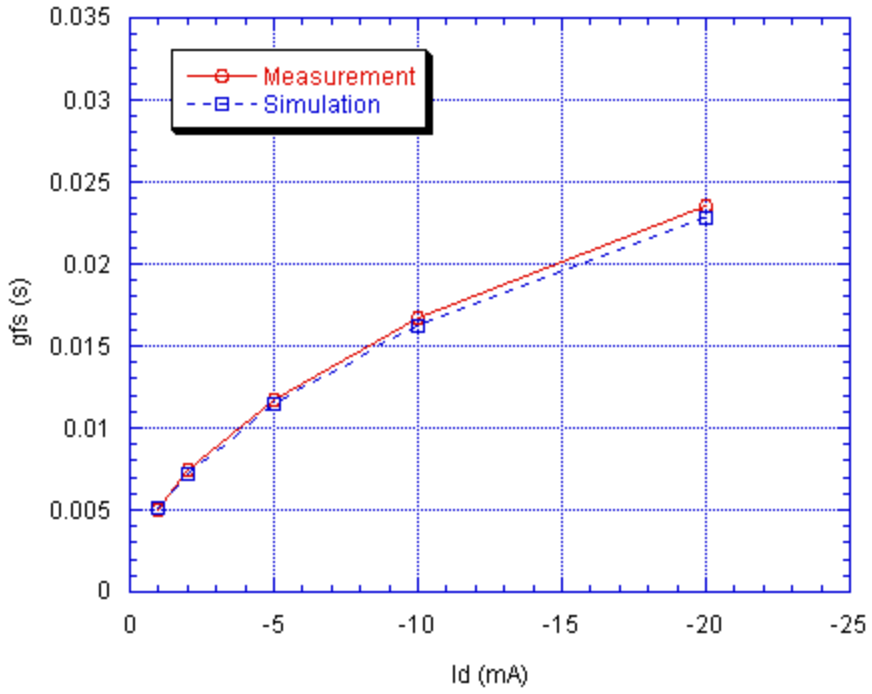
Bee Technologies Inc.

MOSFET MODEL

Pspice model parameter	Model description
LEVEL	
L	Channel Length
W	Channel Width
KP	Transconductance
RS	Source Ohmic Resistance
RD	Ohmic Drain Resistance
VTO	Zero-bias Threshold Voltage
RDS	Drain-Source Shunt Resistance
TOX	Gate Oxide Thickness
CGSO	Zero-bias Gate-Source Capacitance
CGDO	Zero-bias Gate-Drain Capacitance
CBD	Zero-bias Bulk-Drain Junction Capacitance
MJ	Bulk Junction Grading Coefficient
PB	Bulk Junction Potential
FC	Bulk Junction Forward-bias Capacitance Coefficient
RG	Gate Ohmic Resistance
IS	Bulk Junction Saturation Current
N	Bulk Junction Emission Coefficient
RB	Bulk Series Resistance
PHI	Surface Inversion Potential
GAMMA	Body-effect Parameter
DELTA	Width effect on Threshold Voltage
ETA	Static Feedback on Threshold Voltage
THETA	Modility Modulation
KAPPA	Saturation Field Factor
VMAX	Maximum Drift Velocity of Carriers
XJ	Metallurgical Junction Depth
UO	Surface Mobility

Transconductance Characteristic

Circuit Simulation Result

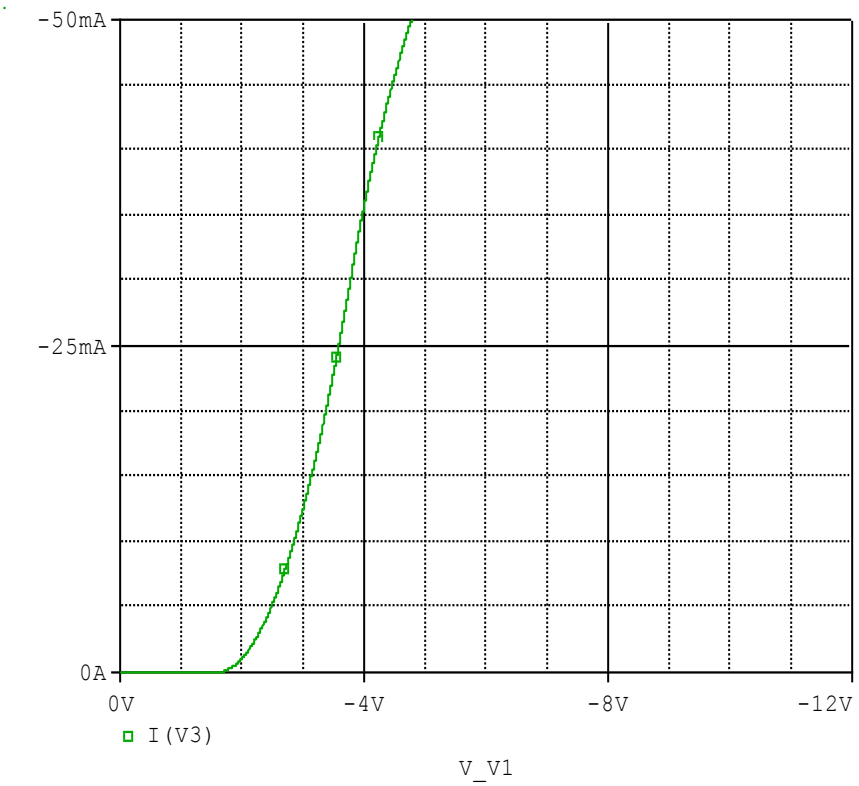


Comparison table

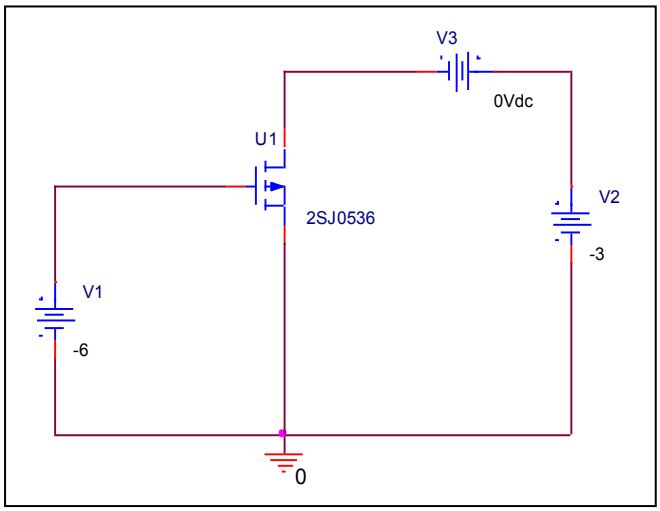
Id(mA)	gfs (s)		Error(%)
	Measurement	Simulation	
-1	0.0050	0.0051	2.000
-2	0.0074	0.0072	-2.703
-5	0.0118	0.0114	-3.390
-10	0.0167	0.0162	-2.994
-20	0.0235	0.0229	-2.553

Vgs-Id Characteristic

Circuit Simulation result

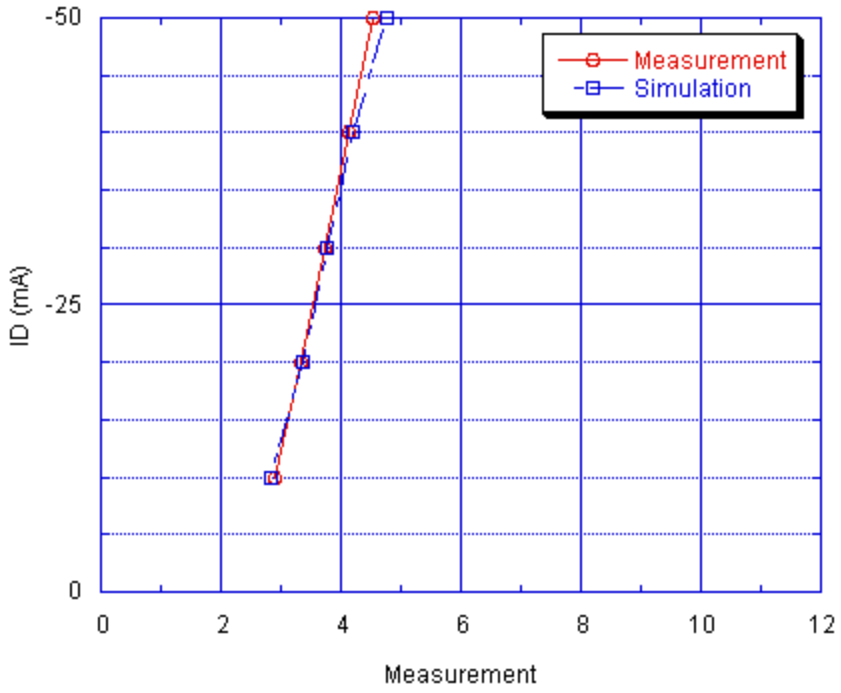


Evaluation circuit



Comparison Graph

Circuit Simulation Result

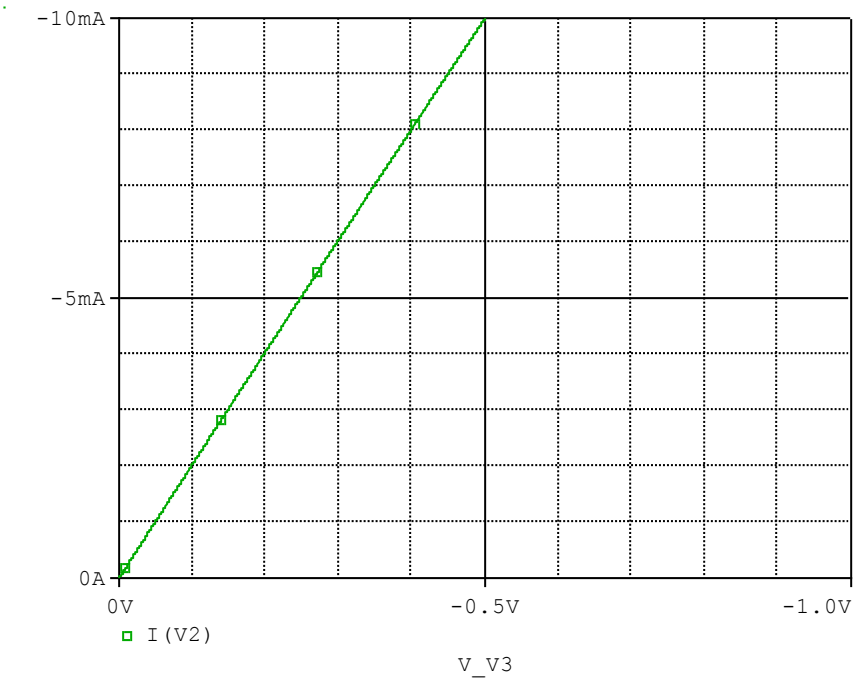


Simulation Result

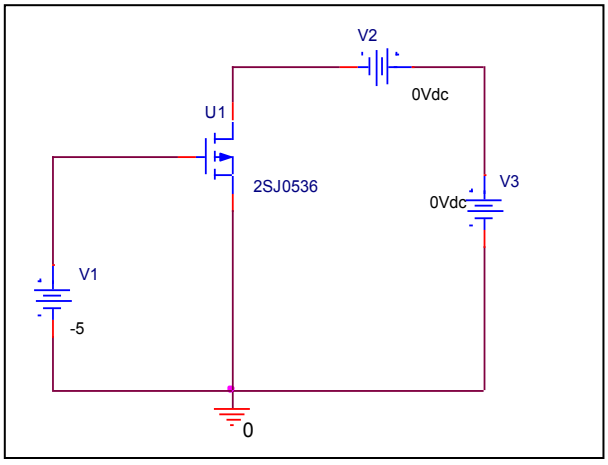
I_D(mA)	V_{GS}(V)		Error (%)
	Measurement	Simulation	
-10	2.9	2.848	-1.793
-20	3.35	3.373	0.687
-30	3.75	3.776	0.693
-40	4.15	4.195	1.084
-50	4.55	4.777	4.989

Rds(on) Characteristic

Circuit Simulation result



Evaluation circuit

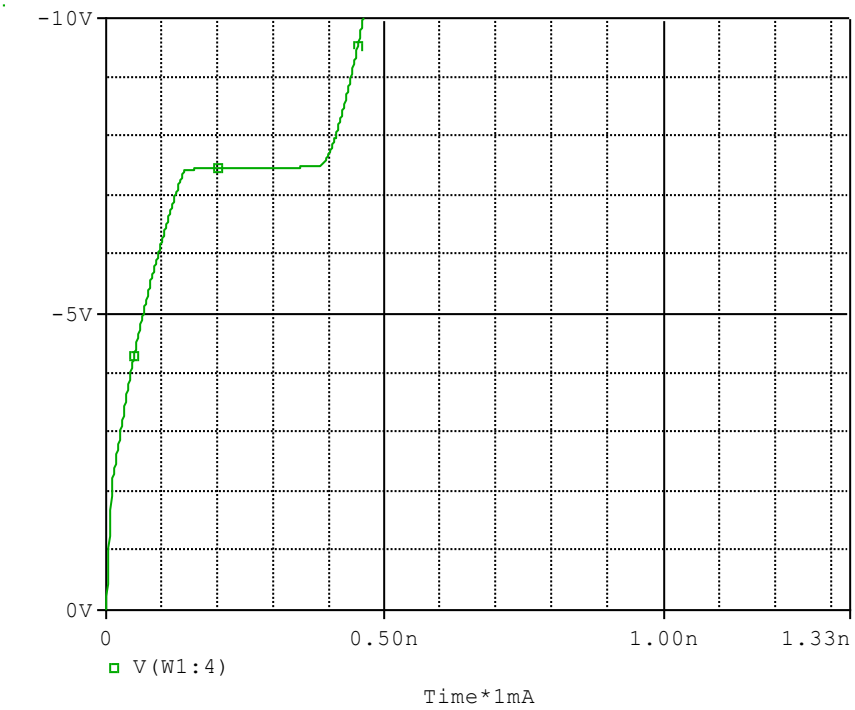


Simulation Result

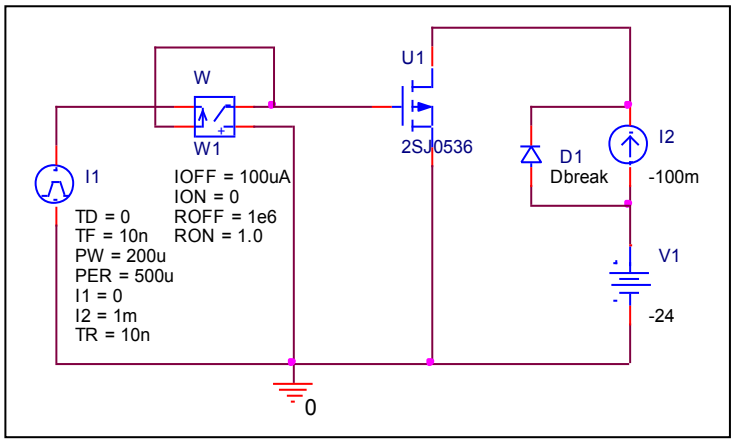
I_D=-10mA, V_{GS}=-5V	Measurement		Simulation		Error (%)
R_{DS} (on)	50	Ω	50	Ω	0

Gate Charge Characteristic

Circuit Simulation result



Evaluation circuit



Simulation Result

V_{DD}=-24V,I_D=-0.1A	Measurement		Simulation		Error (%)
Qgs	0.14	nC	0.141	nC	0.714
Qgd	0.24	nC	0.240	nC	0
Qg	0.62	nC	0.461	nC	-25.645

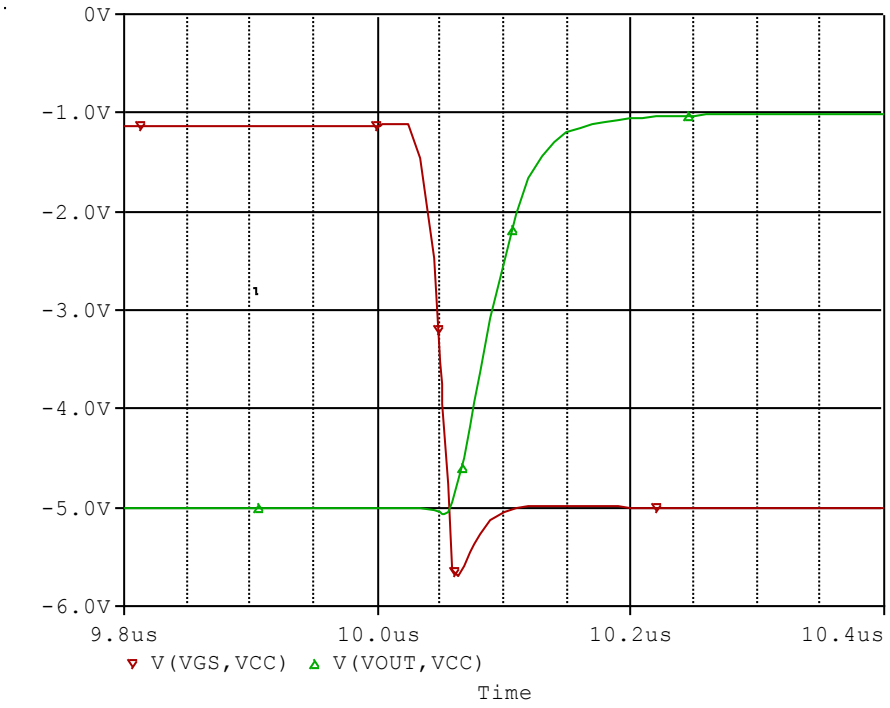
Capacitance Characteristic

Simulation Result

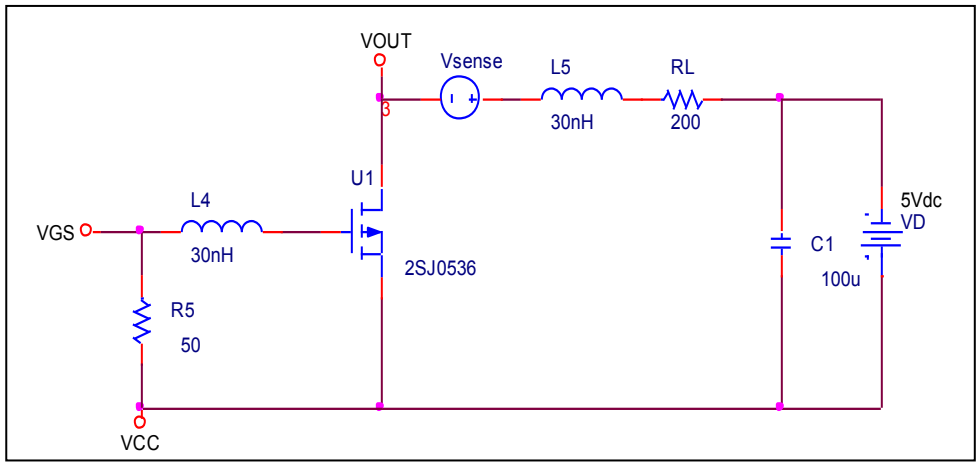
V_{DS}(V)	Cbd(pF)		Error(%)
	Measurement	Simulation	
1	4.4335	4.4	-0.756
2	3.4358	3.47	0.995
5	2.4186	2.422	0.141
10	1.8229	1.81	-0.708
20	1.3639	1.34	-1.752

Switching Time Characteristic

Circuit Simulation result



Evaluation circuit

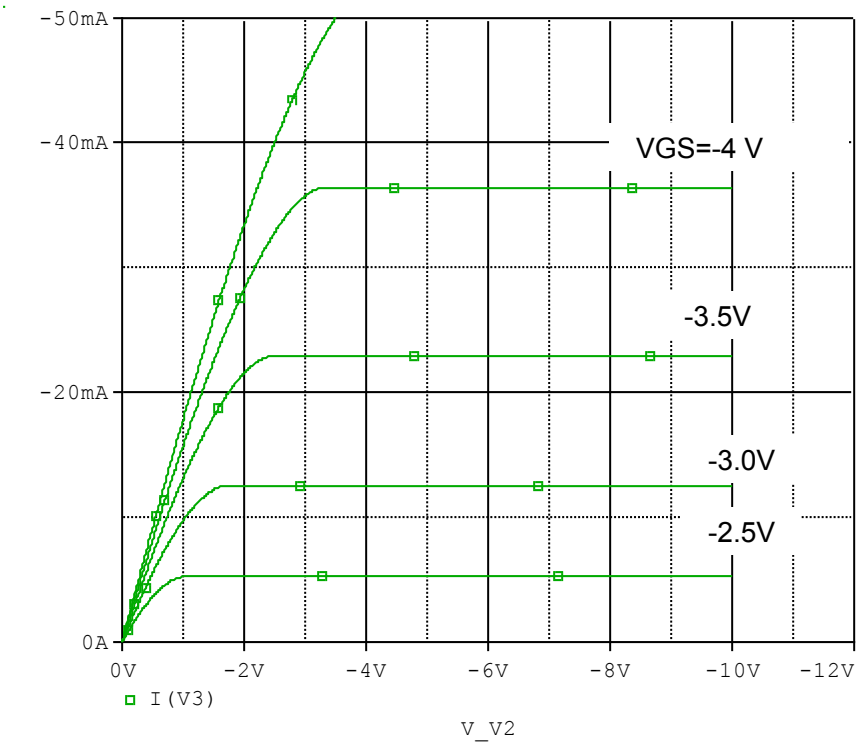


Simulation Result

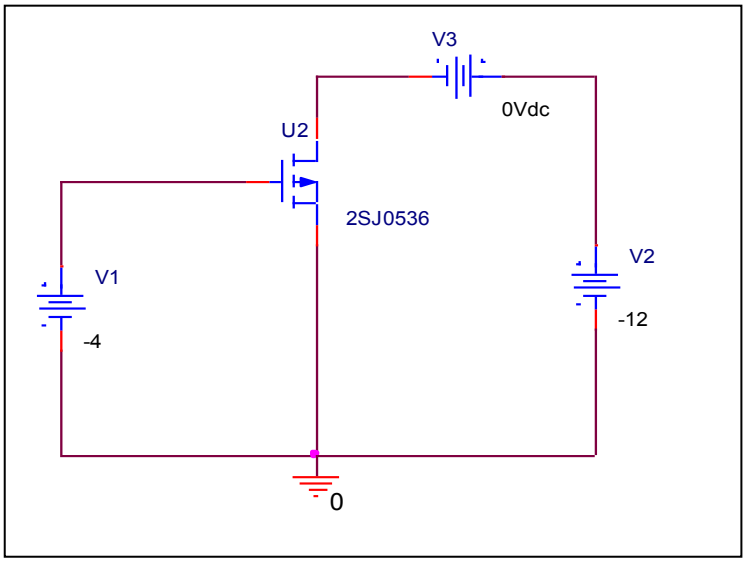
R_L=200, V_{DD}=-5V V_{GS}=0V~ -5V	Measurement		Simulation		Error(%)
ton	100	ns	95.736	ns	-4.264

Output Characteristic

Circuit Simulation result

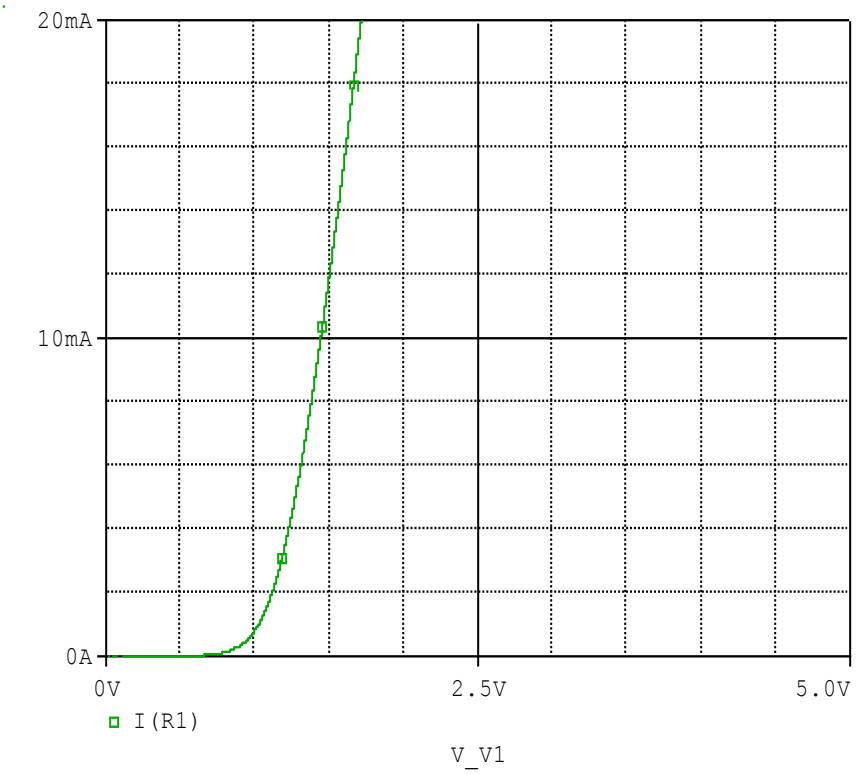


Evaluation circuit

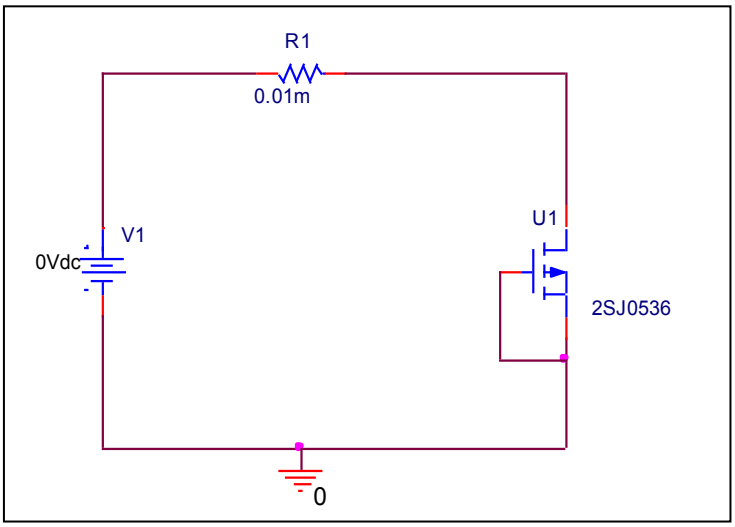


Reverse Drain Current Characteristic

Circuit Simulation Result

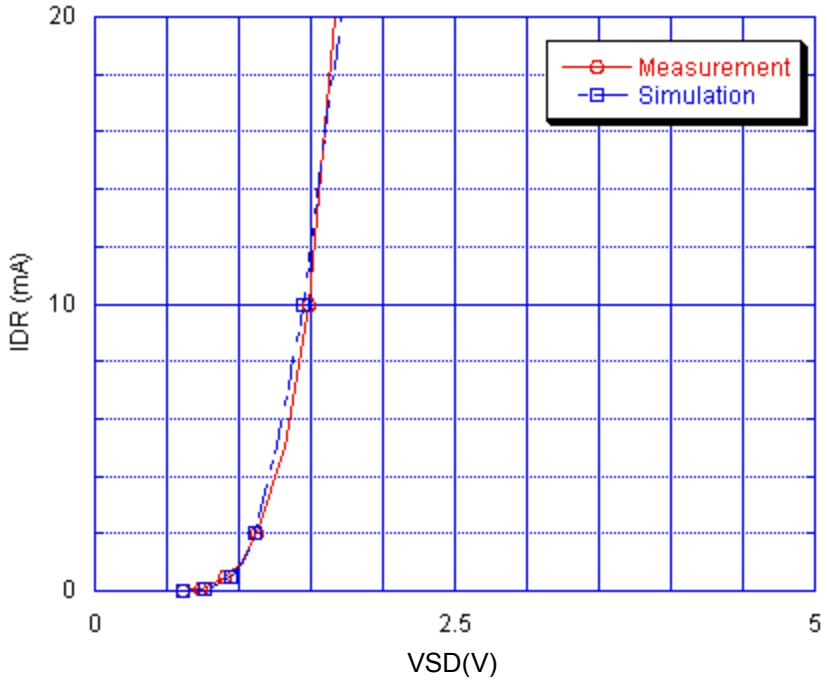


Evaluation Circuit



Comparison Graph

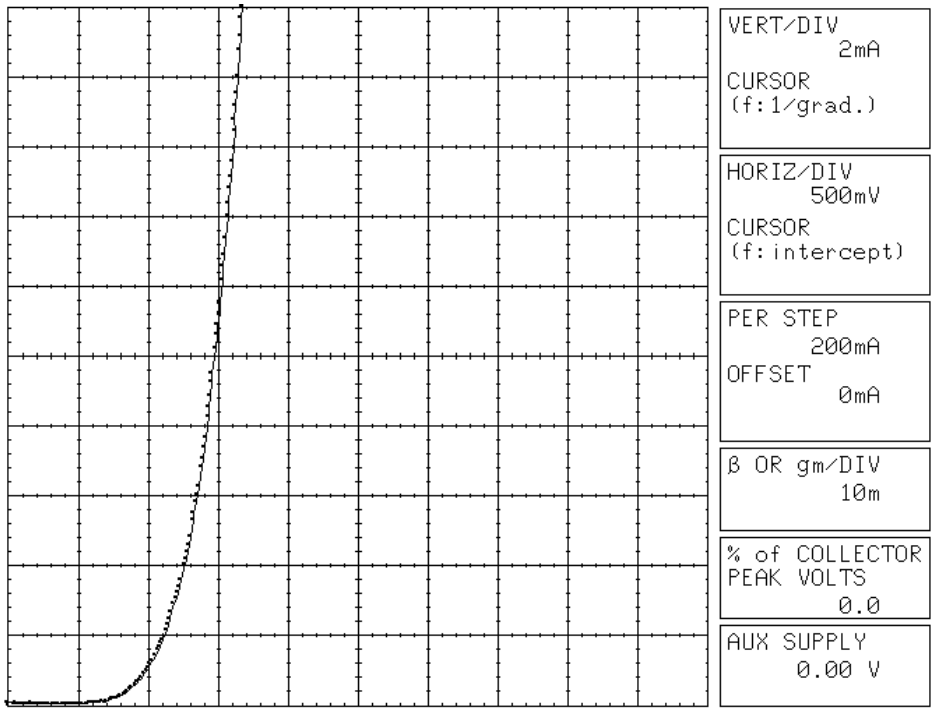
Circuit Simulation Result



Simulation Result

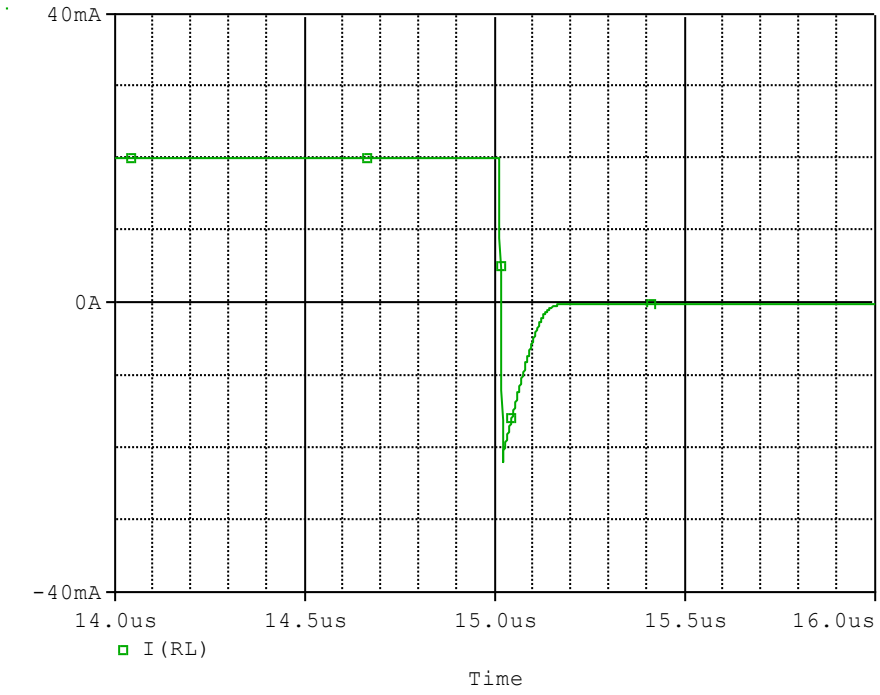
IDR (mA)	VSD (V)		%Error
	Measurement	Simulation	
0.01	0.569	0.545	-4.218
0.02	0.611	0.607	-0.655
0.05	0.674	0.698	3.561
0.1	0.737	0.770	4.478
0.2	0.802	0.843	4.988
0.5	0.906	0.943	4.084
1	1.008	1.0246	1.647
2	1.132	1.116	-1.413
5	1.324	1.2706	-4.033
10	1.486	1.4425	-2.927
20	1.672	1.7147	2.554

Reverse Drain Current Characteristics Reference

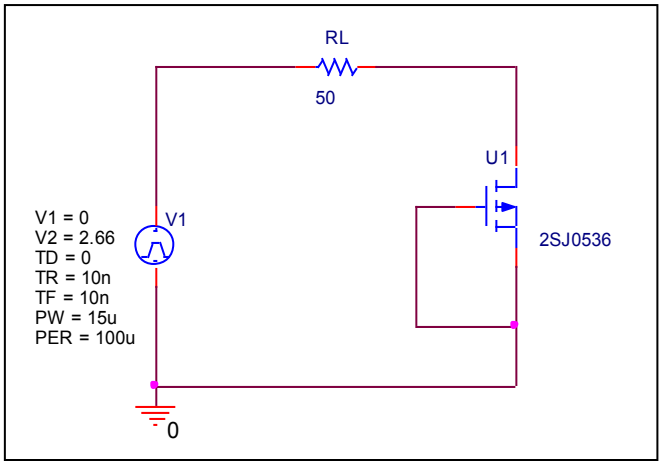


Reverse Recovery Characteristic

Circuit Simulation Result



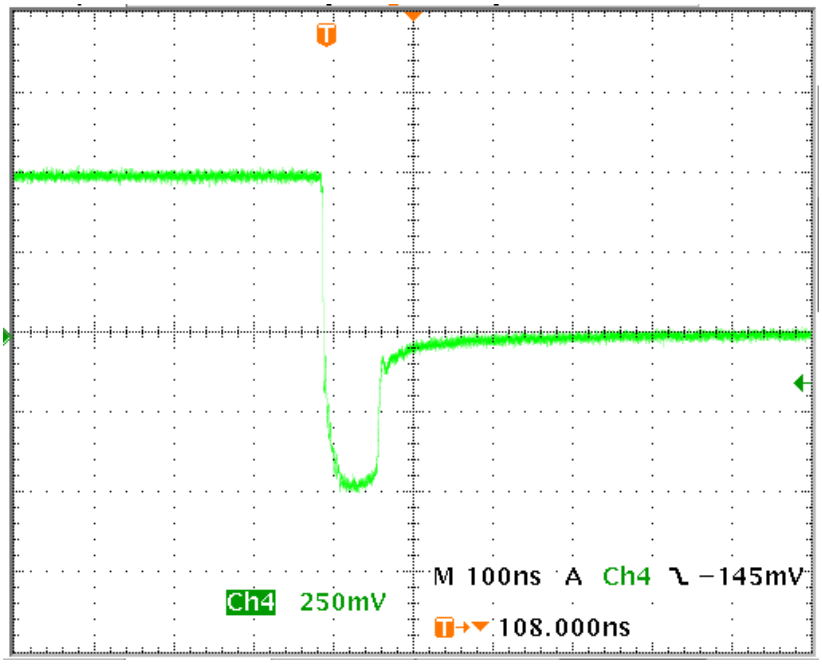
Evaluation Circuit



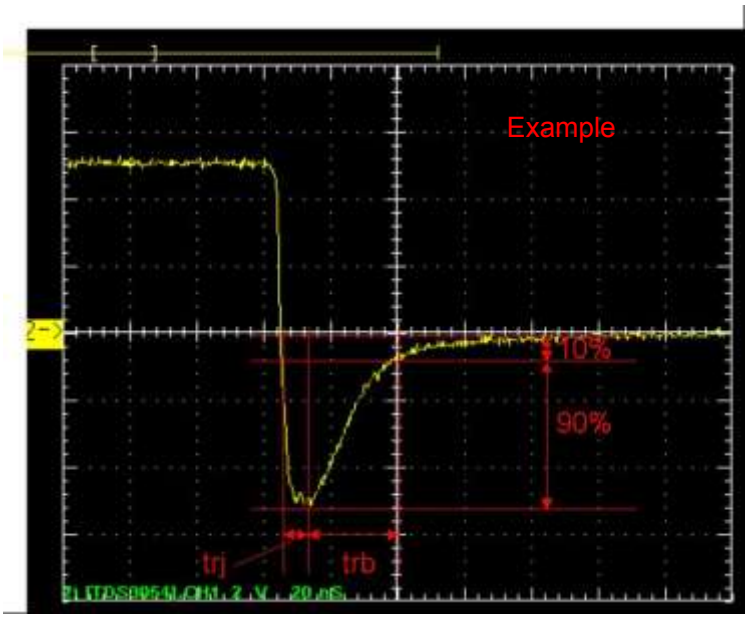
Compare Measurement vs. Simulation

	Measurement		Simulation		Error (%)
trj	**44**	**ns**	**43.007**	**ns**	**-2.257**
trb	**68**	**ns**	**66.396**	**ns**	**-2.359**
trr	**112**	**ns**	**109.403**	**ns**	**-2.319**

Reverse Recovery Characteristic Reference



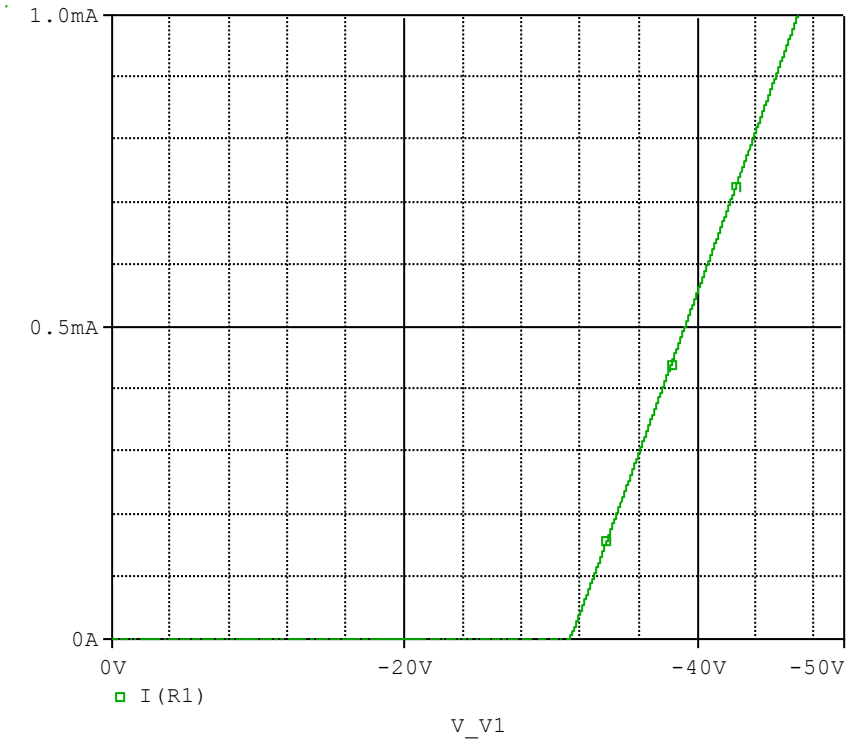
Trj=44(ns)
Trb=68(ns)
Conditions:Ifwd=Irev=0.02(A),Rl=50



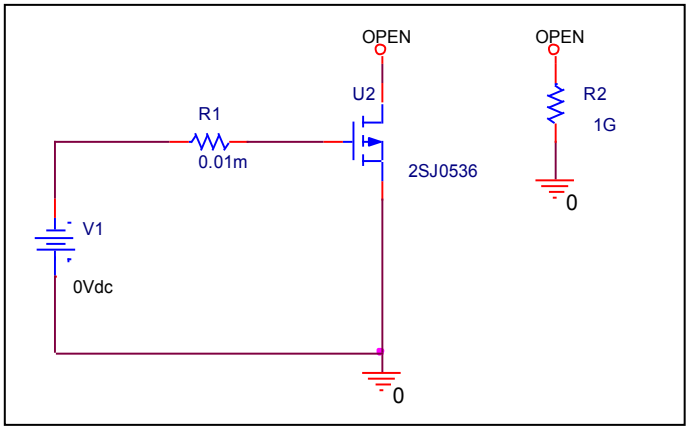
Relation between trj and trb

Zener Voltage Characteristic

Circuit Simulation Result



Evaluation Circuit



Zener Voltage Characteristic Reference