

Device Modeling Report

COMPONENTS: Power MOSFET (Professional Model)
PART NUMBER: 2SJ655
MANUFACTURER: SANYO
Body Diode (Professional Model) / ESD Protection Diode



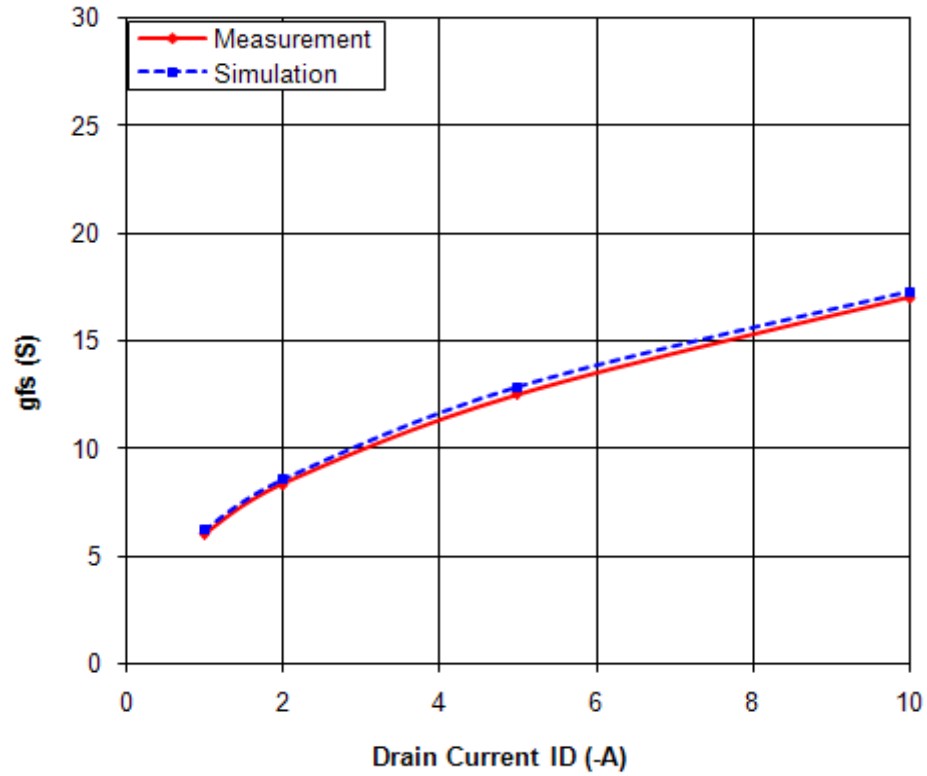
Bee Technologies Inc.

MOSFET MODEL

PSpice model parameter	Model description
LEVEL	
L	Channel Length
W	Channel Width
KP	Transconductance
RS	Source Ohmic Resistance
RD	Ohmic Drain Resistance
VTO	Zero-bias Threshold Voltage
RDS	Drain-Source Shunt Resistance
TOX	Gate Oxide Thickness
CGSO	Zero-bias Gate-Source Capacitance
CGDO	Zero-bias Gate-Drain Capacitance
CBD	Zero-bias Bulk-Drain Junction Capacitance
MJ	Bulk Junction Grading Coefficient
PB	Bulk Junction Potential
FC	Bulk Junction Forward-bias Capacitance Coefficient
RG	Gate Ohmic Resistance
IS	Bulk Junction Saturation Current
N	Bulk Junction Emission Coefficient
RB	Bulk Series Resistance
PHI	Surface Inversion Potential
GAMMA	Body-effect Parameter
DELTA	Width effect on Threshold Voltage
ETA	Static Feedback on Threshold Voltage
THETA	Mobility Modulation
KAPPA	Saturation Field Factor
VMAX	Maximum Drift Velocity of Carriers
XJ	Metallurgical Junction Depth
UO	Surface Mobility

Transconductance Characteristic

Circuit Simulation Result

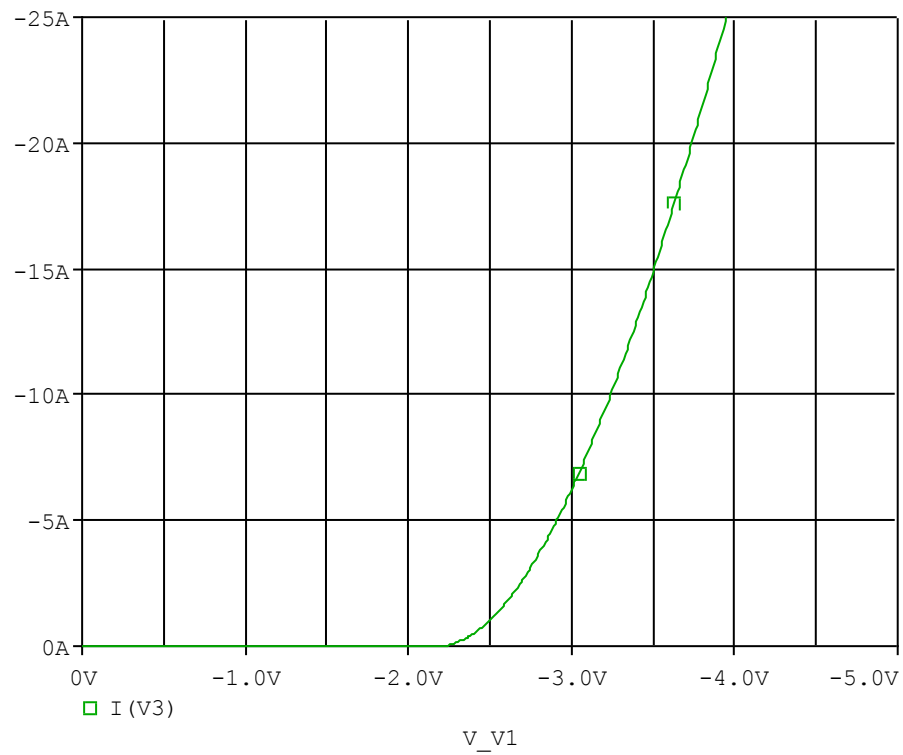


Comparison table

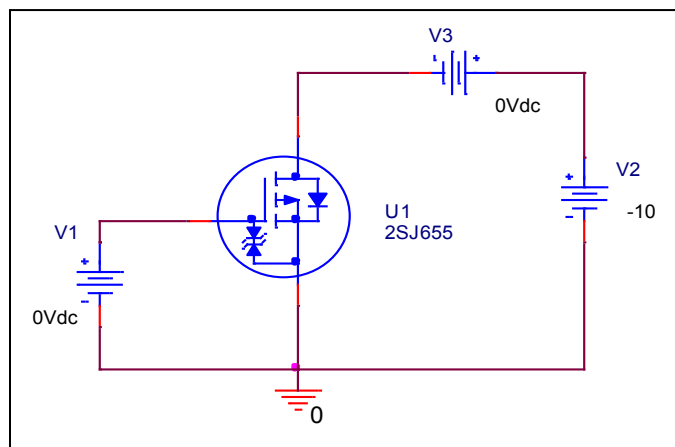
-Id(A)	gfs(S)		Error (%)
	Measurement	Simulation	
1	6.000	6.188	3.13
2	8.300	8.533	2.81
5	12.500	12.853	2.82
10	17.000	17.254	1.49

V_{gs}-I_d Characteristic

Circuit Simulation result

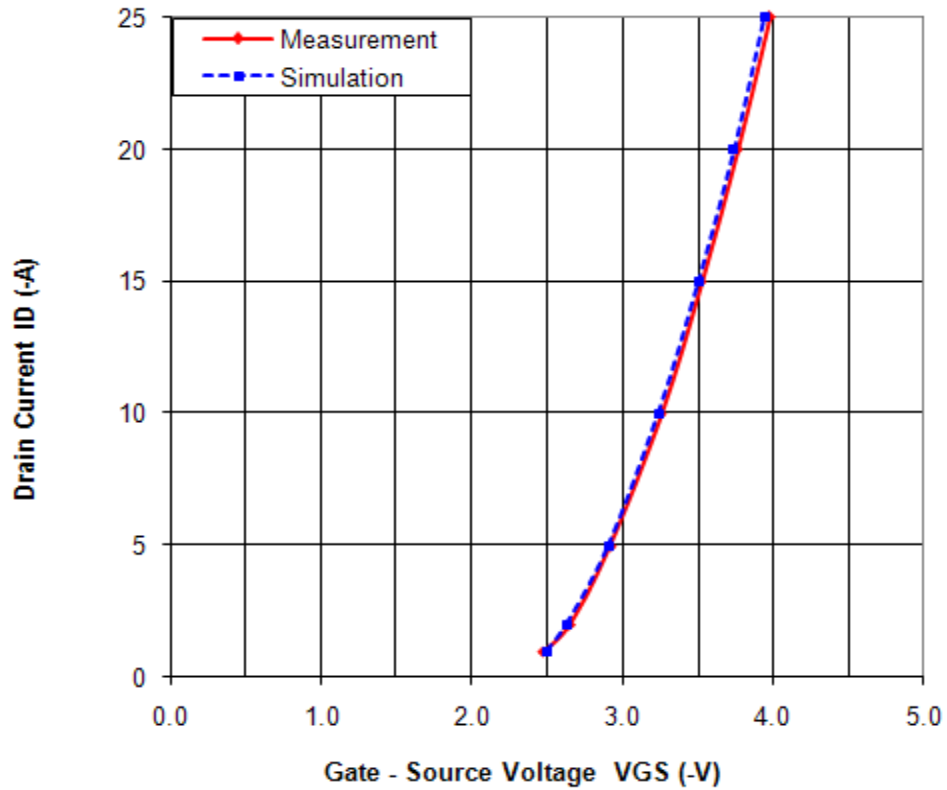


Evaluation circuit



Comparison Graph

Circuit Simulation Result

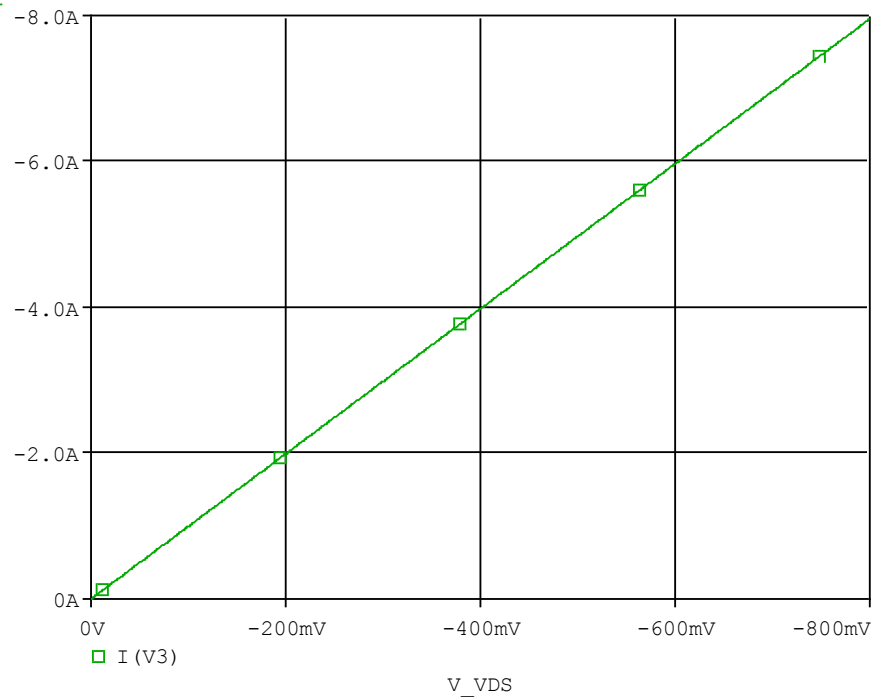


Simulation Result

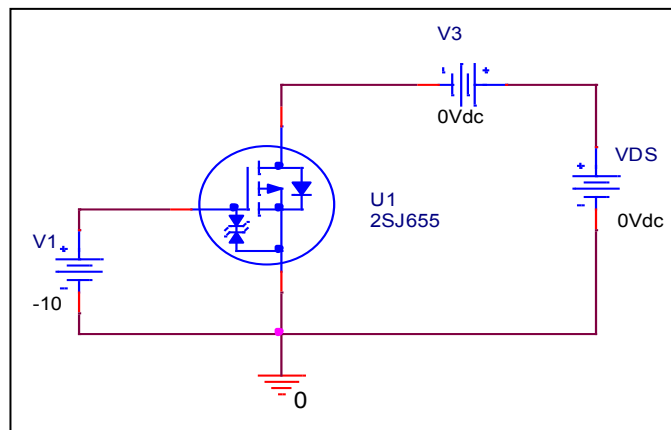
$-I_D(A)$	$-V_{GS}(V)$		Error (%)
	Measurement	Simulation	
1	2.475	2.495	0.81
2	2.650	2.631	-0.72
5	2.920	2.910	-0.34
10	3.260	3.241	-0.58
15	3.525	3.506	-0.54
20	3.765	3.738	-0.72
25	3.980	3.948	-0.80

R_{ds(on)} Characteristic

Circuit Simulation result



Evaluation circuit

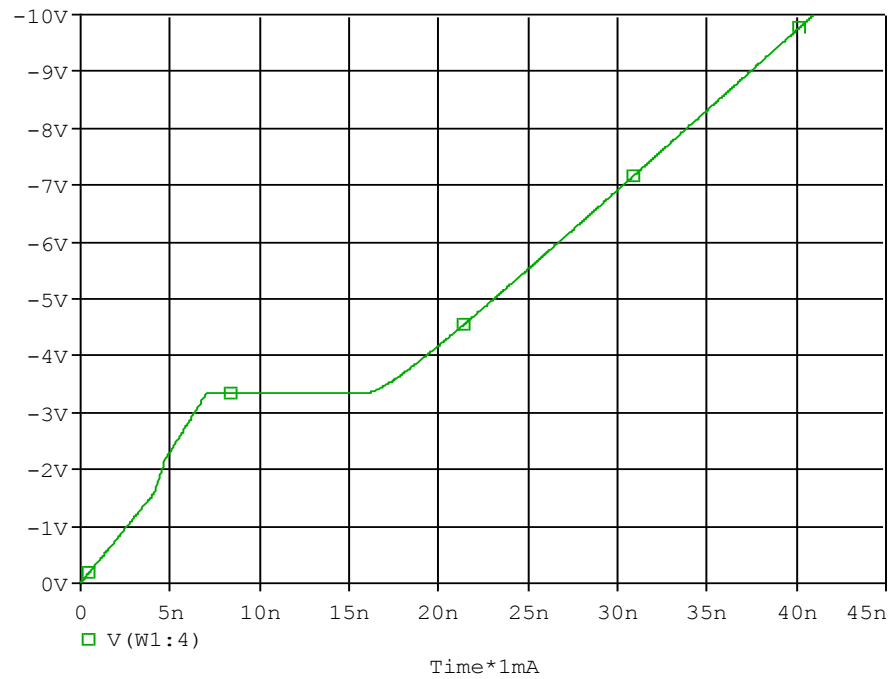


Simulation Result

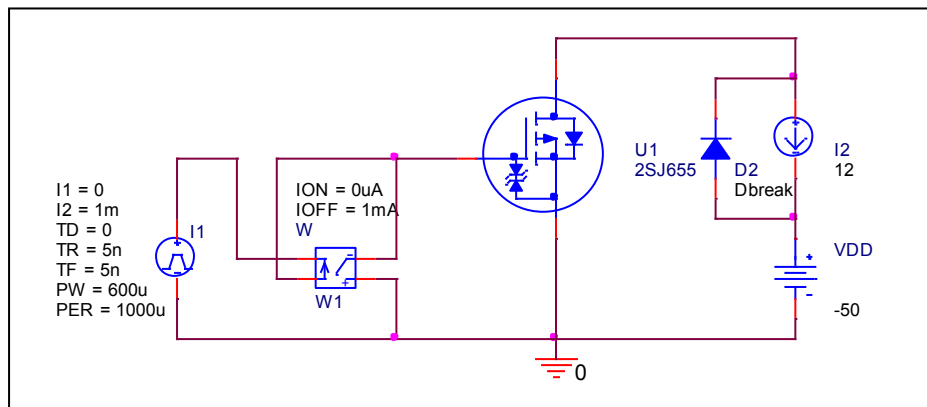
$I_D = -6A, V_{GS} = -10V$		Measurement	Simulation	Error (%)
$R_{DS(on)}$	m Ω	100.000	100.581	0.58

Gate Charge Characteristic

Circuit Simulation result



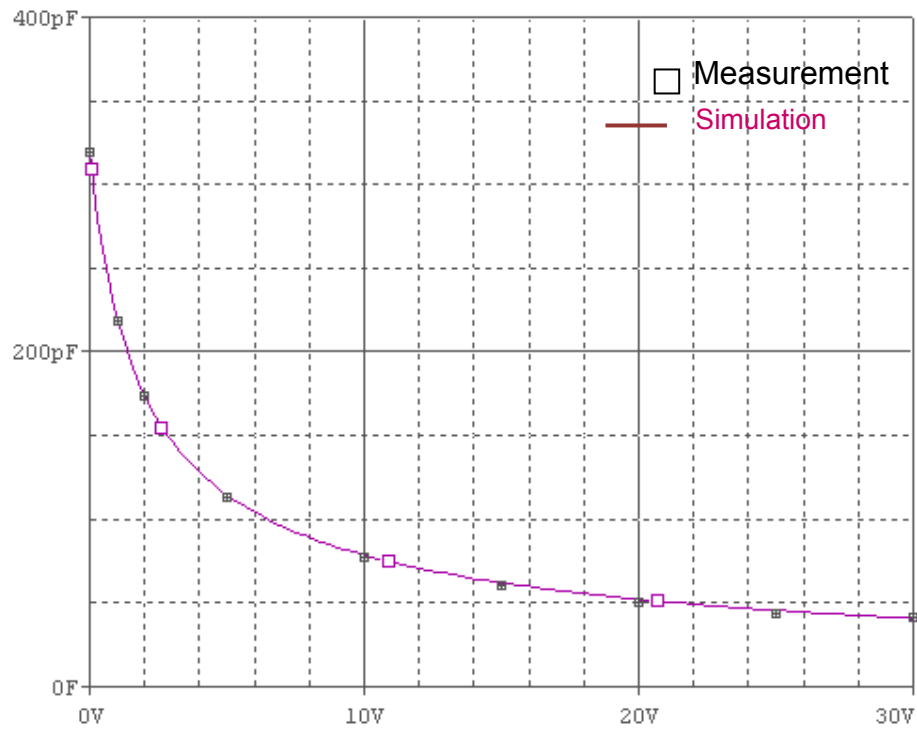
Evaluation circuit



Simulation Result

$V_{DD} = -50V, I_D = -12A$ $V_{GS} = -10V$		Measurement	Simulation	Error (%)
Qgs	nC	7.000	7.086	1.23
Qgd	nC	9.000	9.094	1.04
Qg	nC	41.000	41.024	0.06

Capacitance Characteristic

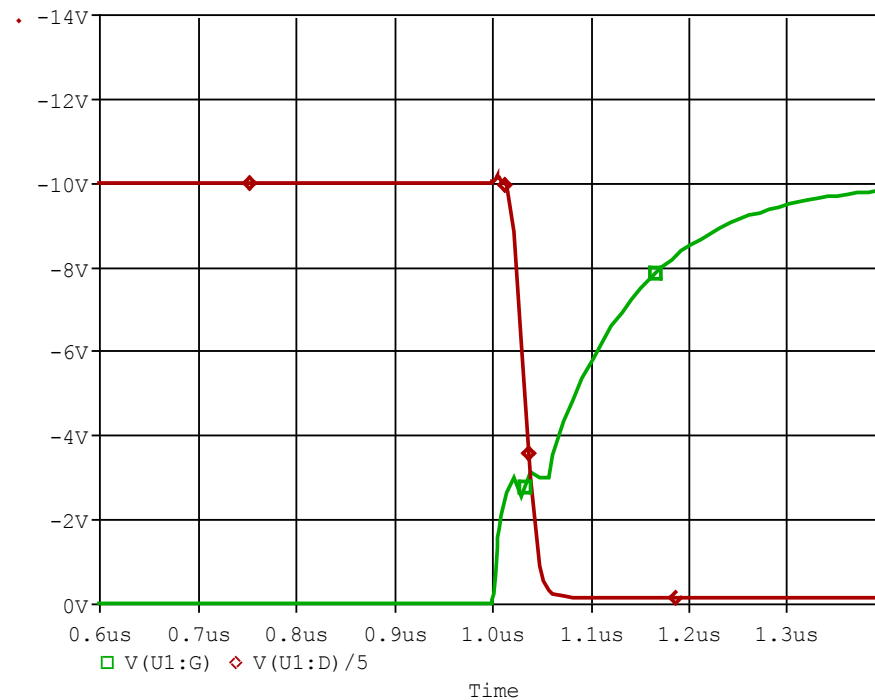


Simulation Result

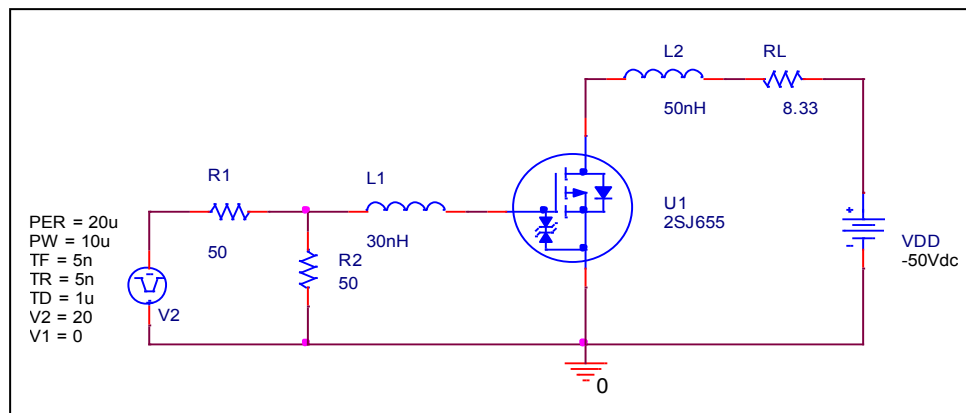
V_{SD} (V)	Cbd(pF)		Error(%)
	Measurement	Simulation	
1.0	220.000	221.000	0.45
2.0	175.000	174.000	-0.57
5.0	114.000	115.000	0.88
10.0	78.000	79.000	1.28
15.0	62.500	62.000	-0.80
20.0	52.000	52.500	0.96
25.0	45.000	45.800	1.78
30.0	42.000	41.200	-1.90

Switching Time Characteristic

Circuit Simulation result



Evaluation circuit

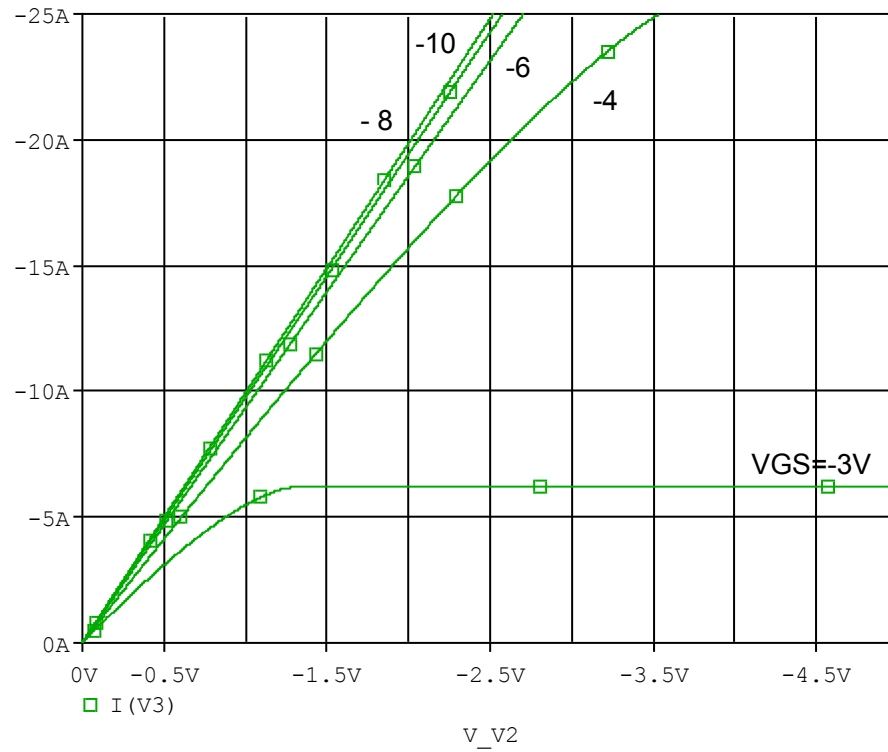


Simulation Result

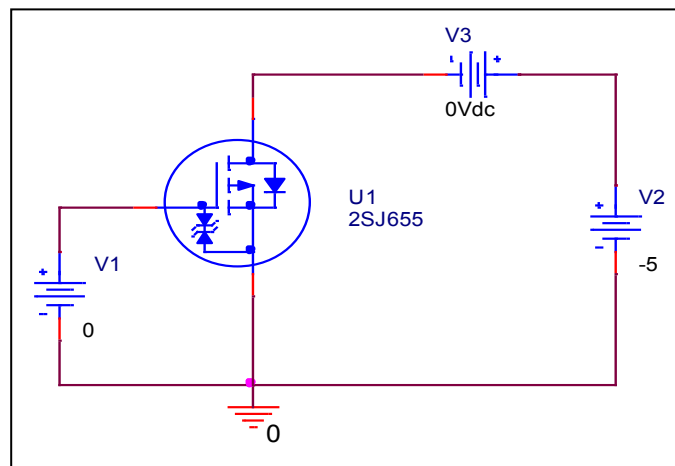
$I_D = -6A$, $V_{DD} = -50V$ $V_{GS} = 0/-10V$		Measurement	Simulation	Error(%)
$t_d(on)$	ns	17.000	17.276	1.62

Output Characteristic

Circuit Simulation result

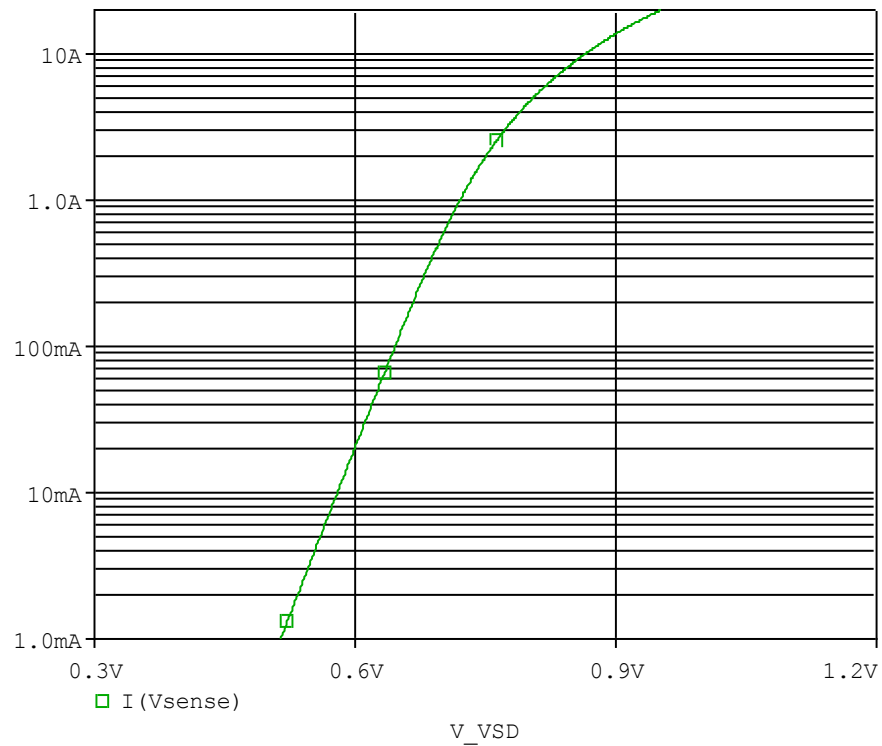


Evaluation circuit

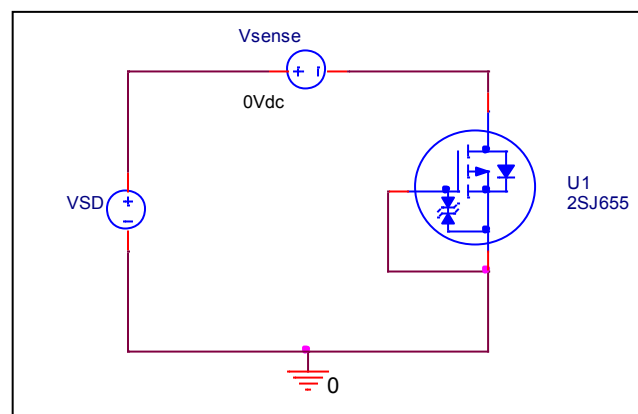


BODY DIODE SPICE MODEL Forward Current Characteristic

Circuit Simulation Result

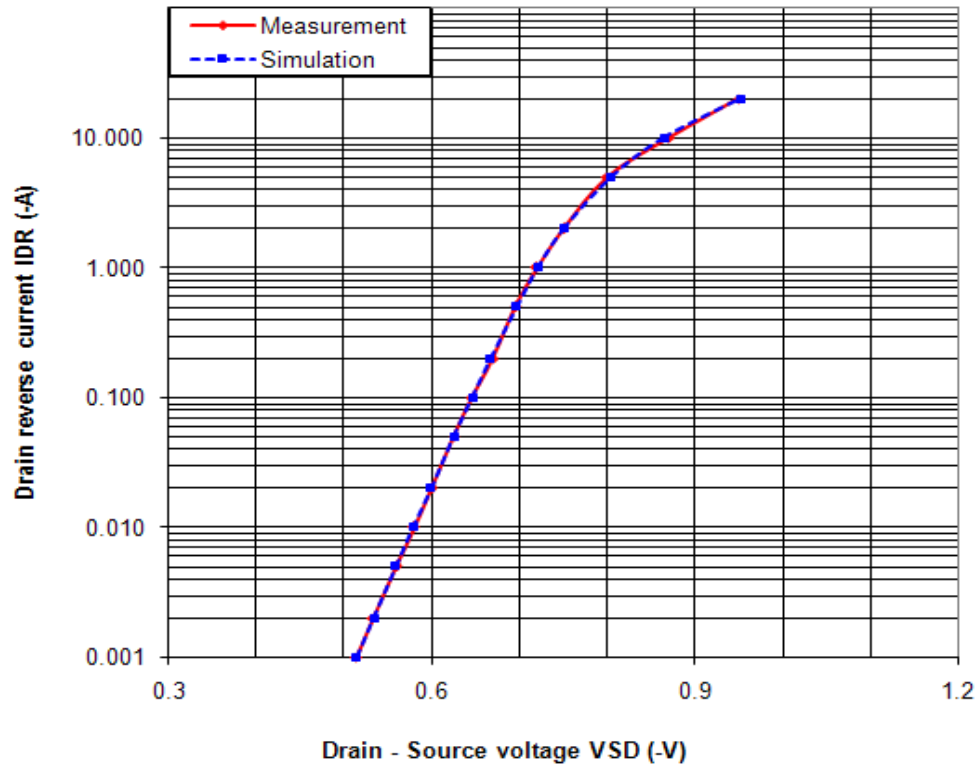


Evaluation Circuit



Comparison Graph

Circuit Simulation Result

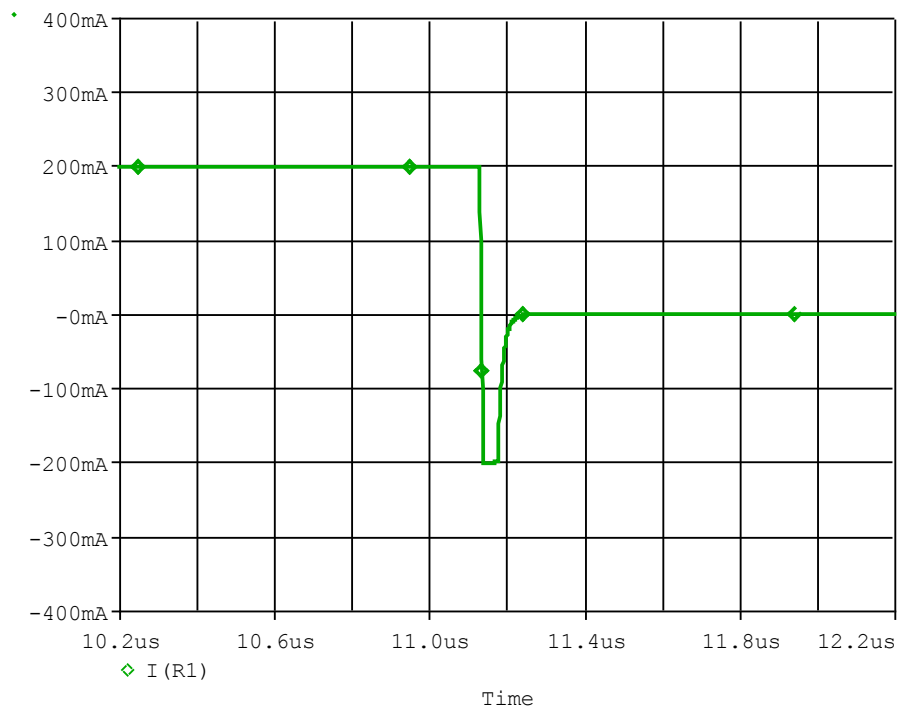


Simulation Result

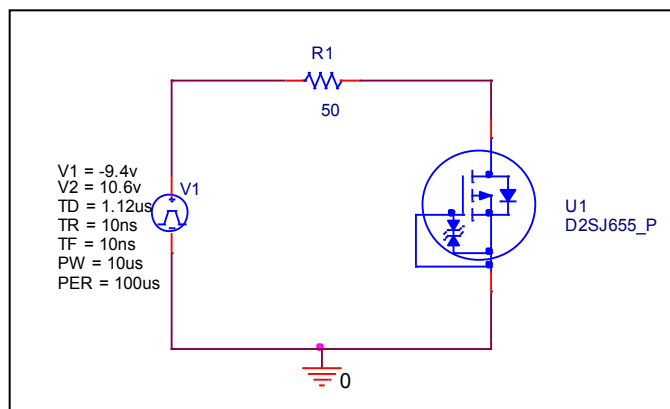
IDR(-A)	VSD (-V)		%Error
	Measurement	Simulation	
0.001	0.514	0.514	0.02
0.010	0.580	0.579	-0.17
0.100	0.645	0.646	0.16
1.000	0.720	0.721	0.14
2.000	0.750	0.750	0.04
5.000	0.800	0.804	0.50
10.000	0.870	0.865	-0.57
20.000	0.950	0.952	0.21

Reverse Recovery Characteristic

Circuit Simulation Result



Evaluation Circuit

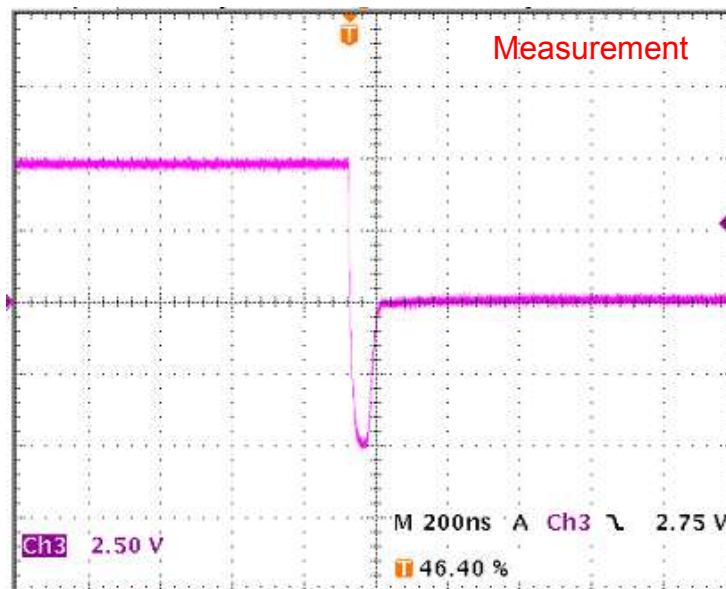


Compare Measurement vs. Simulation

		Measurement	Simulation	Error (%)
trj	ns	40.000	40.573	1.43
trb	ns	30.000	30.043	0.14
trr	ns	70.000	70.616	0.88

Reverse Recovery Characteristic

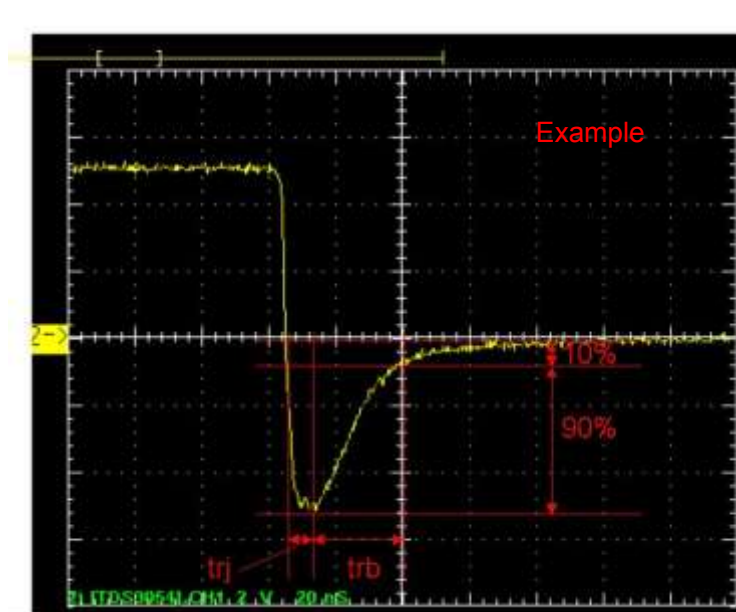
Reference



Trj= 40.00 (ns)

Trb= 30.00 (ns)

Conditions: Ifwd=Irev=0.2(A), RI=50

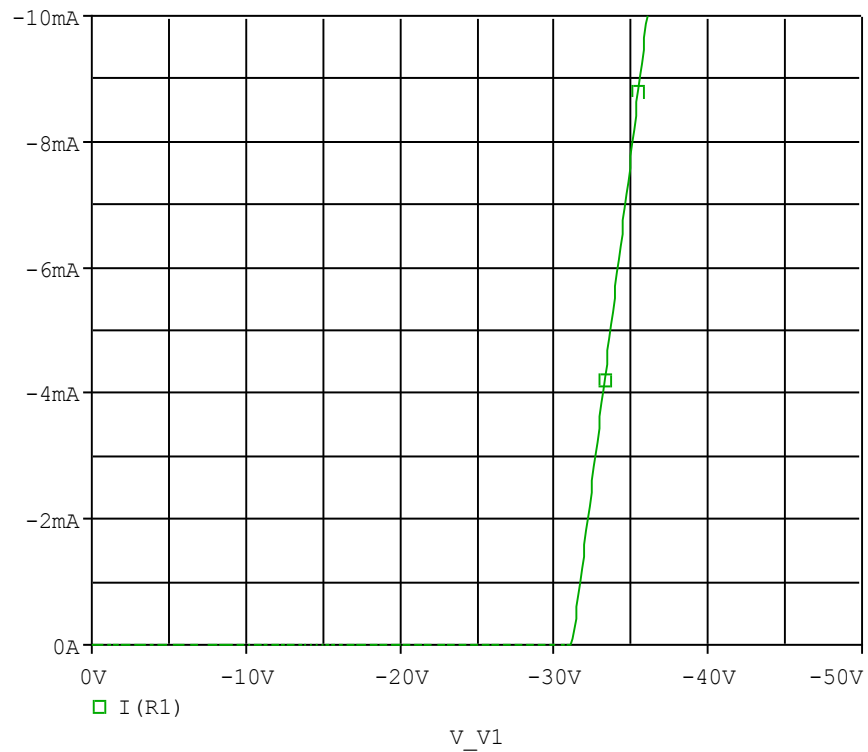


Relation between trj and trb

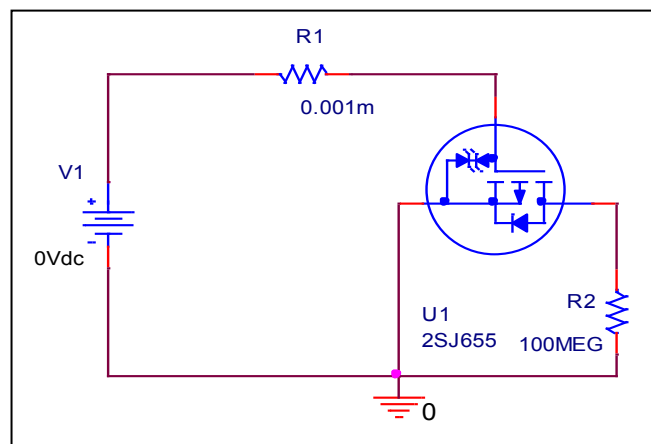
ESD PROTECTION DIODE SPICE MODEL

Zener Voltage Characteristic

Circuit Simulation Result



Evaluation Circuit



Zener Voltage Characteristic

Reference

