

# **Device Modeling Report**

COMPONENTS: Power MOSFET (Professional)  
PART NUMBER: 2SK2700  
MANUFACTURER: TOSHIBA  
Body Diode (Professional) / ESD Protection Diode

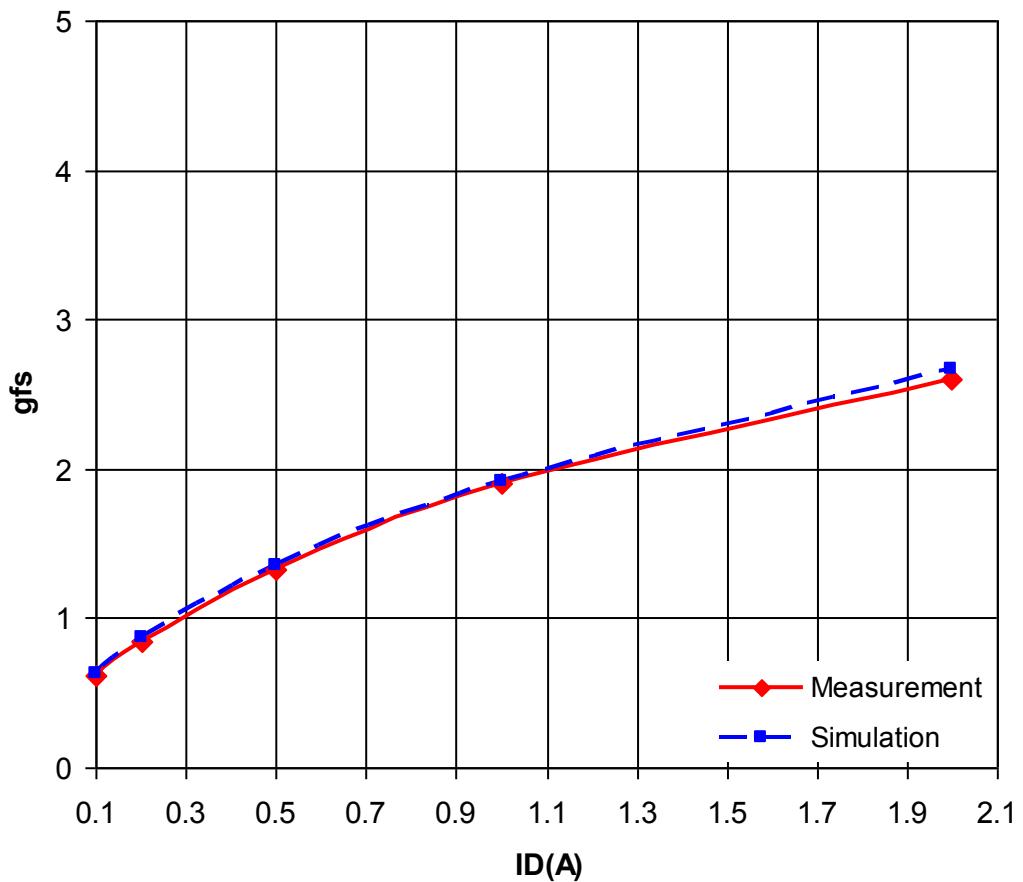


## MOSFET MODEL

PSpice model parameter	Model description
LEVEL	
L	Channel Length
W	Channel Width
KP	Transconductance
RS	Source Ohmic Resistance
RD	Ohmic Drain Resistance
VTO	Zero-bias Threshold Voltage
RDS	Drain-Source Shunt Resistance
TOX	Gate Oxide Thickness
CGSO	Zero-bias Gate-Source Capacitance
CGDO	Zero-bias Gate-Drain Capacitance
CBD	Zero-bias Bulk-Drain Junction Capacitance
MJ	Bulk Junction Grading Coefficient
PB	Bulk Junction Potential
FC	Bulk Junction Forward-bias Capacitance Coefficient
RG	Gate Ohmic Resistance
IS	Bulk Junction Saturation Current
N	Bulk Junction Emission Coefficient
RB	Bulk Series Resistance
PHI	Surface Inversion Potential
GAMMA	Body-effect Parameter
DELTA	Width effect on Threshold Voltage
ETA	Static Feedback on Threshold Voltage
THETA	Mobility Modulation
KAPPA	Saturation Field Factor
VMAX	Maximum Drift Velocity of Carriers
XJ	Metallurgical Junction Depth
UO	Surface Mobility

## Transconductance Characteristic

Circuit Simulation Result

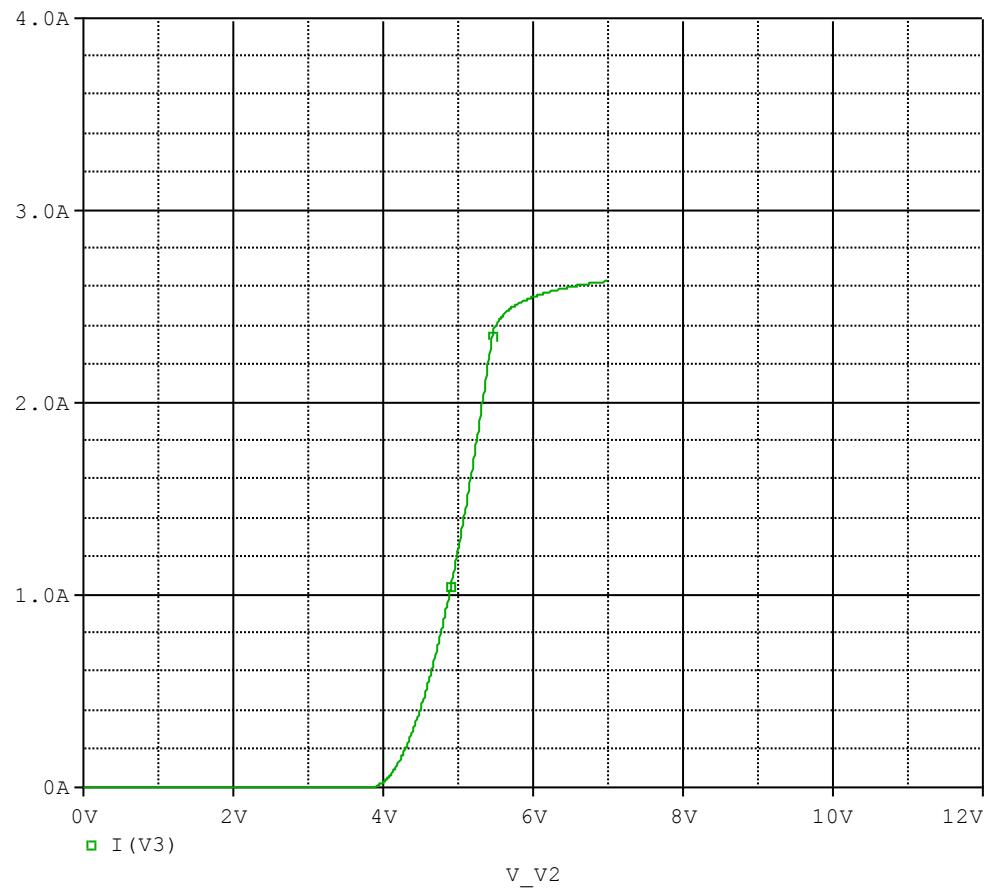


Comparison table

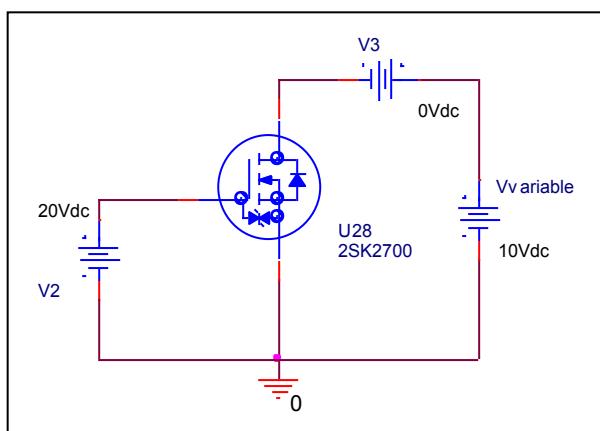
$Id(A)$	$g_{fs}$		Error(%)
	Measurement	Simulation	
0.1	0.610	0.625	2.459
0.2	0.850	0.870	2.353
0.5	1.330	1.351	1.579
1	1.900	1.923	1.211
2	2.600	2.667	2.577

## V<sub>gs</sub>-I<sub>d</sub> Characteristic

Circuit Simulation result

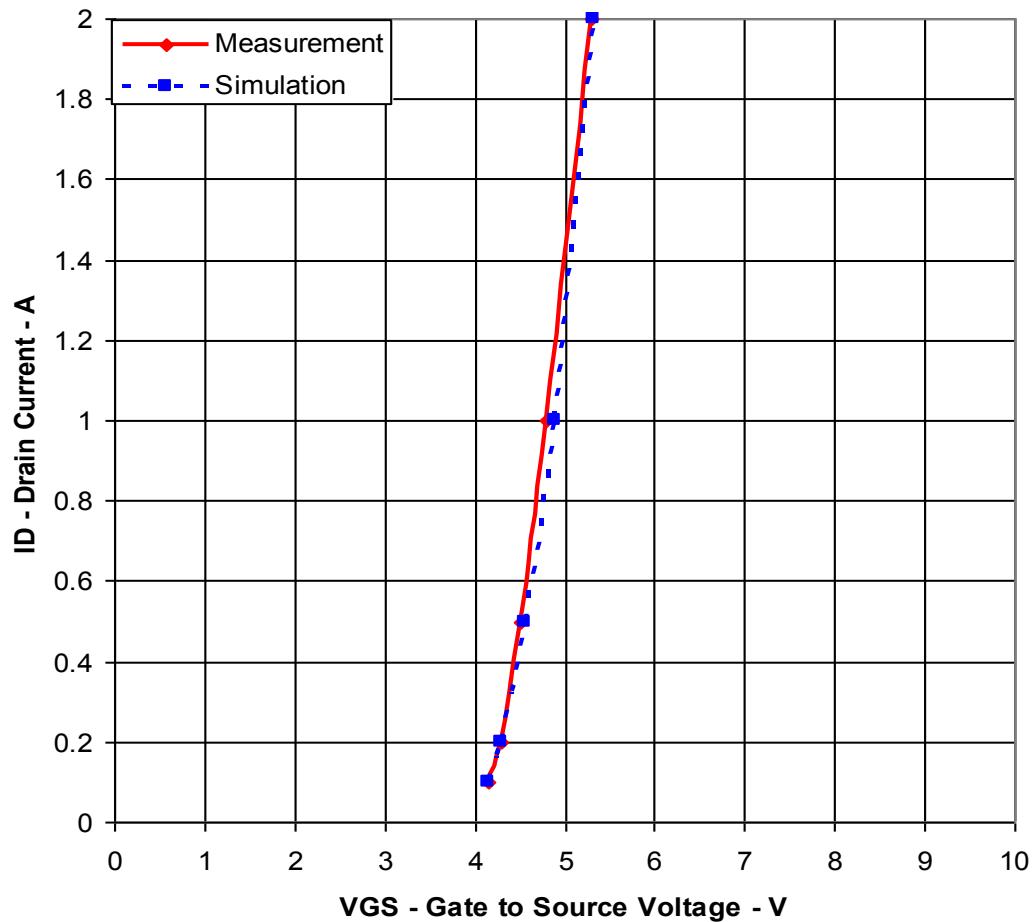


Evaluation circuit



## Comparison Graph

Circuit Simulation Result

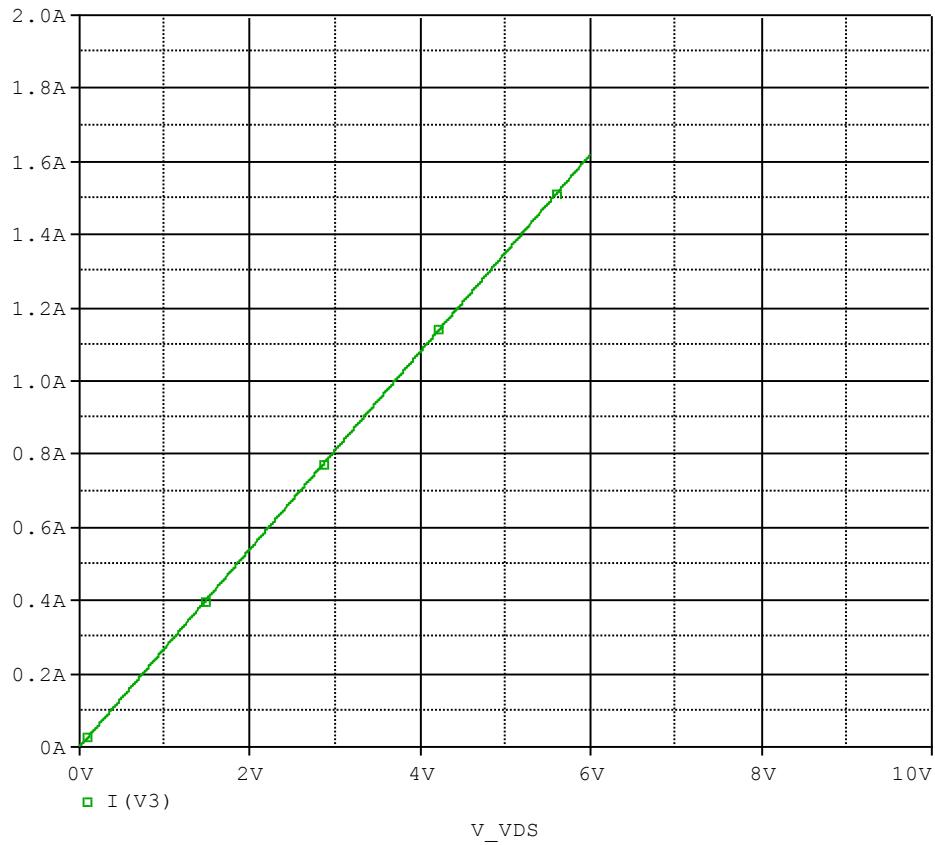


Simulation Result

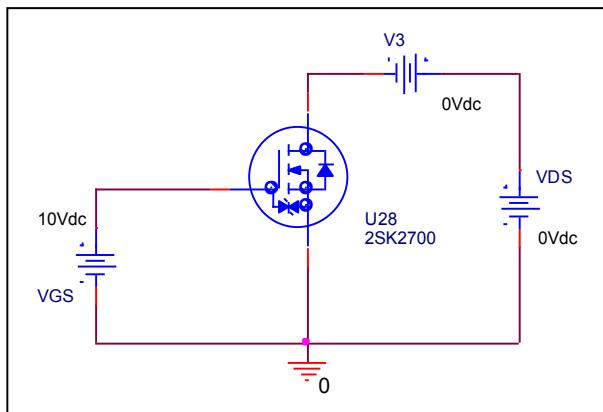
$I_D$ (A)	$V_{GS}$ (V)		Error (%)
	Measurement	Simulation	
0.1	4.150	4.158	0.193
0.2	4.300	4.296	-0.093
0.5	4.500	4.570	1.556
1	4.800	4.881	1.688
2	5.300	5.325	0.472

## Rds(on) Characteristic

### Circuit Simulation result



### Evaluation circuit

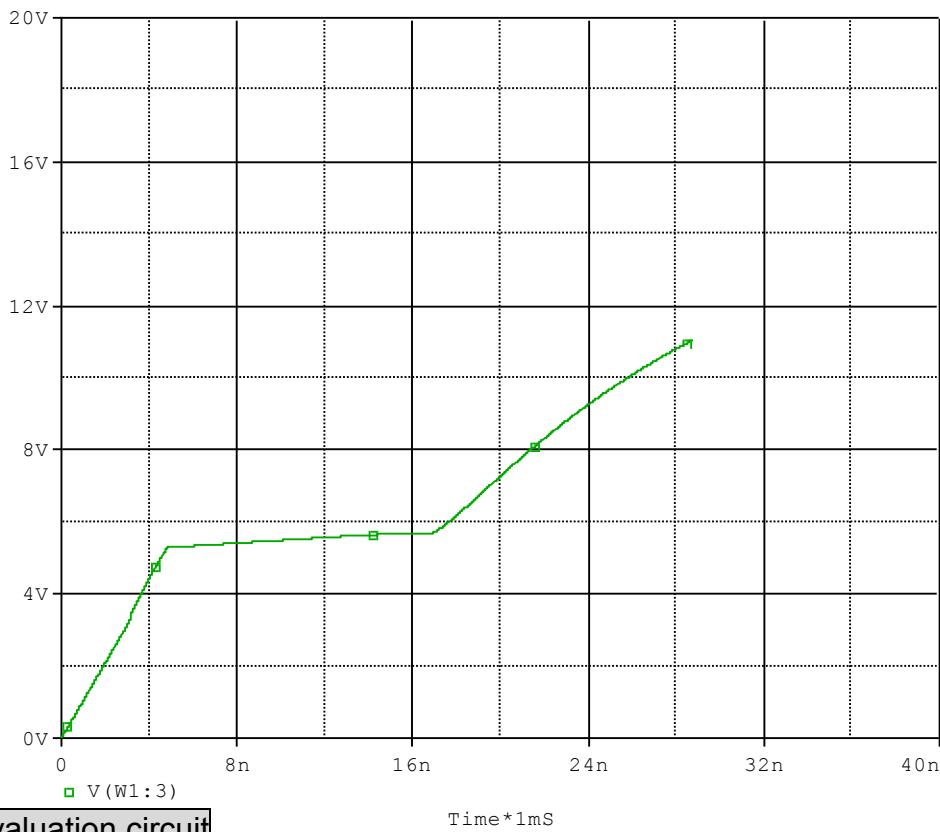


### Simulation Result

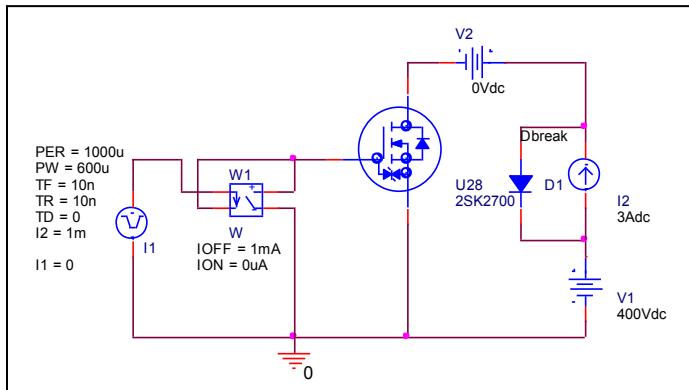
I <sub>D</sub> =1.5A, V <sub>GS</sub> =10V	Measurement		Simulation		Error (%)
R <sub>DS</sub> (on)	0.370	Ω	0.3667	Ω	-0.892

## Gate Charge Characteristic

Circuit Simulation result



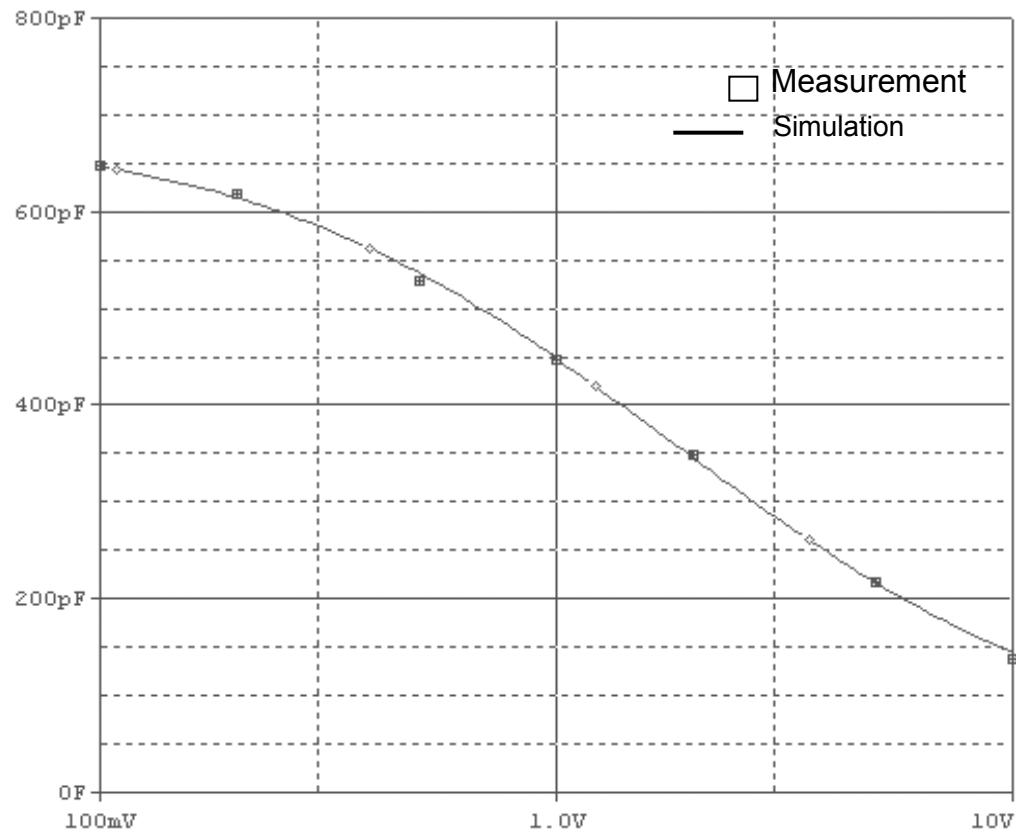
Evaluation circuit



Simulation Result

$V_{DD}=400V, I_D=15A, V_{GS}=10V$	Measurement	Simulation	Error (%)
$Q_{gs}(nC)$	5.000	4.912	-1.760
$Q_{gd}(nC)$	12.000	11.938	-0.517
$Q_g$	25.500	25.775	1.078

## Capacitance Characteristic

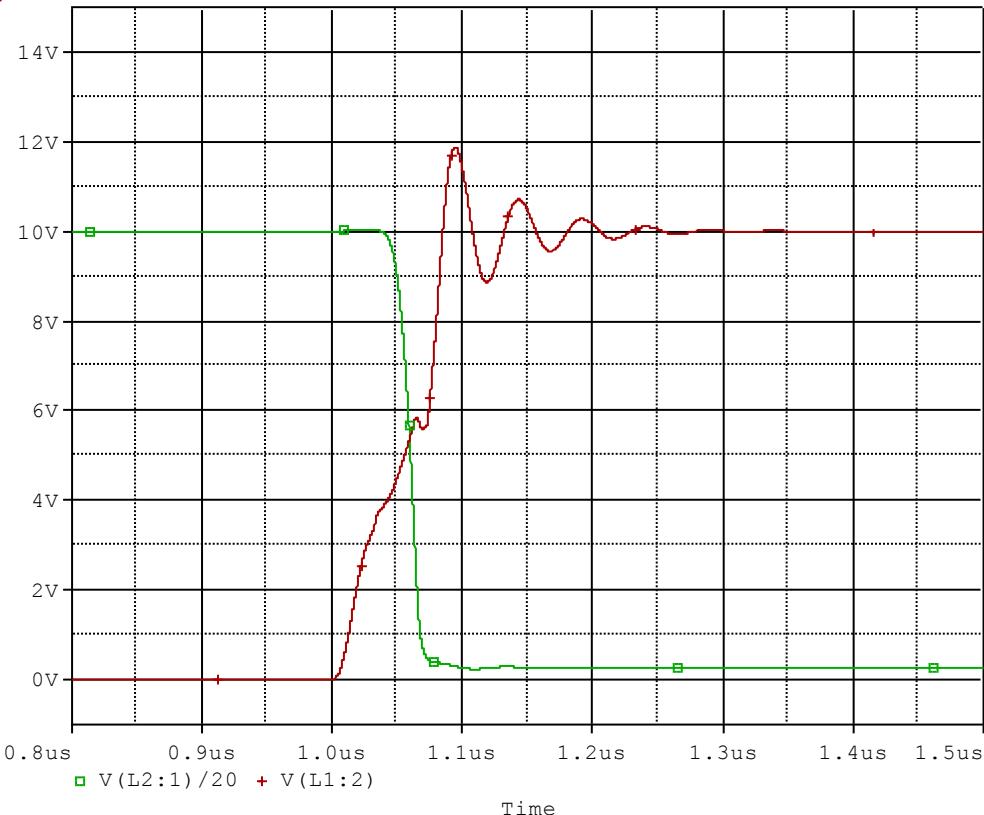


**Simulation Result**

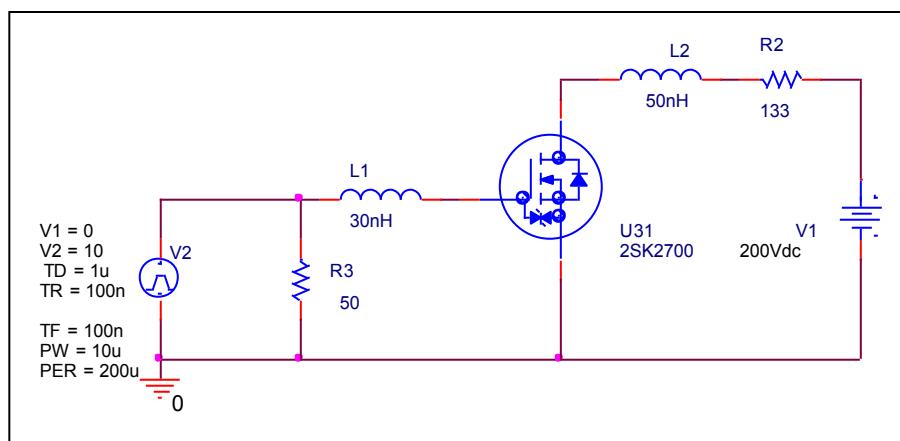
$V_{ds}(V)$	$C_{bd}(pF)$		Error(%)
	Measurement	Simulation	
0.1	650	650	0.000
0.2	620	616	-0.645
0.5	530	535	0.943
1	450	451	0.222
2	350	348	-0.571
5	220	221	0.455
10	140	142	1.429

## Switching Time Characteristic

Circuit Simulation result



Evaluation circuit

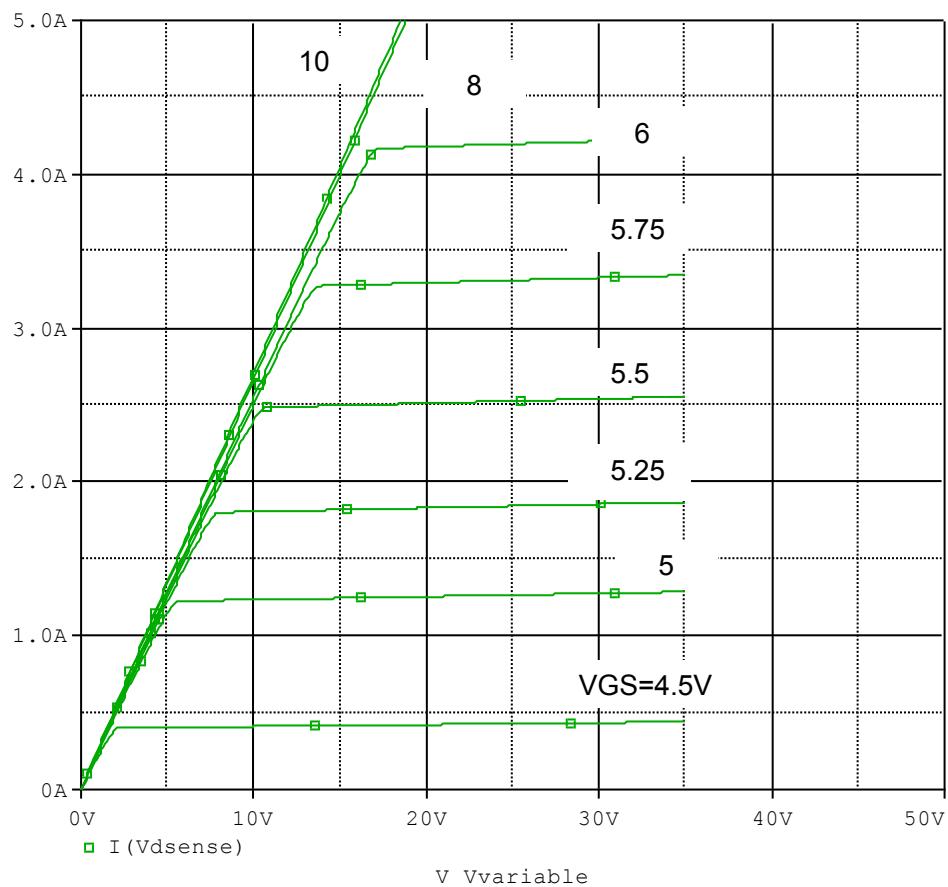


Simulation Result

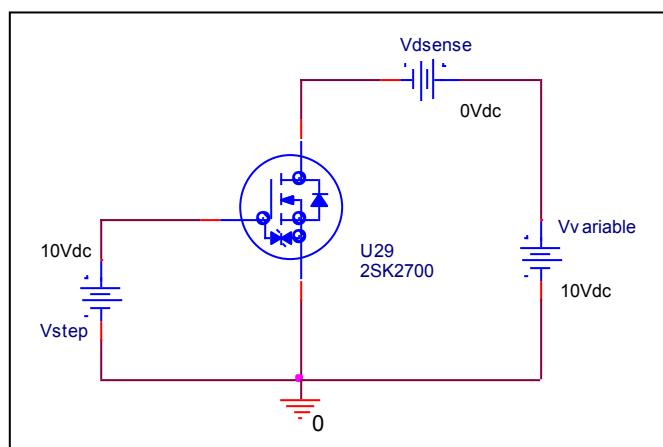
$I_D=7A$ , $V_{DD}=210V$ $V_{GS}=0/10V$	Measurement	Simulation	Error(%)
Ton(ns)	55.000	55.118	0.215

## Output Characteristic

Circuit Simulation result

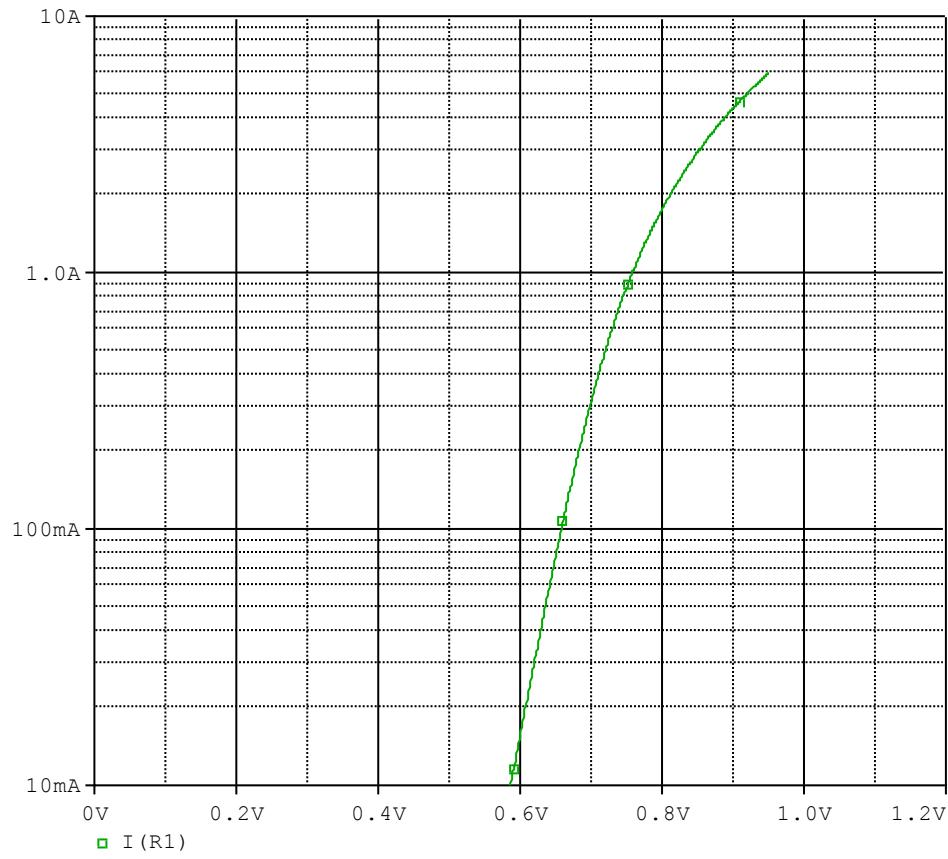


Evaluation circuit

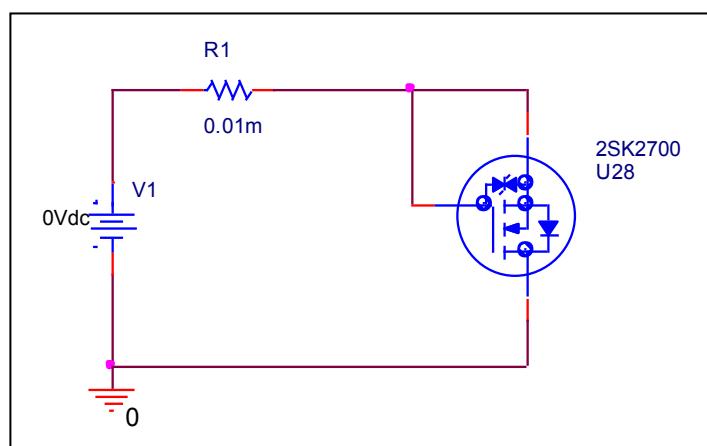


## Forward Current Characteristic

Circuit Simulation Result

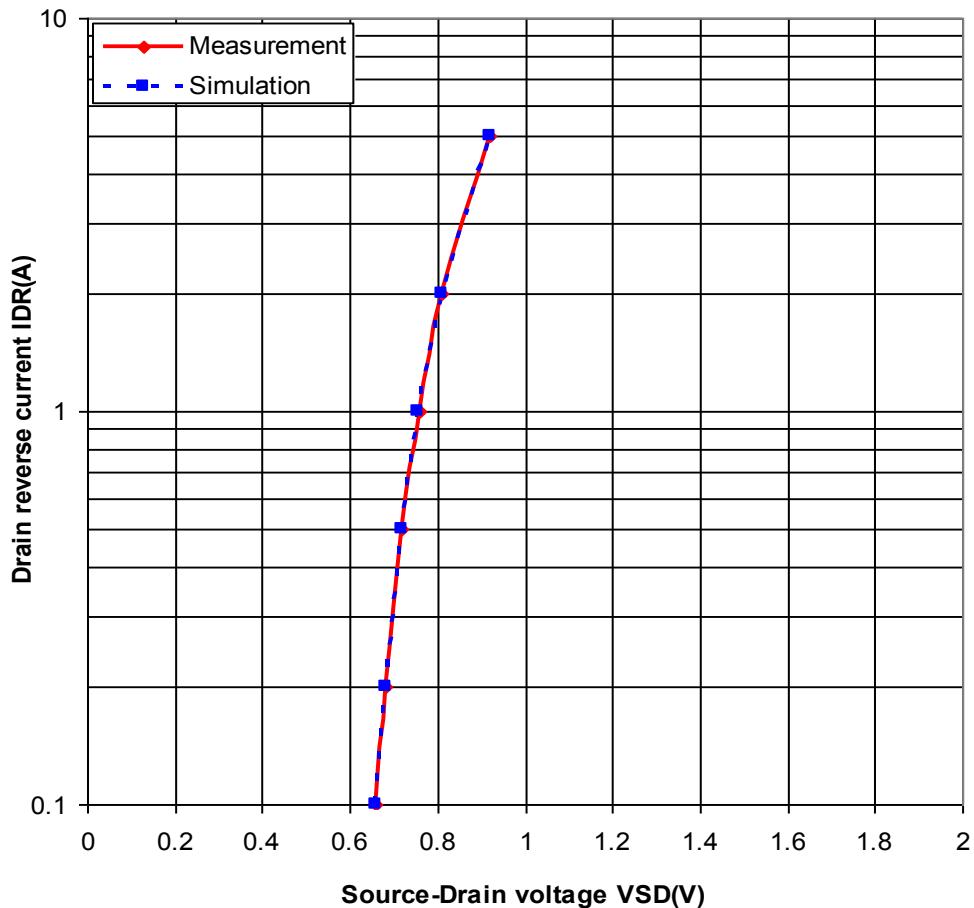


Evaluation Circuit



## Comparison Graph

Circuit Simulation Result

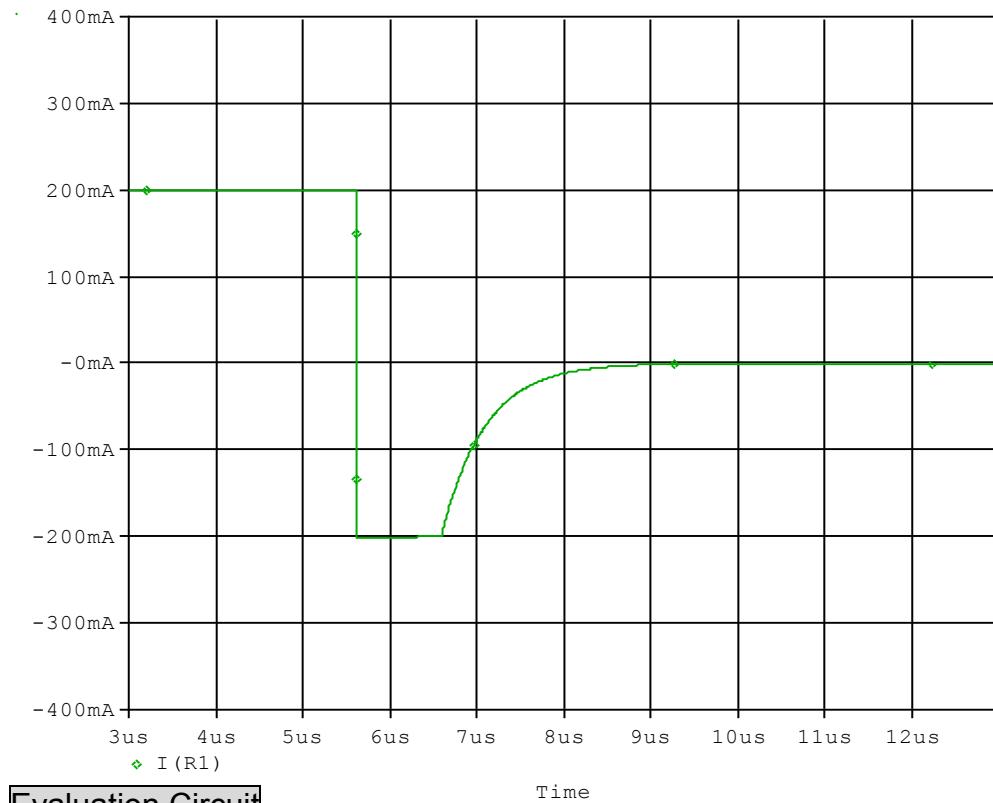


Simulation Result

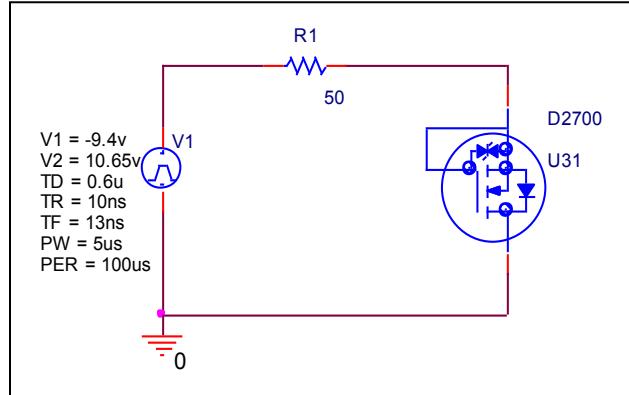
IDR(A)	VSD(V)		%Error
	Measuremen	Simulation	
0.01	0.590	0.586	-0.678
0.02	0.600	0.607	1.167
0.05	0.640	0.636	-0.625
0.1	0.660	0.658	-0.303
0.2	0.680	0.682	0.294
0.5	0.720	0.720	0.000
1	0.760	0.757	-0.395
2	0.810	0.810	0.000
5	0.920	0.920	0.000

## Reverse Recovery Characteristic

**Circuit Simulation Result**



**Evaluation Circuit**

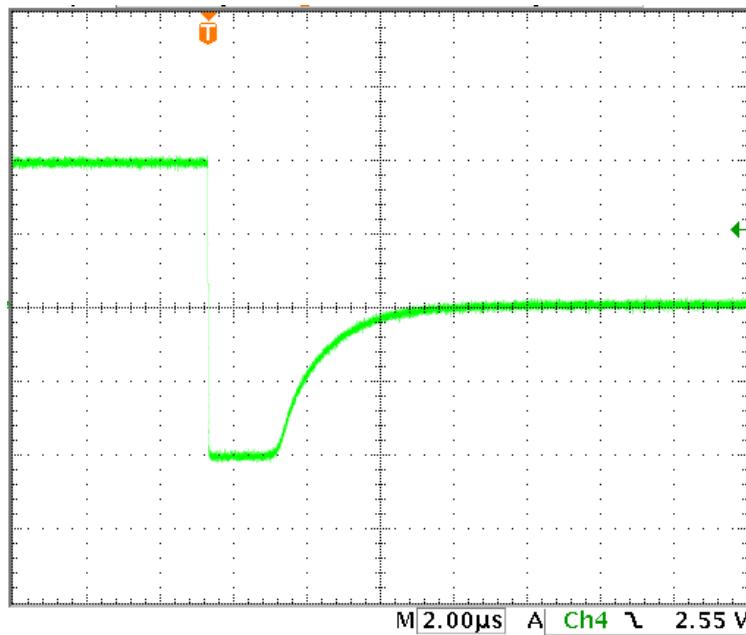


### Compare Measurement vs. Simulation

	<b>Measurement</b>	<b>Simulation</b>	<b>Error (%)</b>
<b>Trj(<math>\mu</math>s)</b>	<b>0.975</b>	<b>0.975</b>	<b>0</b>
<b>Trb(<math>\mu</math>s)</b>	<b>1.180</b>	<b>1.180</b>	<b>0</b>
<b>Trr(<math>\mu</math>s)</b>	<b>2.155</b>	<b>2.155</b>	<b>0</b>

## Reverse Recovery Characteristic

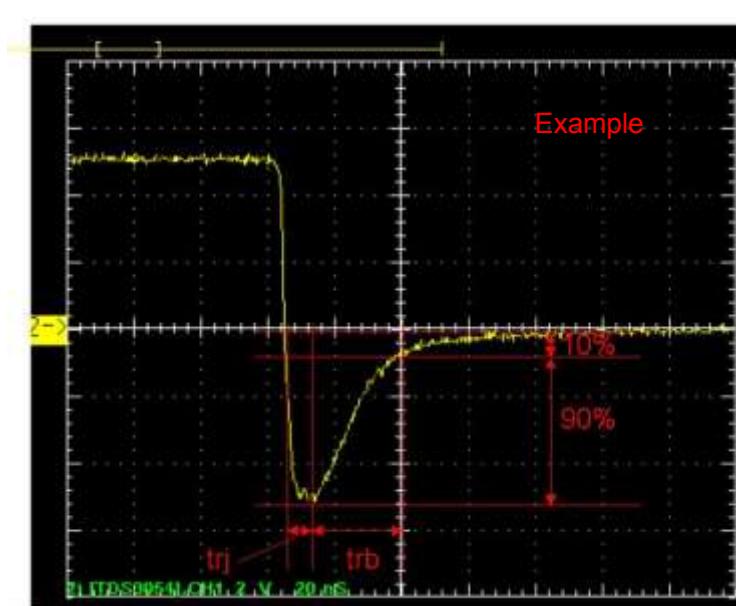
Reference



Trj=0.975 (us)

Trb=1.180(us)

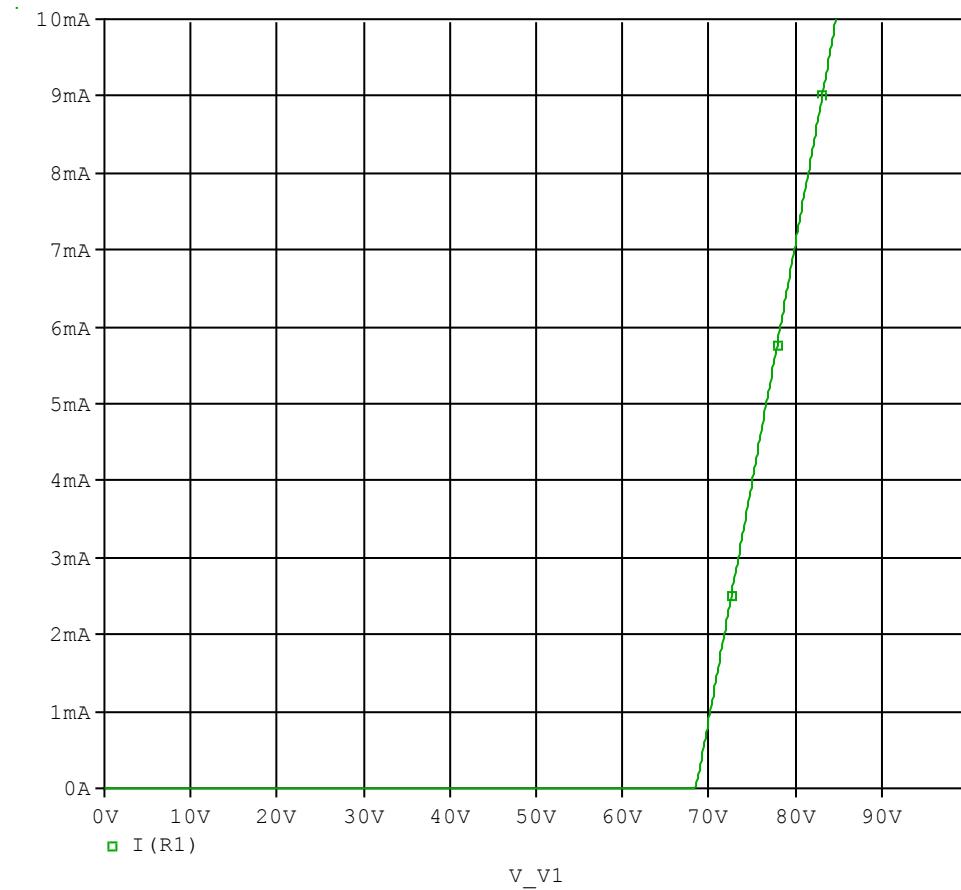
Conditions: Ifwd=Irev=0.2(A), RI=50



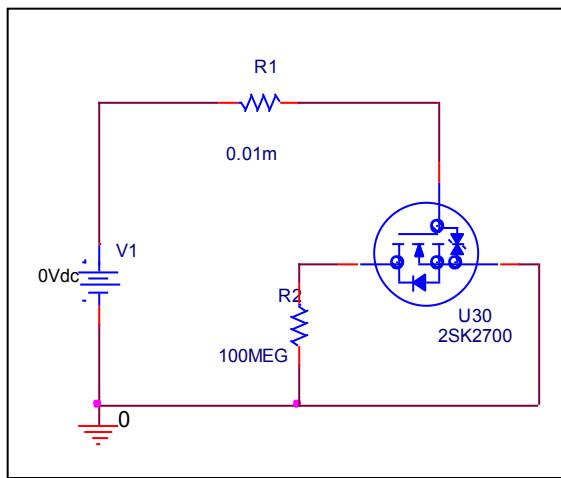
Relation between trj and trb

## Zener Voltage Characteristic

### Circuit Simulation Result



### Evaluation Circuit



## Zener Voltage Characteristic

## Reference

