

# **Device Modeling Report**

**COMPONENTS:** Power MOSFET (Model parameter)

**PART NUMBER:** 2SK2989

**MANUFACTURER:** TOSHIBA

Body Diode (Model parameter) / ESD Protection Diode



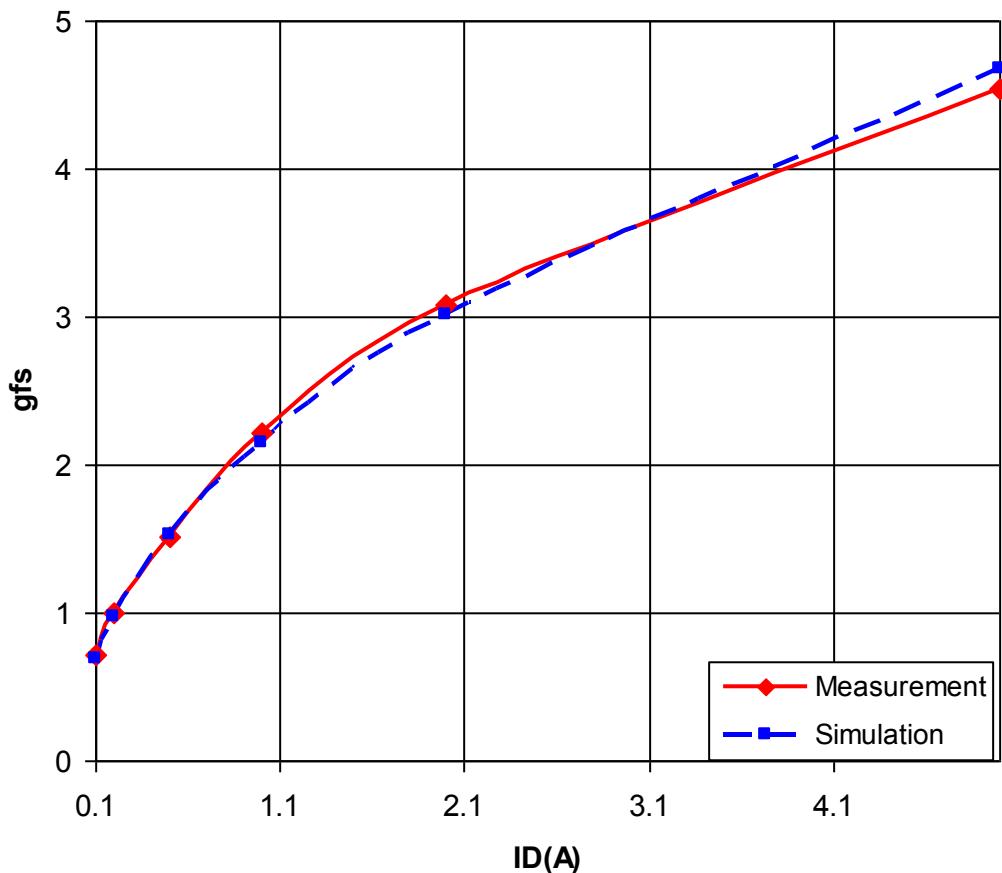
**Bee Technologies Inc.**

## MOSFET MODEL

PSpice model parameter	Model description
LEVEL	
L	Channel Length
W	Channel Width
KP	Transconductance
RS	Source Ohmic Resistance
RD	Ohmic Drain Resistance
VTO	Zero-bias Threshold Voltage
RDS	Drain-Source Shunt Resistance
TOX	Gate Oxide Thickness
CGSO	Zero-bias Gate-Source Capacitance
CGDO	Zero-bias Gate-Drain Capacitance
CBD	Zero-bias Bulk-Drain Junction Capacitance
MJ	Bulk Junction Grading Coefficient
PB	Bulk Junction Potential
FC	Bulk Junction Forward-bias Capacitance Coefficient
RG	Gate Ohmic Resistance
IS	Bulk Junction Saturation Current
N	Bulk Junction Emission Coefficient
RB	Bulk Series Resistance
PHI	Surface Inversion Potential
GAMMA	Body-effect Parameter
DELTA	Width effect on Threshold Voltage
ETA	Static Feedback on Threshold Voltage
THETA	Mobility Modulation
KAPPA	Saturation Field Factor
VMAX	Maximum Drift Velocity of Carriers
XJ	Metallurgical Junction Depth
UO	Surface Mobility

## Transconductance Characteristic

Circuit Simulation Result

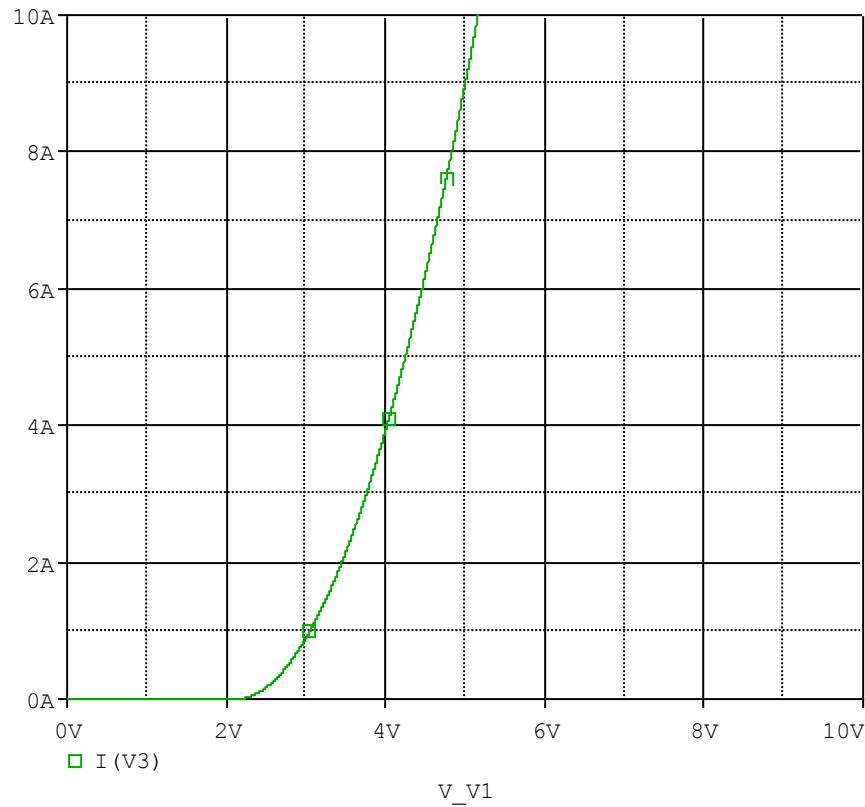


Comparison table

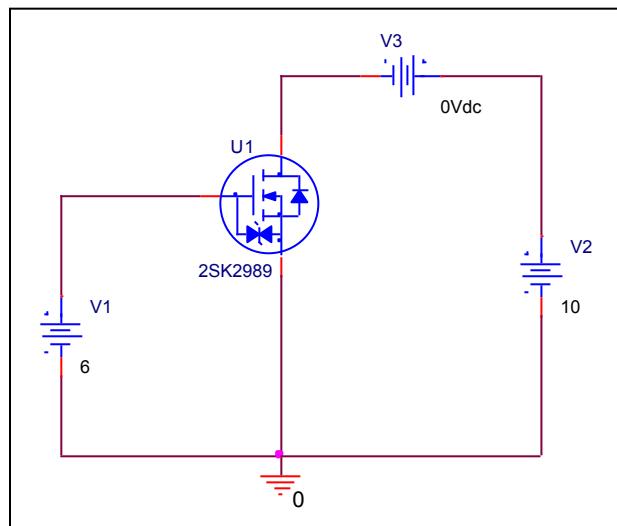
Id(A)	gfs		Error(%)
	Measurement	Simulation	
0.1	0.71429	0.689655	-3.449
0.2	1	0.971	-2.900
0.5	1.515151	1.529	0.914
1	2.2222	2.151	-3.204
2	3.0769	3.012	-2.109
5	4.5455	4.677	2.893

## V<sub>gs</sub>-I<sub>d</sub> Characteristic

Circuit Simulation result

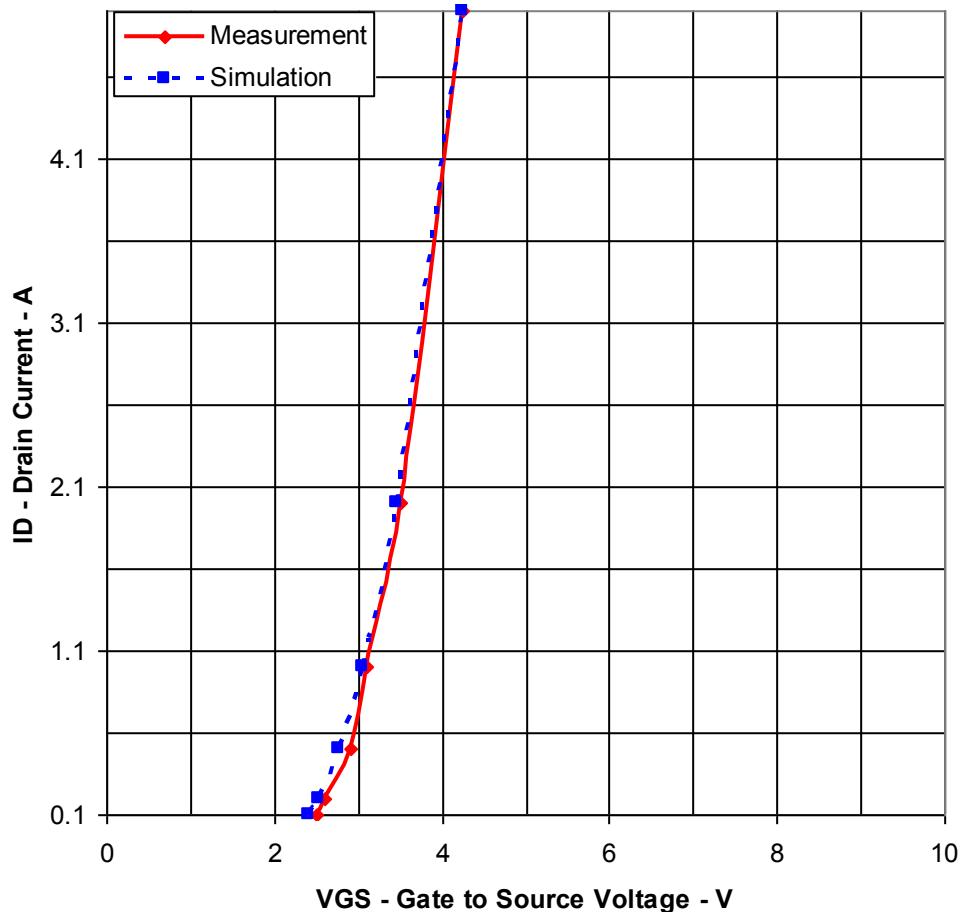


Evaluation circuit



## Comparison Graph

Circuit Simulation Result

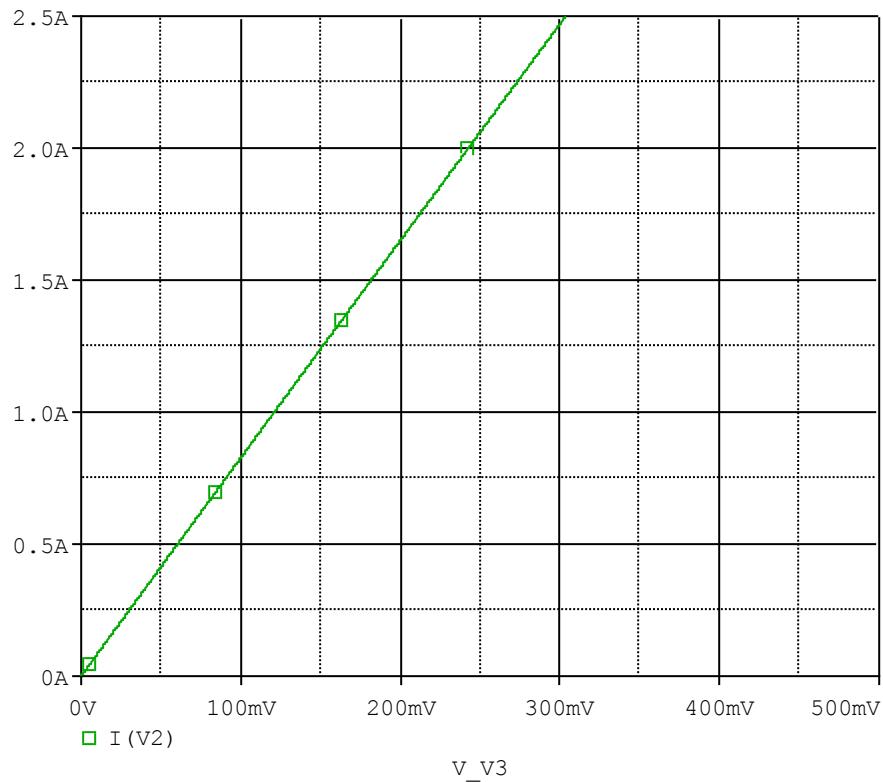


Simulation Result

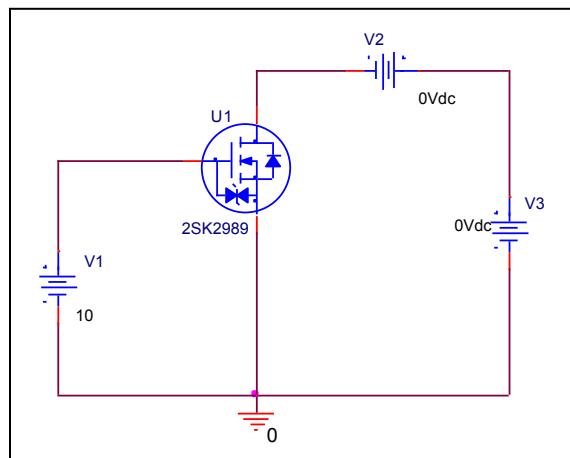
$I_D$ (A)	$V_{GS}$ (V)		Error (%)
	Measurement	Simulation	
0.1	2.5	2.4079	-3.684
0.2	2.6	2.5312	-2.646
0.5	2.9	2.7768	-4.248
1	3.1	3.0553	-1.442
2	3.5	3.452	-1.371
5	4.25	4.2492	-0.019

## Rds(on) Characteristic

Circuit Simulation result



Evaluation circuit

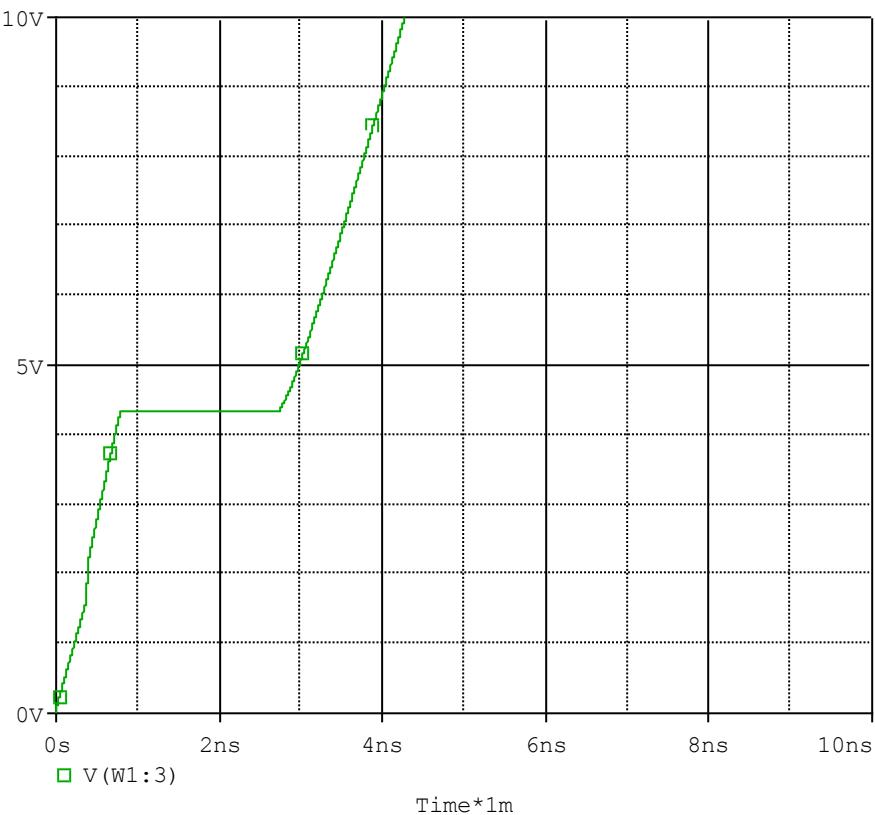


Simulation Result

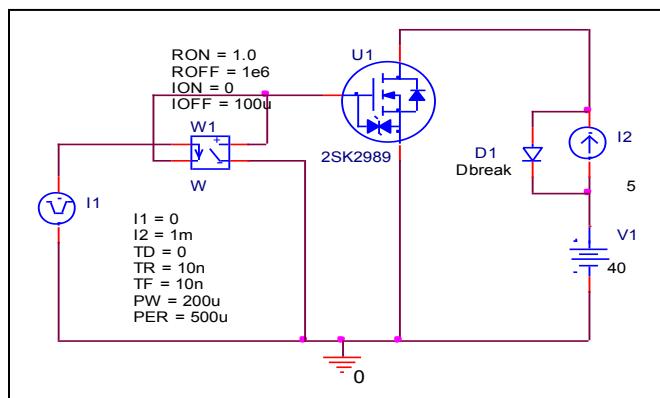
$I_D=2.5A, V_{GS}=10V$	Measurement		Simulation		Error (%)
$R_{DS} \text{ (on)}$	0.12	$\Omega$	0.121	$\Omega$	0.833

## Gate Charge Characteristic

### Circuit Simulation result



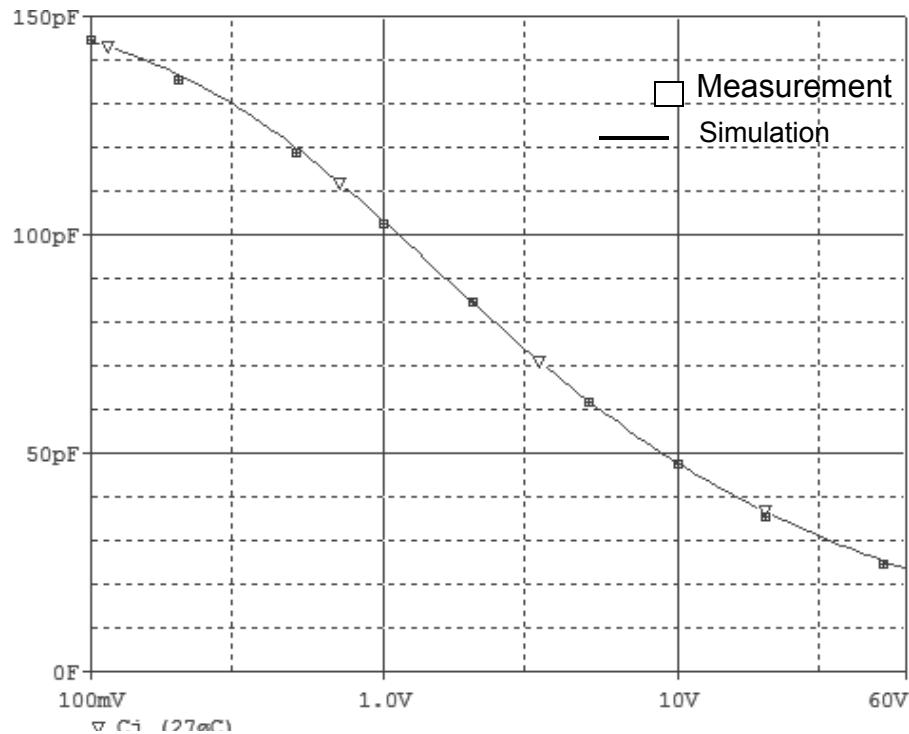
### Evaluation circuit



### Simulation Result

$V_{DD}=80V, I_D=5A$	Measurement		Simulation		Error (%)
$Q_{gs}$	0.8	nC	0.789	nC	-1.375
$Q_{gd}$	1.9	nC	1.9289	nC	1.521
$Q_g$	6.4	nC	4.2721	nC	-33.248

## Capacitance Characteristic

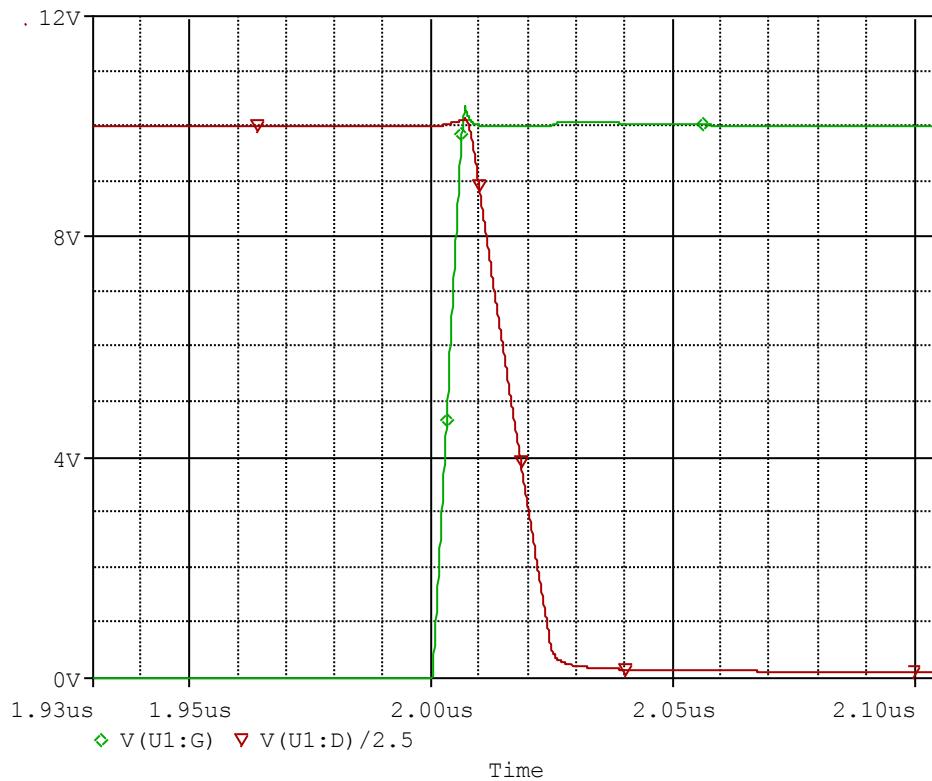


### Simulation Result

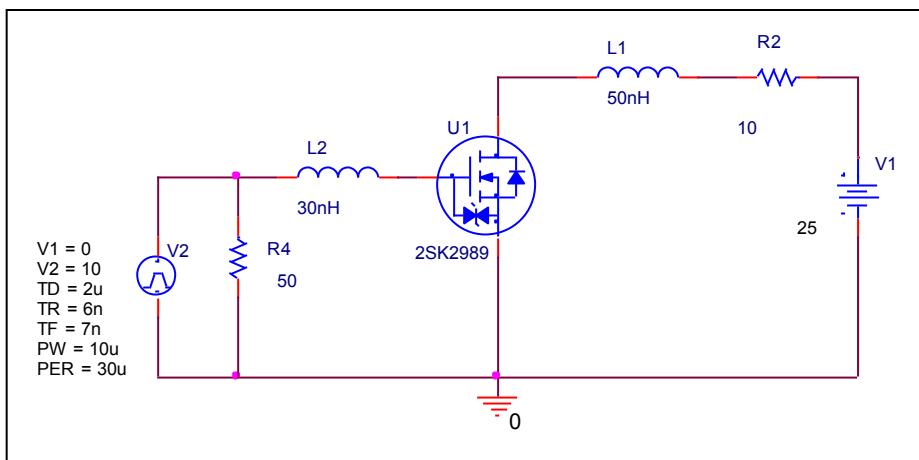
$V_{ds}$ (V)	$C_{bd}$ (pF)		Error(%)
	Measurement	Simulation	
0.1	145	144.3	-0.483
0.2	136	136.5	0.368
0.5	121	120	-0.826
1	103	103	0.000
2	85	84.5	-0.588
5	62	61.8	-0.323
10	48	47.5	-1.042
20	36	36.5	1.389
50	25	25.5	2.000

## Switching Time Characteristic

### Circuit Simulation result



### Evaluation circuit

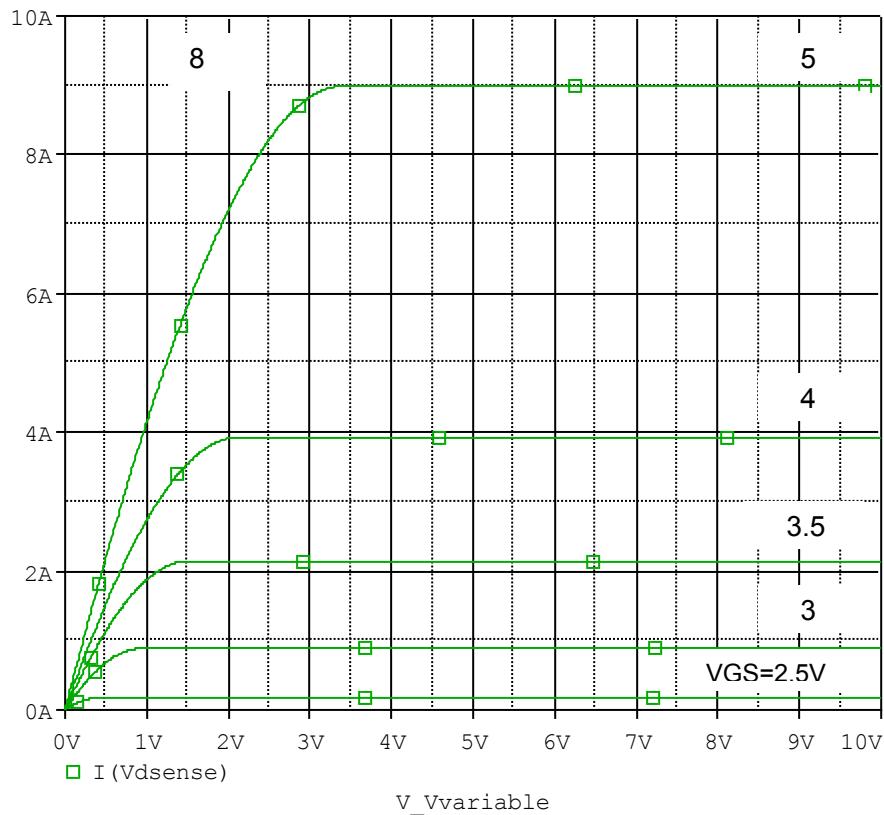


### Simulation Result

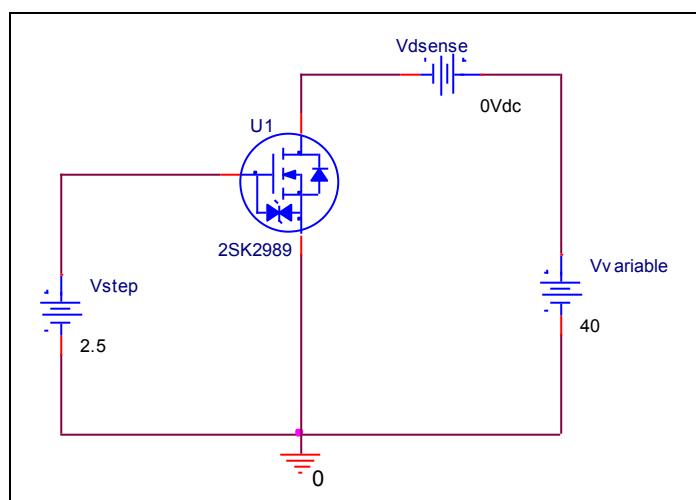
$I_D=2.5\text{ A}$ , $V_{DD}=25\text{V}$ $V_{GS}=0/10\text{V}$	Measurement	Simulation	Error(%)
ton	23.000 ns	22.959 ns	-0.178

## Output Characteristic

Circuit Simulation result

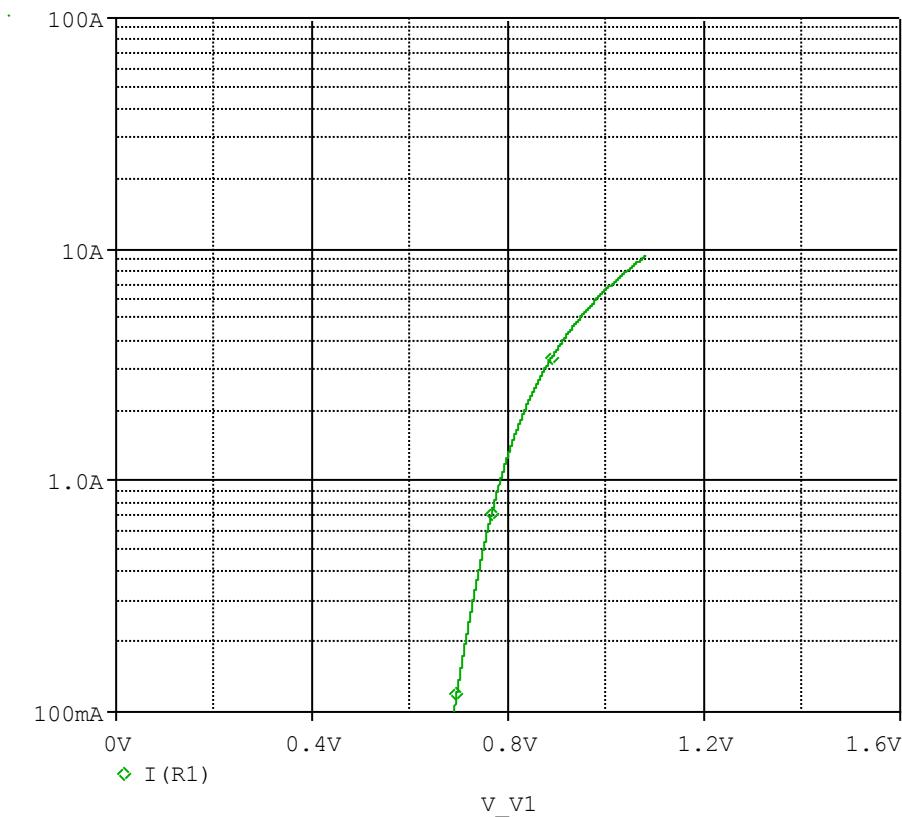


Evaluation circuit

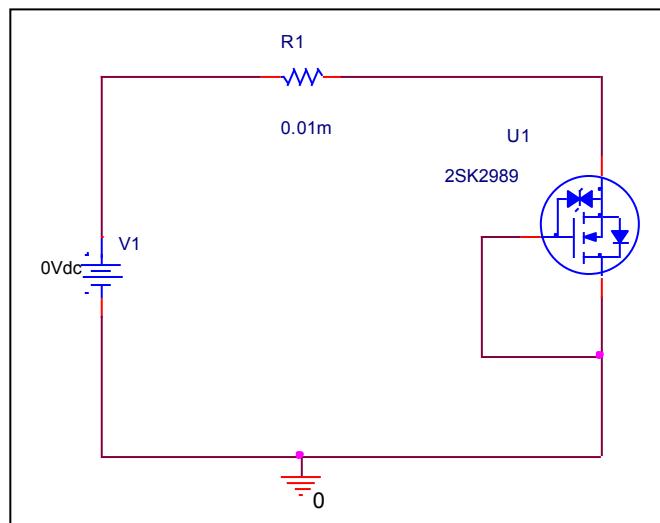


## BODY DIODE Forward Current Characteristic

Circuit Simulation Result

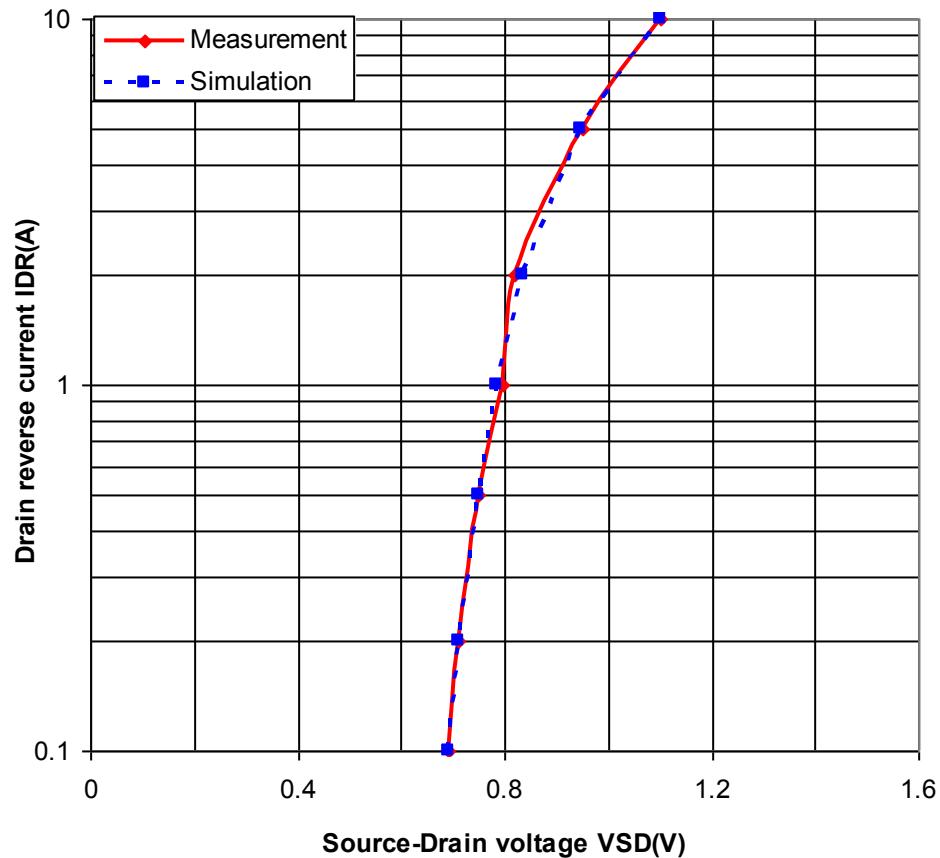


Evaluation Circuit



## Comparison Graph

Circuit Simulation Result

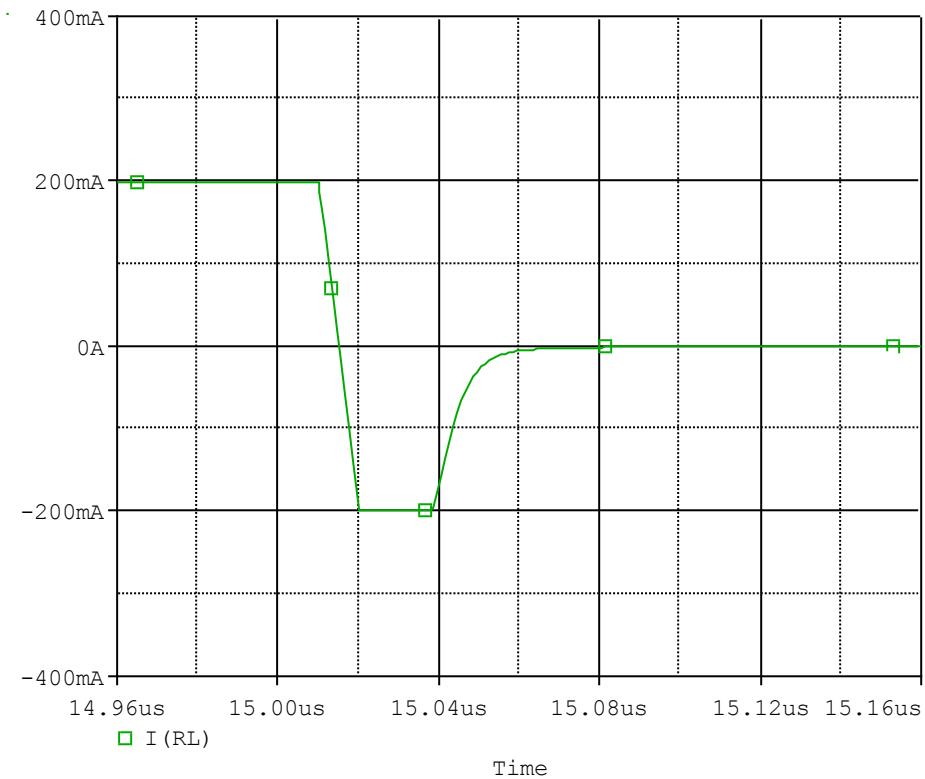


Simulation Result

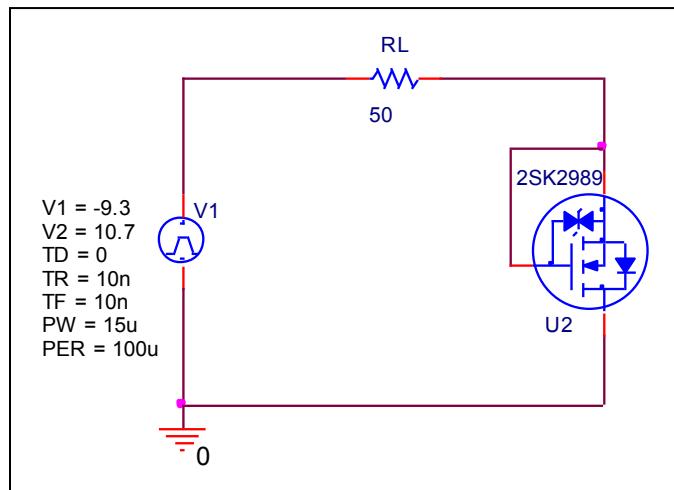
Ifwd(A)	VSD(V)		%Error
	Measuremen	Simulation	
0.1	0.69	0.69	0
0.2	0.71	0.713	0.423
0.5	0.75	0.749	-0.133
1	0.795	0.784	-1.384
2	0.82	0.833	1.585
5	0.95	0.945	-0.526
10	1.1	1.101	0.091

## Reverse Recovery Characteristic

Circuit Simulation Result



Evaluation Circuit

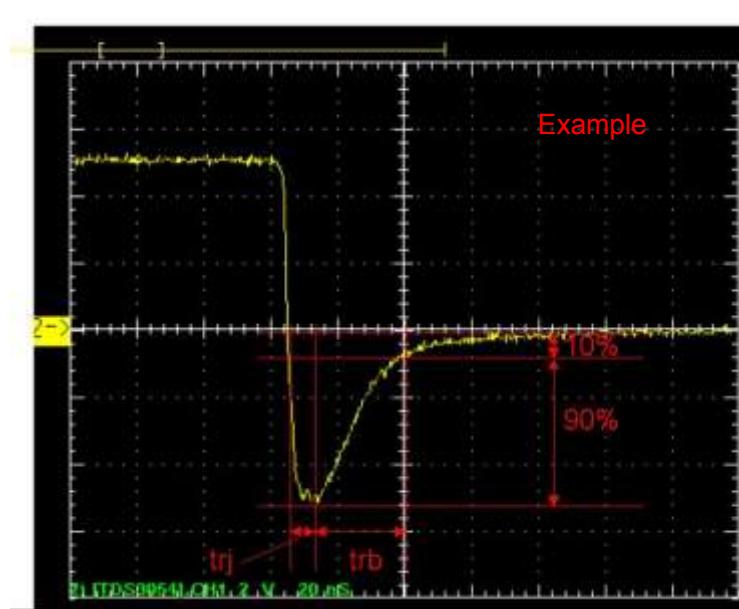
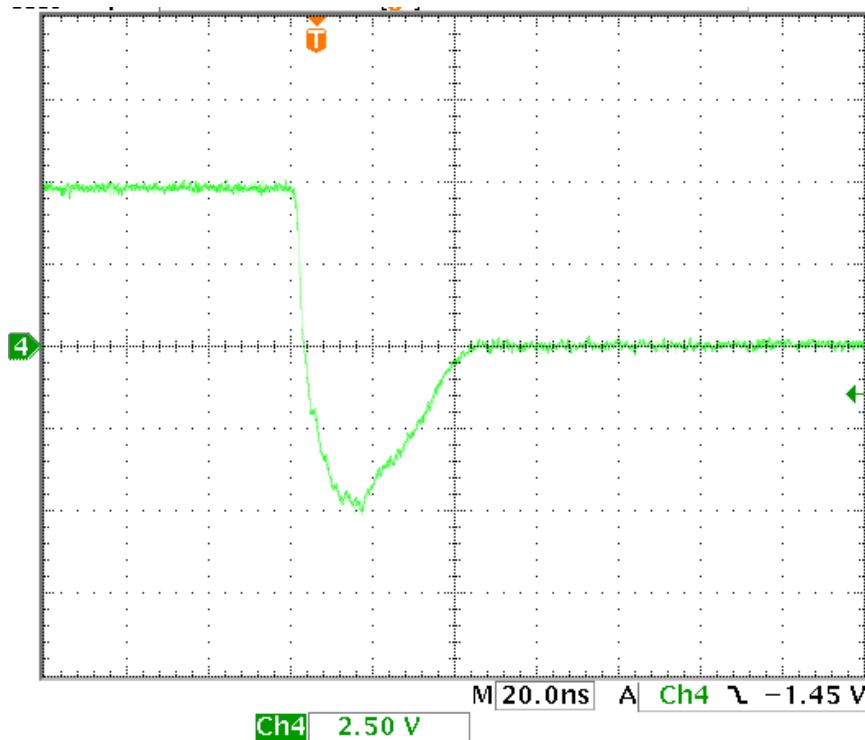


Compare Measurement vs. Simulation

Trr(ns)	Measurement	Simulation	Error (%)
Trj+Trb (ns)	36.4	36.318	-0.225

## Reverse Recovery Characteristic

Reference

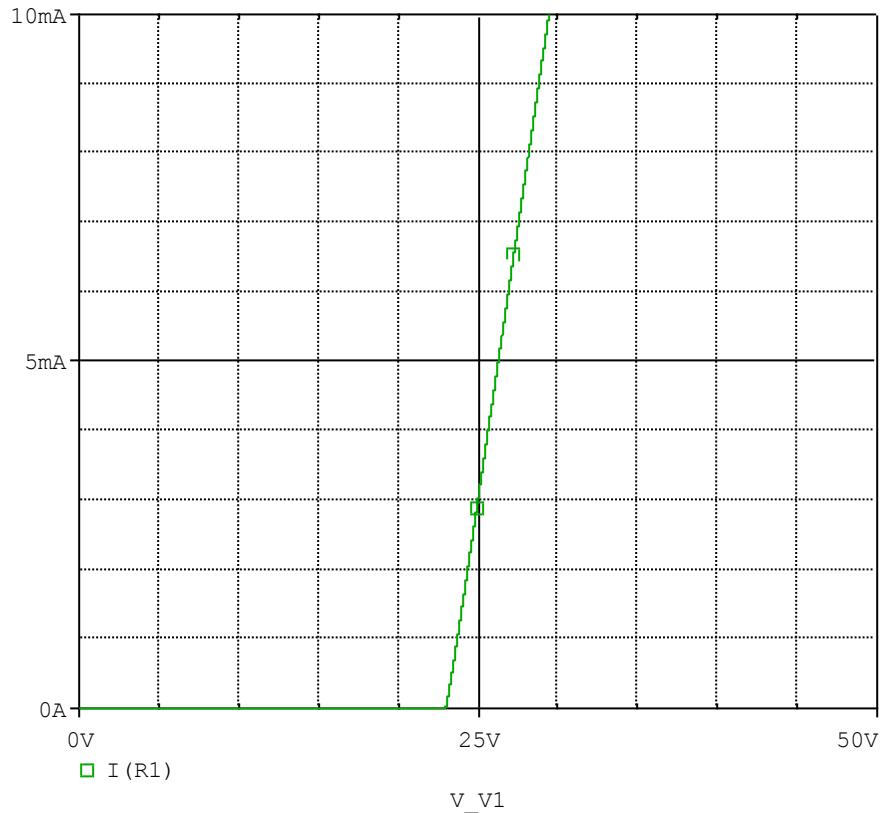


Relation between trj and trb

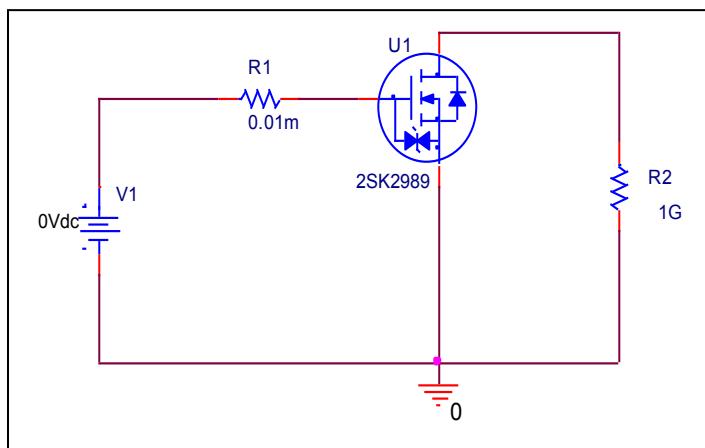
# ESD PROTECTION DIODE

## Zener Voltage Characteristic

### Circuit Simulation Result



### Evaluation Circuit



## Zener Voltage Characteristic

## Reference

