

# **Device Modeling Report**

COMPONENTS: Power MOSFET (Professional)  
PART NUMBER: SSM3J115TU  
MANUFACTURER: TOSHIBA  
Body Diode (Professional) / ESD Protection Diode

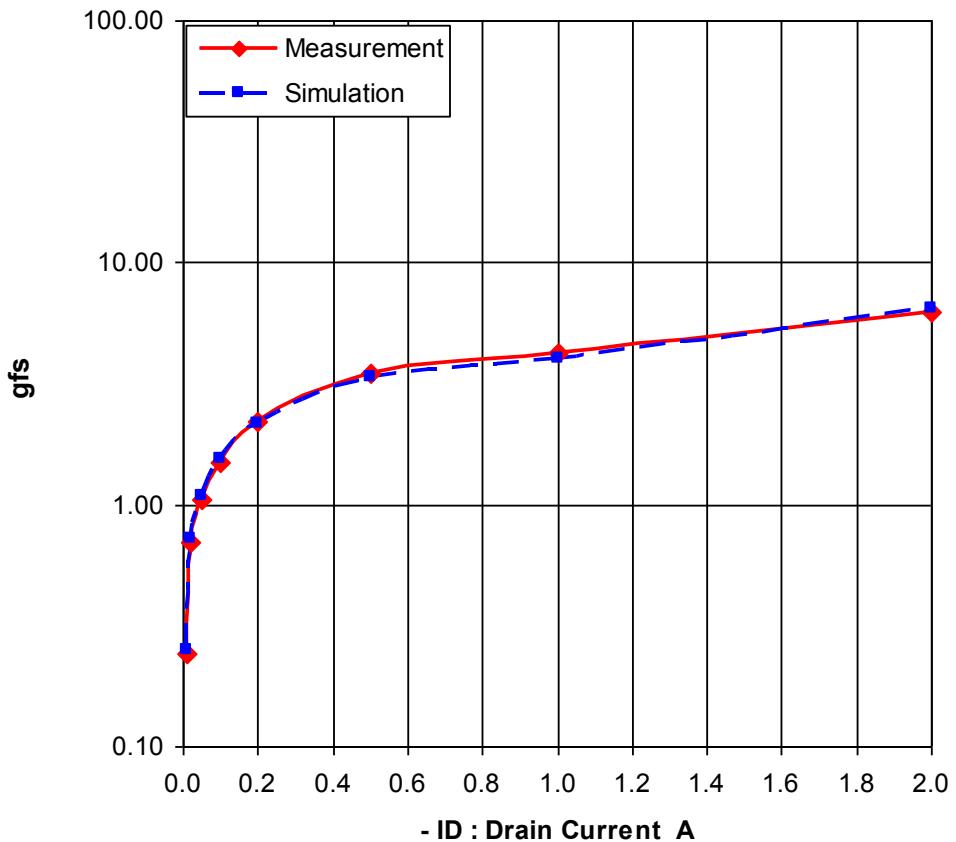


## MOSFET MODEL

Pspice model parameter	Model description
LEVEL	
L	Channel Length
W	Channel Width
KP	Transconductance
RS	Source Ohmic Resistance
RD	Ohmic Drain Resistance
VTO	Zero-bias Threshold Voltage
RDS	Drain-Source Shunt Resistance
TOX	Gate Oxide Thickness
CGSO	Zero-bias Gate-Source Capacitance
CGDO	Zero-bias Gate-Drain Capacitance
CBD	Zero-bias Bulk-Drain Junction Capacitance
MJ	Bulk Junction Grading Coefficient
PB	Bulk Junction Potential
FC	Bulk Junction Forward-bias Capacitance Coefficient
RG	Gate Ohmic Resistance
IS	Bulk Junction Saturation Current
N	Bulk Junction Emission Coefficient
RB	Bulk Series Resistance
PHI	Surface Inversion Potential
GAMMA	Body-effect Parameter
DELTA	Width effect on Threshold Voltage
ETA	Static Feedback on Threshold Voltage
THETA	Modility Modulation
KAPPA	Saturation Field Factor
VMAX	Maximum Drift Velocity of Carriers
XJ	Metallurgical Junction Depth
UO	Surface Mobility

## Transconductance Characteristic

Circuit Simulation Result

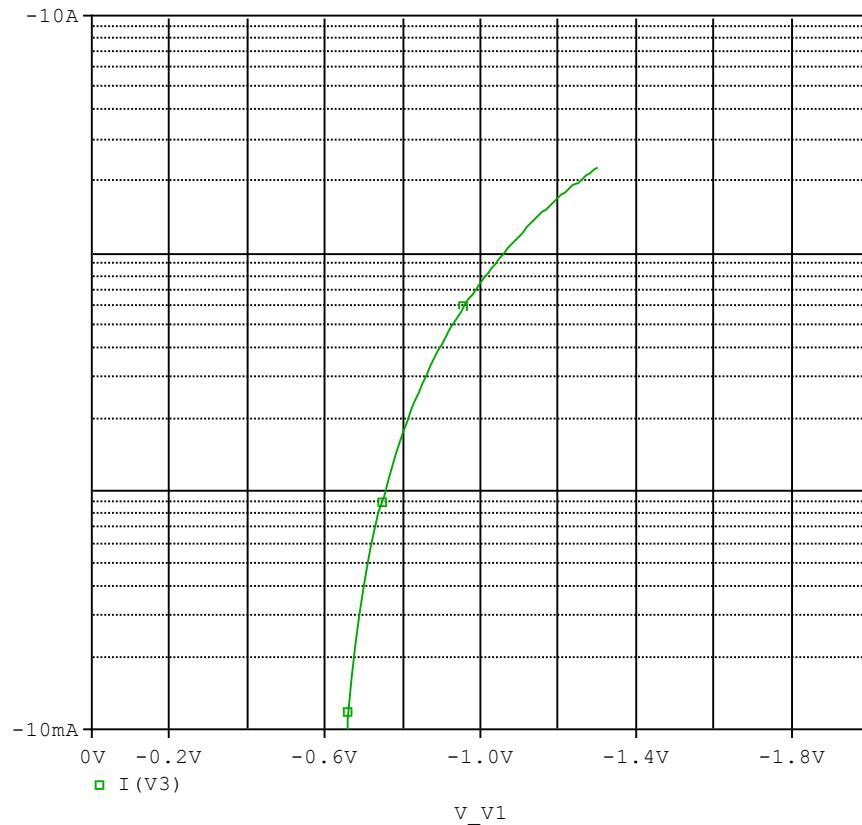


Comparison table

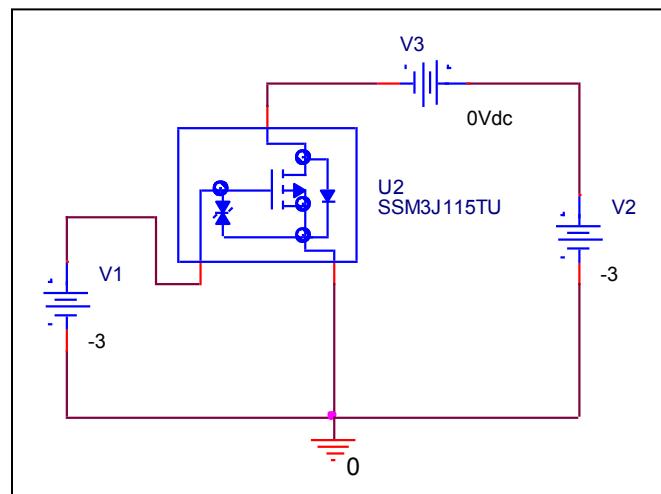
-Id(A)	gfs		Error(%)
	Measurement	Simulation	
0.010	0.240	0.250	4.167
0.020	0.698	0.727	4.155
0.050	1.050	1.089	3.714
0.100	1.500	1.554	3.600
0.200	2.200	2.162	-1.727
0.500	3.500	3.387	-3.229
1.000	4.200	4.049	-3.595
2.000	6.250	6.473	3.568

## V<sub>gs</sub>-I<sub>d</sub> Characteristic

Circuit Simulation result

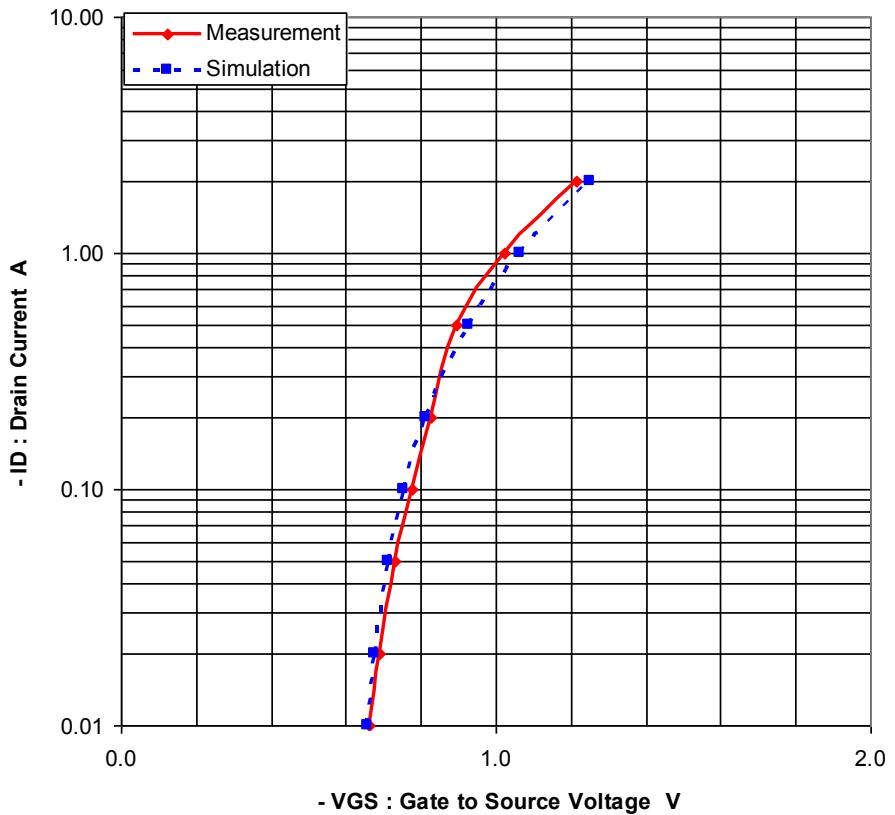


Evaluation circuit



## Comparison Graph

Circuit Simulation Result

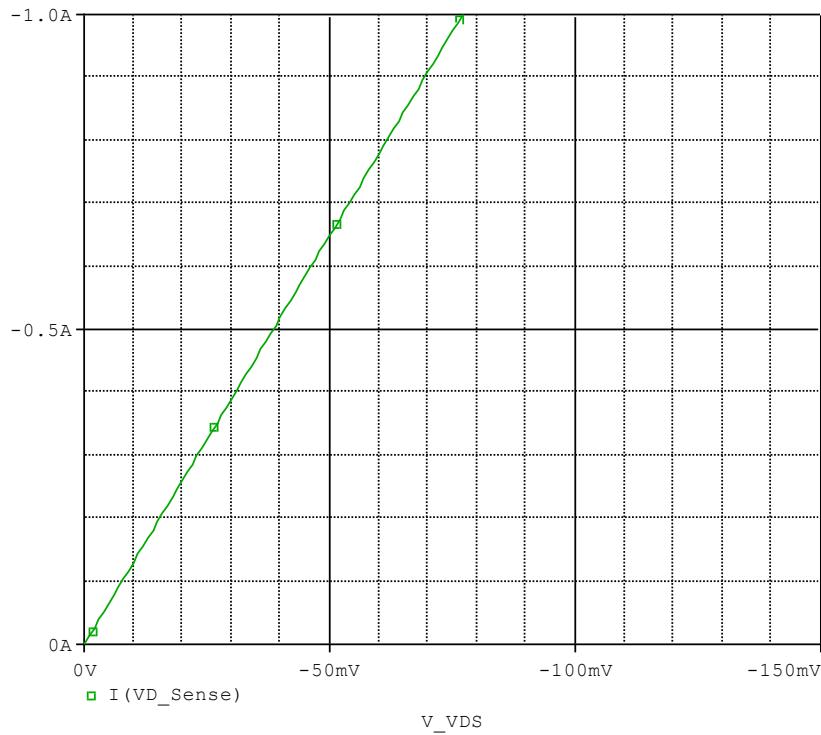


Simulation Result

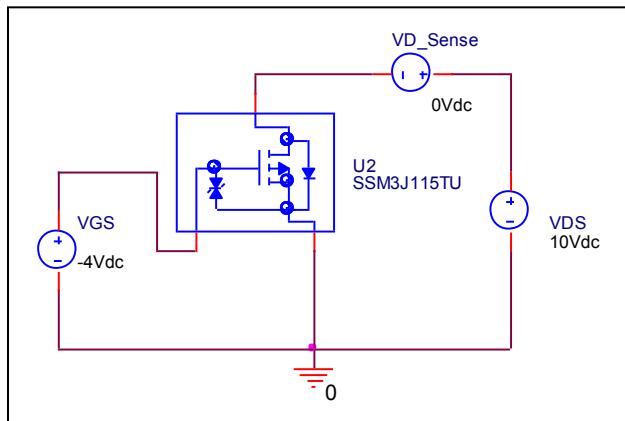
$-I_D$ (A)	$-V_{GS}$ (V)		Error (%)
	Measurement	Simulation	
0.010	0.661	0.657	0.010
0.020	0.691	0.677	0.020
0.050	0.730	0.713	0.050
0.100	0.778	0.754	0.100
0.200	0.827	0.812	0.200
0.500	0.897	0.927	0.500
1.000	1.025	1.062	1.000
2.000	1.214	1.253	2.000

## Rds(on) Characteristic

### Circuit Simulation result



### Evaluation circuit

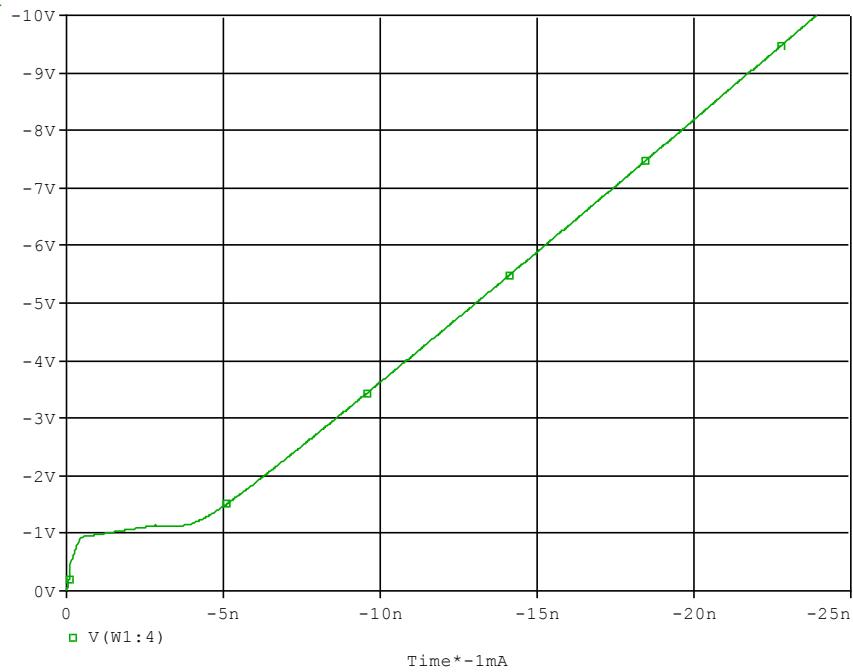


### Simulation Result

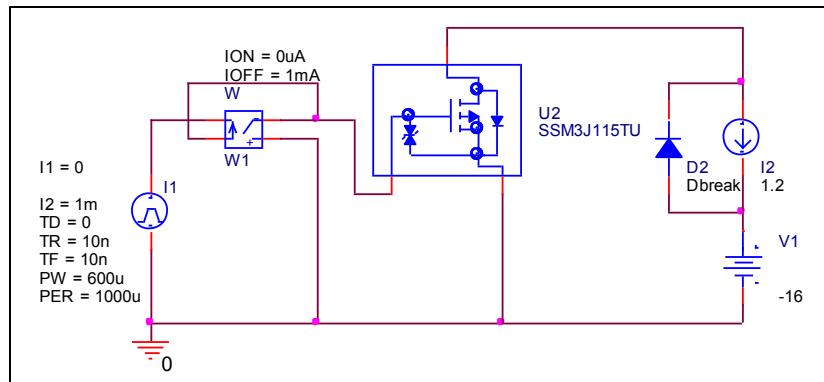
I <sub>D</sub> =-1A, V <sub>GS</sub> =-4V	Measurement	Simulation	Error (%)
R <sub>DS (on)</sub> (Ω)	0.077	0.077017	0.022

## Gate Charge Characteristic

### Circuit Simulation result



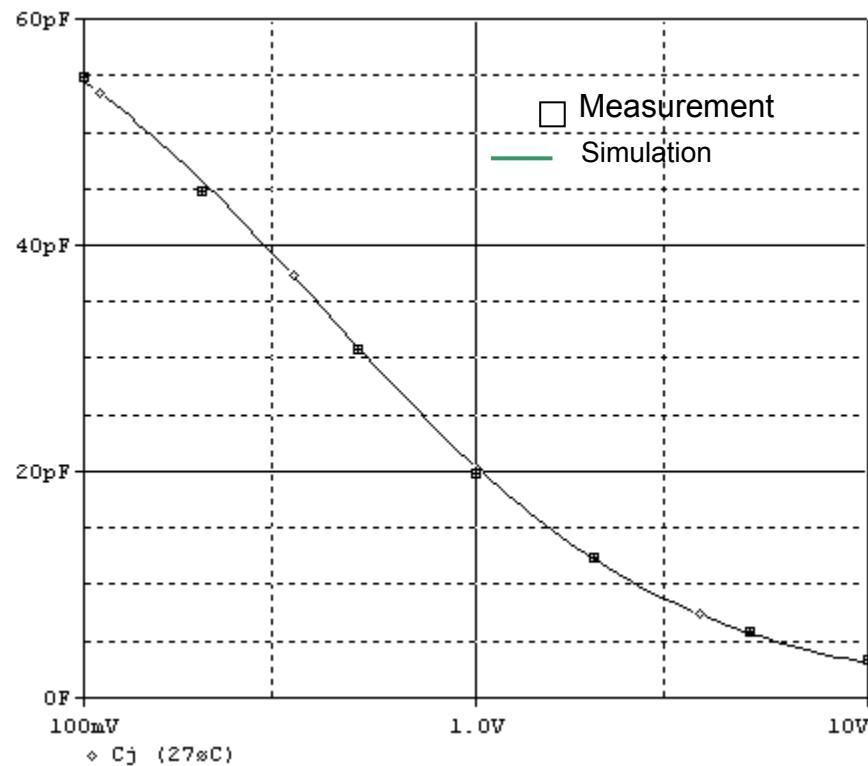
### Evaluation circuit



### Simulation Result

$V_{DD}=-16V, I_D=-1.2A, V_{GS}=-4V$	Measurement	Simulation	Error (%)
Q <sub>gs(nc)</sub>	0.500	0.515	3.000
Q <sub>gd(nc)</sub>	3.500	3.505	0.143
Q <sub>g(nc)</sub>	10.800	10.825	0.231

## Capacitance Characteristic

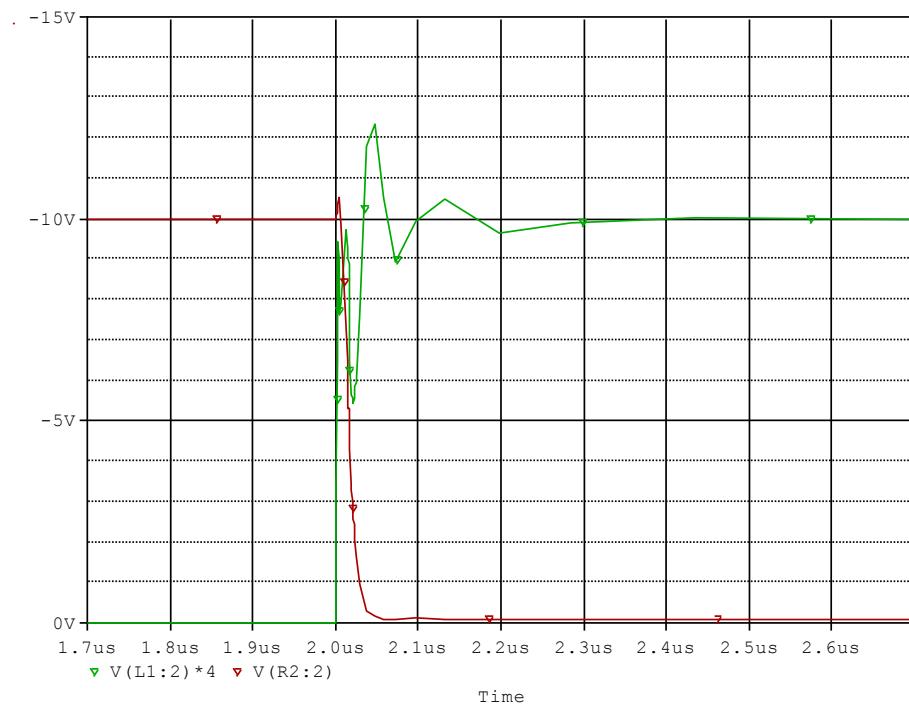


Simulation Result

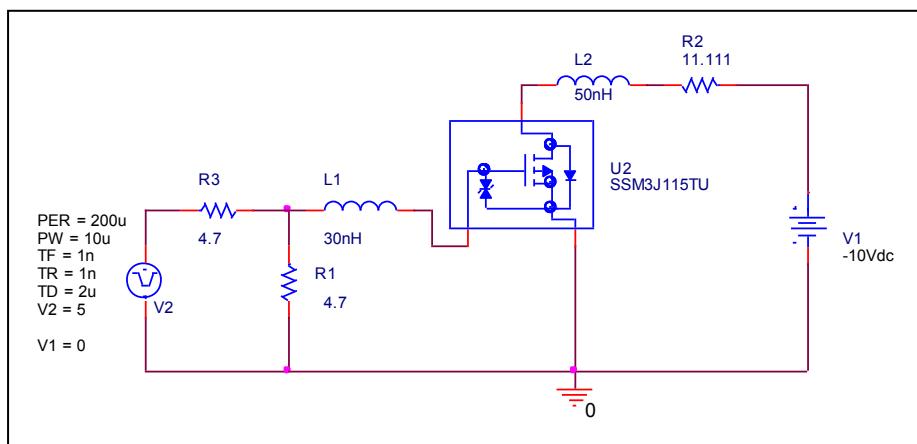
$V_{ds}$ (V)	C <sub>bd</sub> (pF)		Error(%)
	Measurement	Simulation	
0.100	55.000	54.350	-1.180
0.200	45.000	45.800	1.780
0.500	31.000	31.100	0.320
1.000	20.000	20.300	1.500
2.000	12.500	12.300	-1.600
5.000	5.800	5.700	-1.720
10.000	3.300	3.200	-3.030

## Switching Time Characteristic

### Circuit Simulation result



### Evaluation circuit

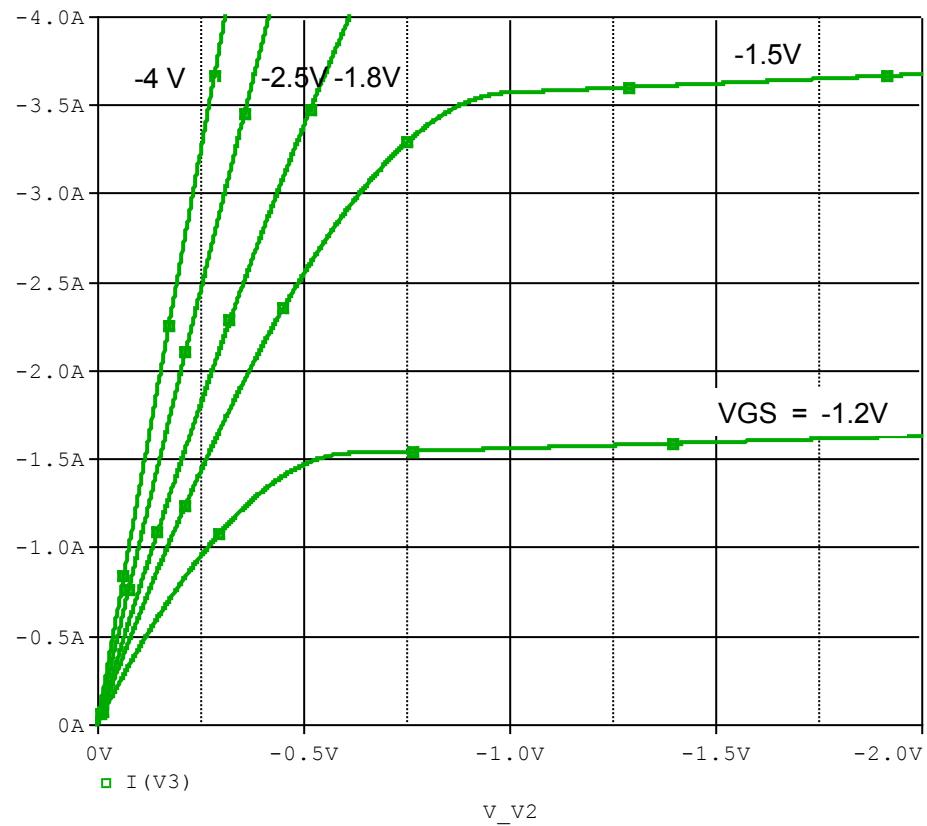


### Simulation Result

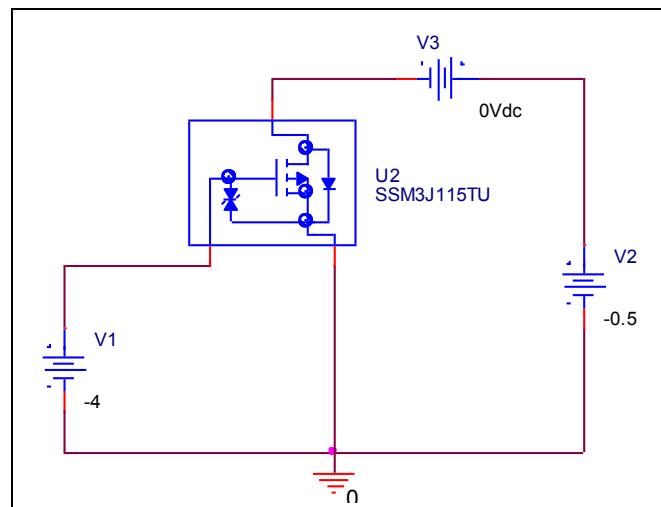
$I_D = -0.9A, V_{DD} = -10V, V_{GS} = -2.5V$	Measurement	Simulation	Error(%)
$T_{on}(ns)$	29.000	28.926	-0.255

## Output Characteristic

Circuit Simulation result

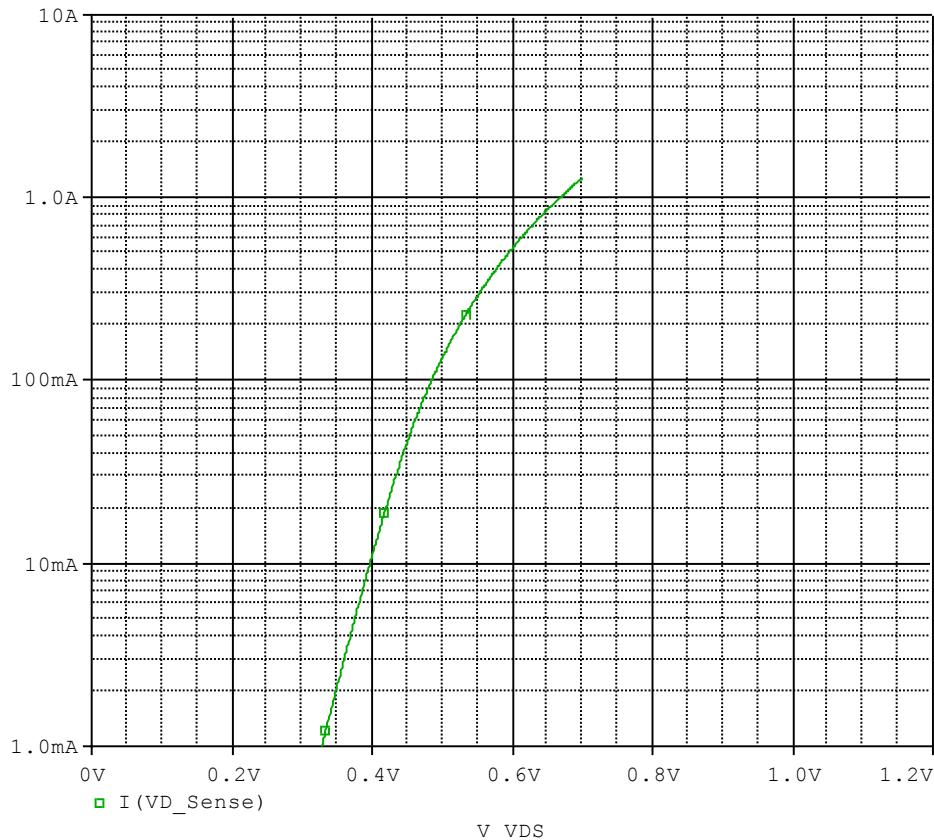


Evaluation circuit

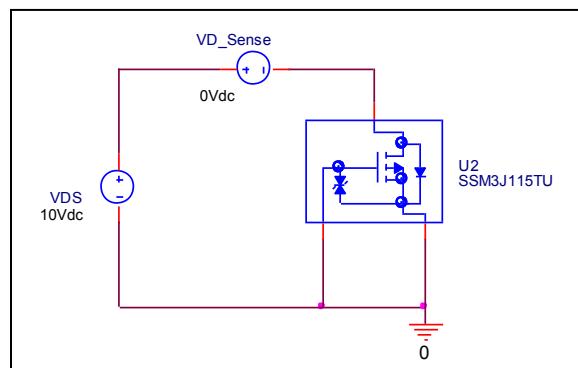


## Forward Current Characteristic

### Circuit Simulation Result

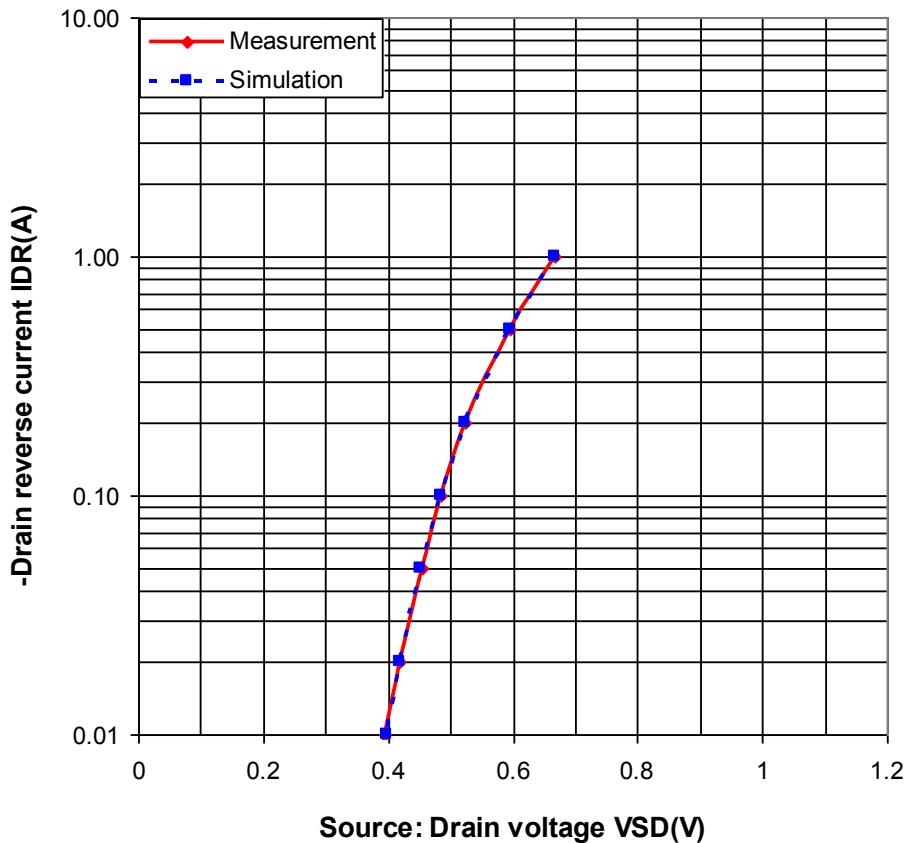


### Evaluation Circuit



## Comparison Graph

Circuit Simulation Result

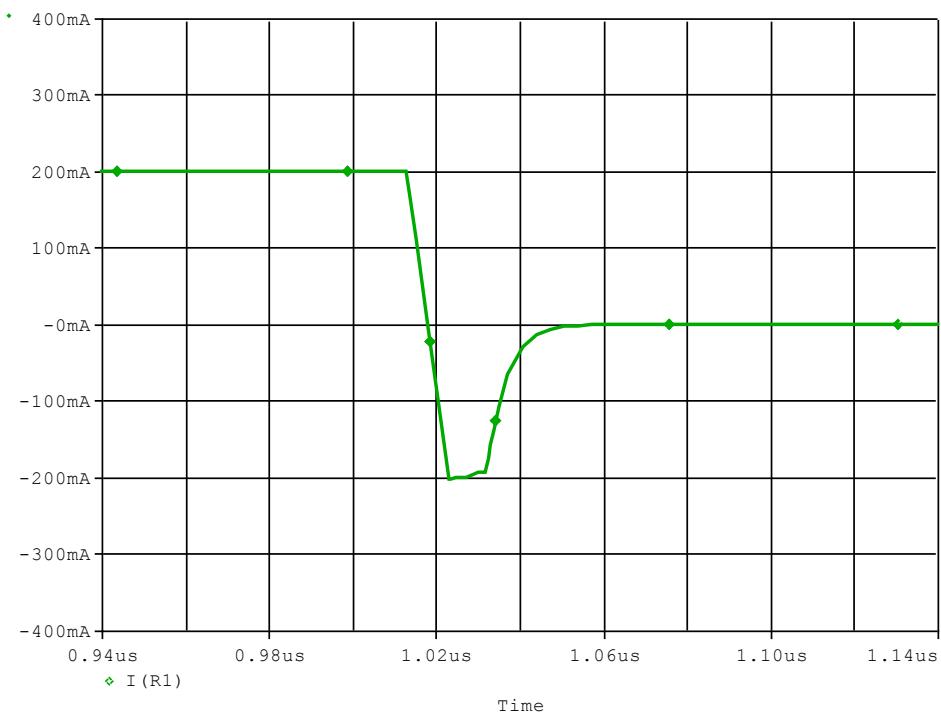


Simulation Result

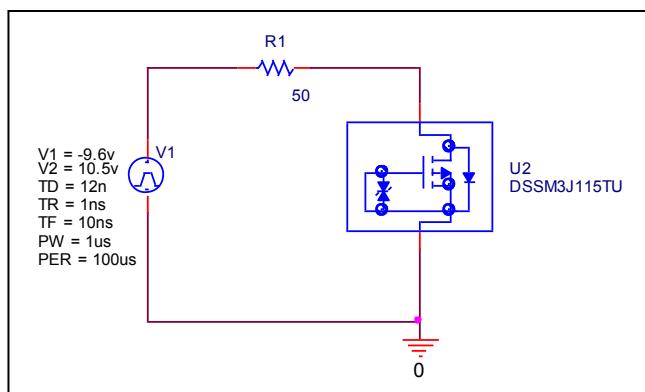
-IDR(A)	VDS(V) Measurement	VDS(V) Simulation	%Error
0.010	0.396	0.397	0.253
0.020	0.420	0.419	-0.238
0.050	0.454	0.453	-0.220
0.100	0.486	0.485	-0.206
0.200	0.523	0.525	0.382
0.500	0.596	0.595	-0.168
1.000	0.668	0.668	0.000
2.000	0.396	0.397	0.253

## Reverse Recovery Characteristic

Circuit Simulation Result



Evaluation Circuit

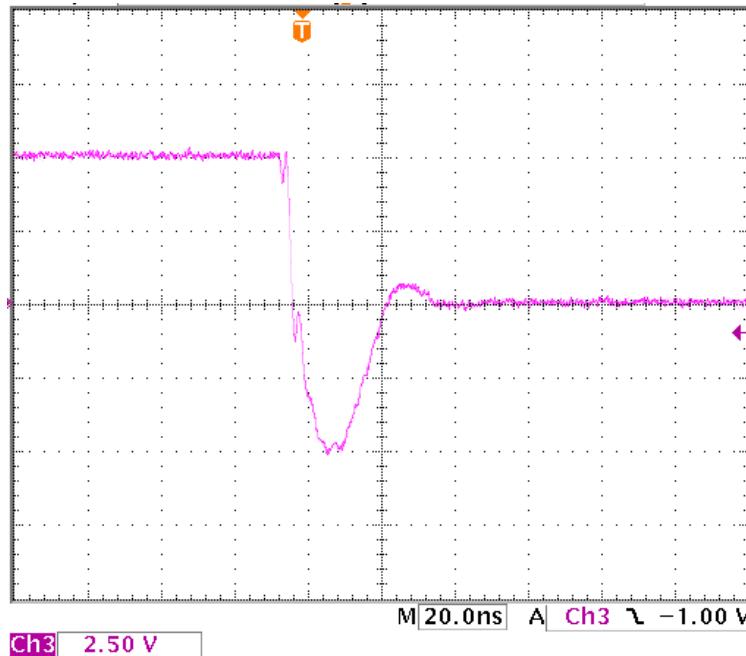


## Compare Measurement vs. Simulation

	Measurement	Simulation	Error (%)
Trj(ns)	13.200	13.291	0.689
trb(ns)	10.800	10.700	-0.926
trr(ns)	24.000	23.991	-0.038

## Reverse Recovery Characteristic

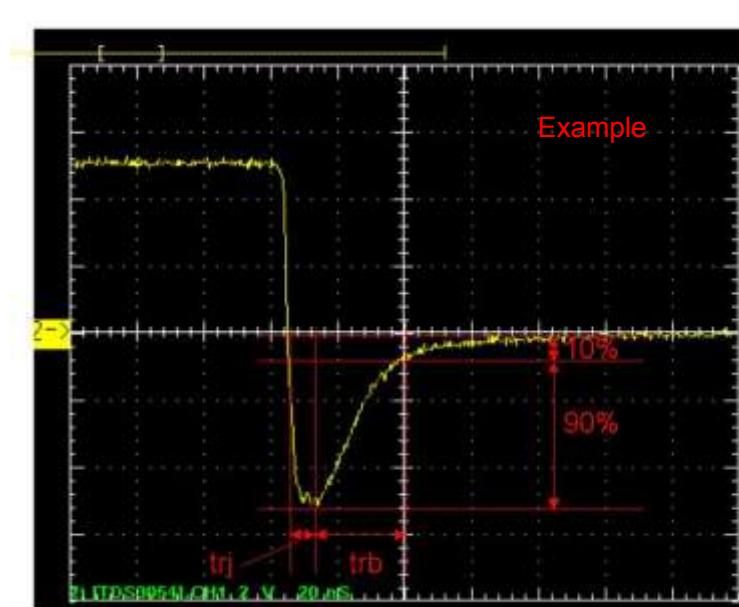
## Reference



Trj=13.2 (ns)

Trb=10.8 (ns)

Conditions: Ifwd=Irev=0.2(A), RI=50

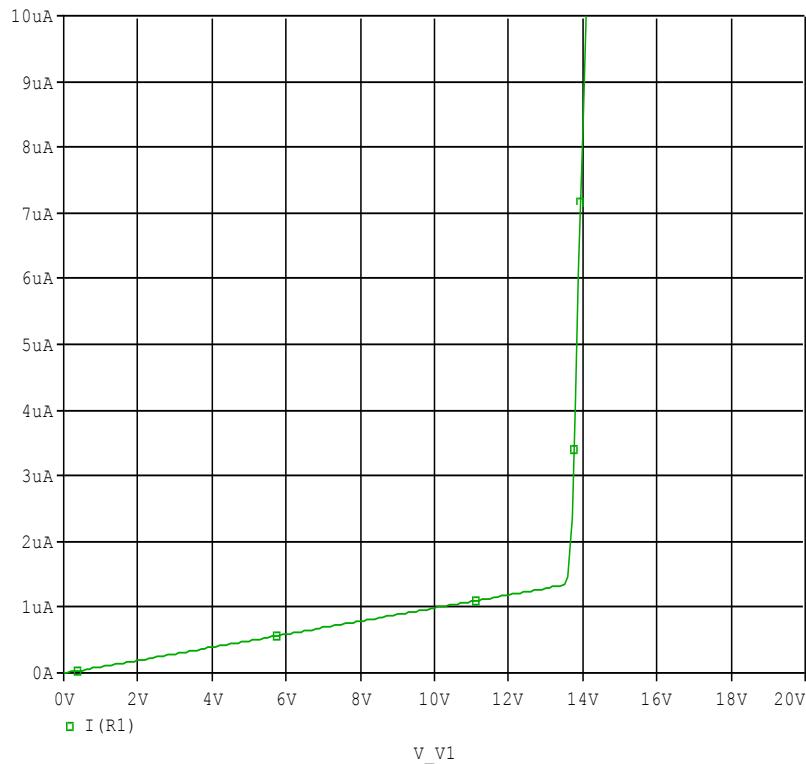


Relation between trj and trb

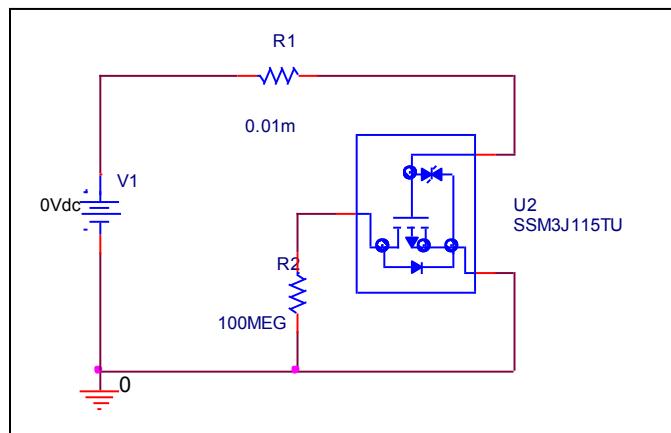
## ESD PROTECTION DIODE SPICE MODEL

### Zener Voltage Characteristic

#### Circuit Simulation Result



#### Evaluation Circuit



## Zener Voltage Characteristic

## Reference

