

Device Modeling Report

COMPONENTS: Power MOSFET (Professional)
PART NUMBER: SSM3K102TU
MANUFACTURER: TOSHIBA
Body Diode (Professional) / ESD Protection Diode



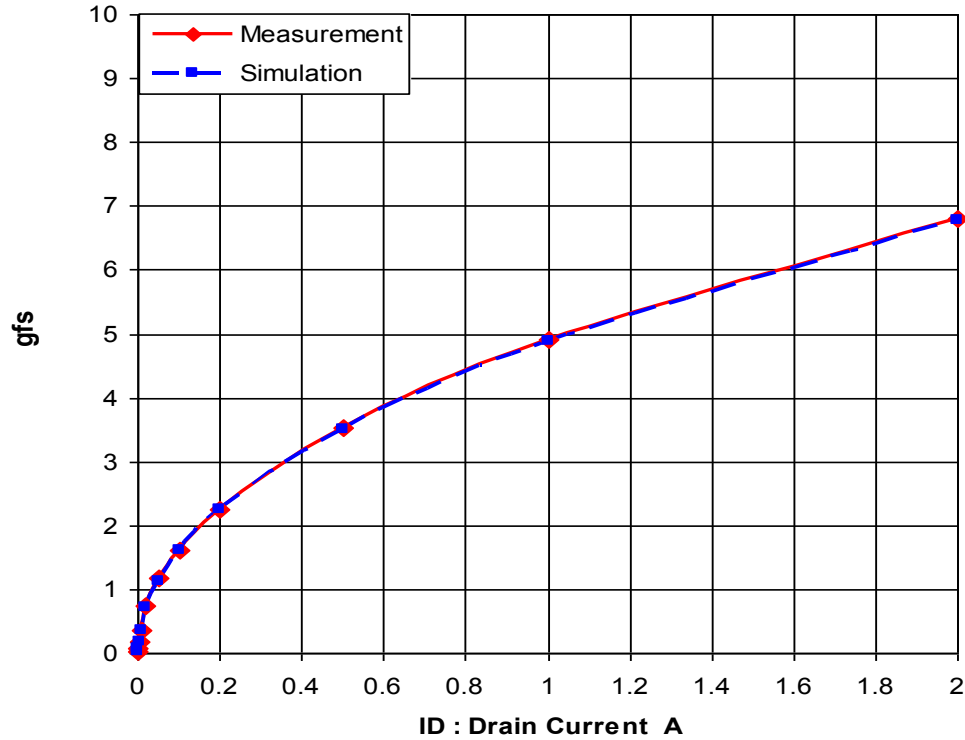
Bee Technologies Inc.

MOSFET MODEL

Pspice model parameter	Model description
LEVEL	
L	Channel Length
W	Channel Width
KP	Transconductance
RS	Source Ohmic Resistance
RD	Ohmic Drain Resistance
VTO	Zero-bias Threshold Voltage
RDS	Drain-Source Shunt Resistance
TOX	Gate Oxide Thickness
CGSO	Zero-bias Gate-Source Capacitance
CGDO	Zero-bias Gate-Drain Capacitance
CBD	Zero-bias Bulk-Drain Junction Capacitance
MJ	Bulk Junction Grading Coefficient
PB	Bulk Junction Potential
FC	Bulk Junction Forward-bias Capacitance Coefficient
RG	Gate Ohmic Resistance
IS	Bulk Junction Saturation Current
N	Bulk Junction Emission Coefficient
RB	Bulk Series Resistance
PHI	Surface Inversion Potential
GAMMA	Body-effect Parameter
DELTA	Width effect on Threshold Voltage
ETA	Static Feedback on Threshold Voltage
THETA	Modility Modulation
KAPPA	Saturation Field Factor
VMAX	Maximum Drift Velocity of Carriers
XJ	Metallurgical Junction Depth
UO	Surface Mobility

Transconductance Characteristic

Circuit Simulation Result

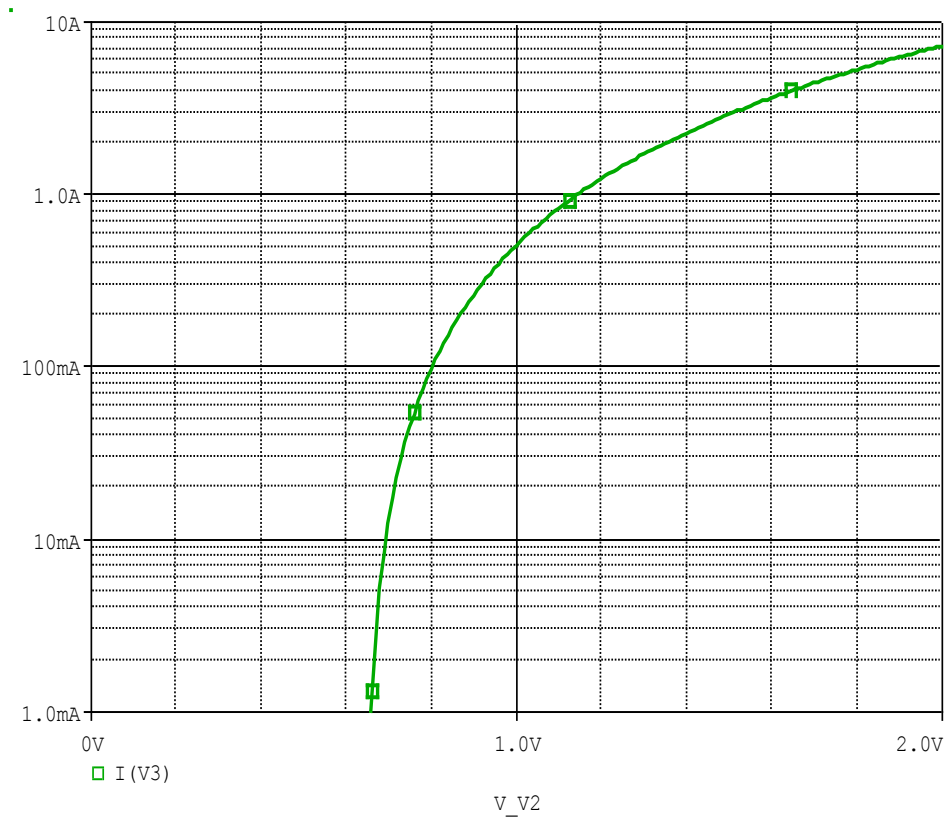


Comparison table

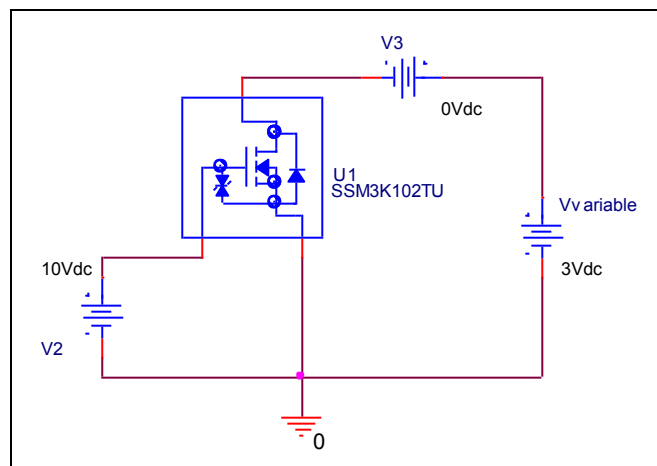
Id(A)	gfs		Error(%)
	Measurement	Simulation	
0.001	0.034	0.035	2.941
0.002	0.070	0.072	2.857
0.005	0.185	0.181	-2.162
0.010	0.370	0.362	-2.162
0.020	0.730	0.723	-0.959
0.050	1.170	1.135	-2.991
0.100	1.620	1.603	-1.049
0.200	2.250	2.252	0.089
0.500	3.530	3.510	-0.567
1.000	4.900	4.895	-0.102
2.000	6.800	6.789	-0.162

V_{gs}-I_d Characteristic

Circuit Simulation result

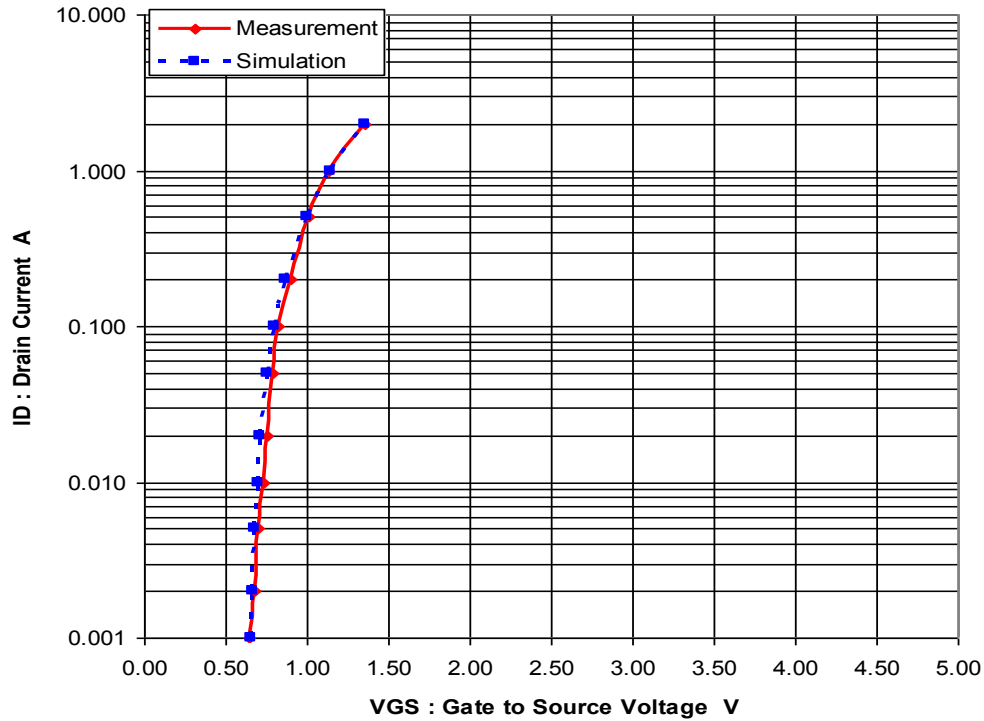


Evaluation circuit



Comparison Graph

Circuit Simulation Result

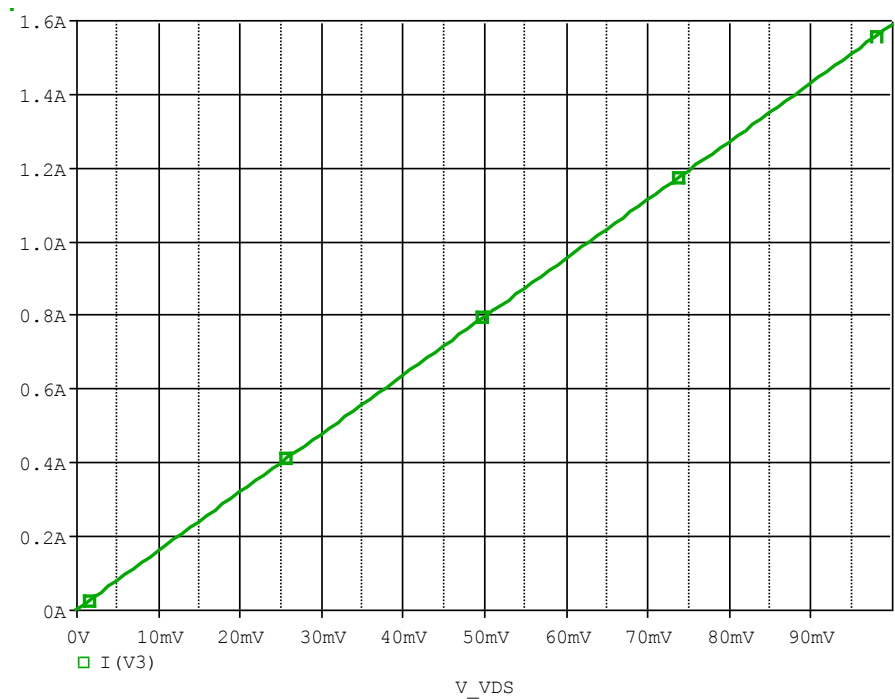


Simulation Result

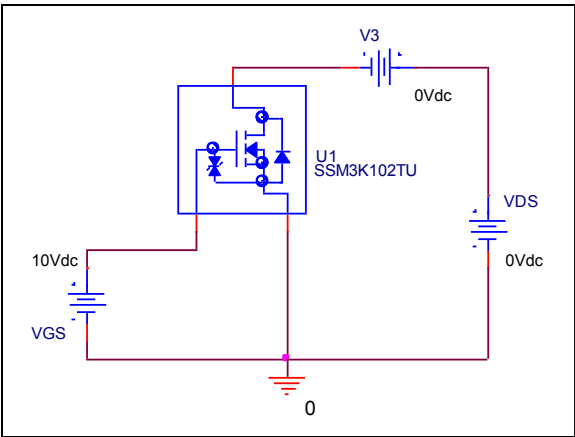
ID(A)	VGS(V)		Error (%)
	Measurement	Simulation	
0.001	0.640	0.660	3.125
0.002	0.680	0.667	-1.912
0.005	0.700	0.680	-2.857
0.010	0.730	0.695	-4.795
0.020	0.750	0.715	-4.667
0.050	0.790	0.757	-4.177
0.100	0.820	0.805	-1.829
0.200	0.900	0.869	-3.444
0.500	1.010	1.000	-0.990
1.000	1.130	1.149	1.681
2.000	1.350	1.356	0.444

Rds(on) Characteristic

Circuit Simulation result



Evaluation circuit

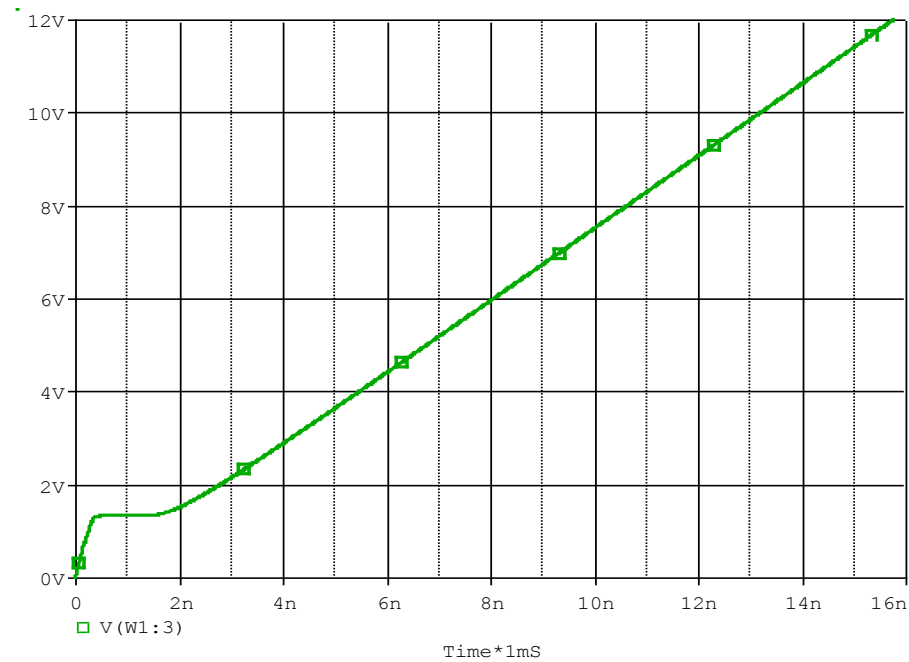


Simulation Result

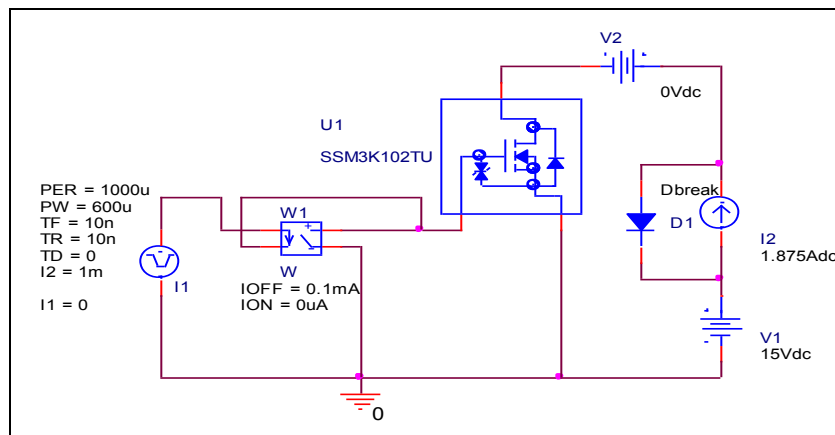
$I_D=1A, V_{GS}=4V$	Measurement	Simulation	Error (%)
$R_{DS(on)}$ (Ω)	63.000	63.025	0.040

Gate Charge Characteristic

Circuit Simulation result



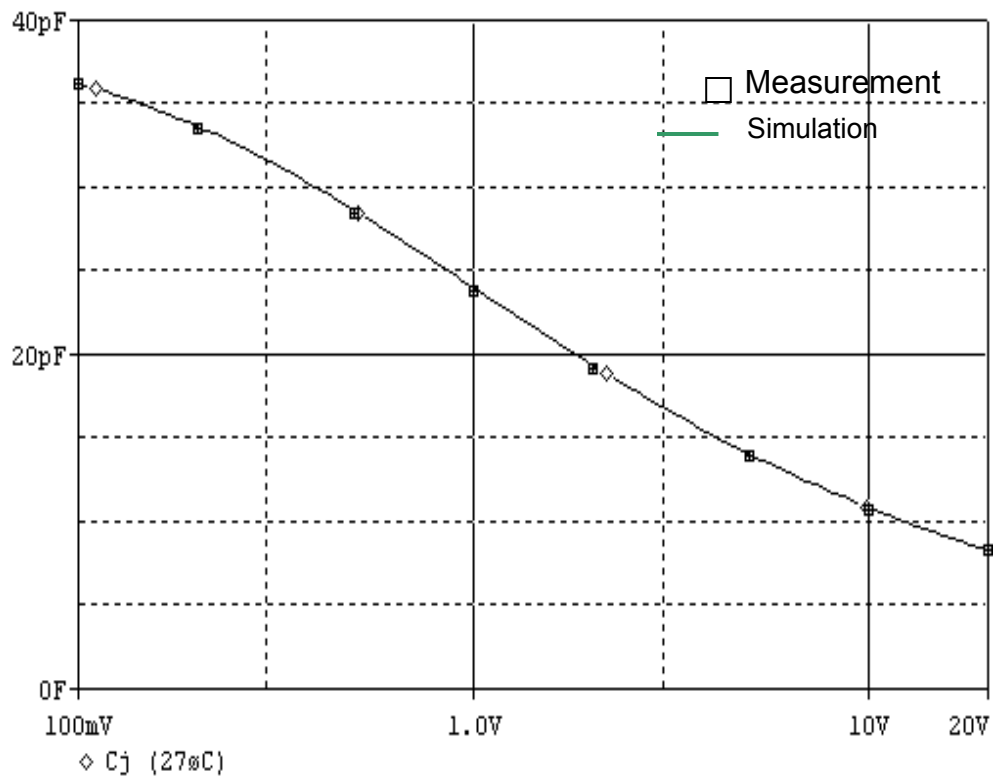
Evaluation circuit



Simulation Result

$V_{DD}=15V, I_D=1.875A$ $V_{GS}=10V$	Measurement	Simulation	Error (%)
Qgs(nc)	0.400	0.403	0.750
Qgd(nc)	1.200	1.211	0.917
Qg(nc)	13.200	13.209	0.068

Capacitance Characteristic

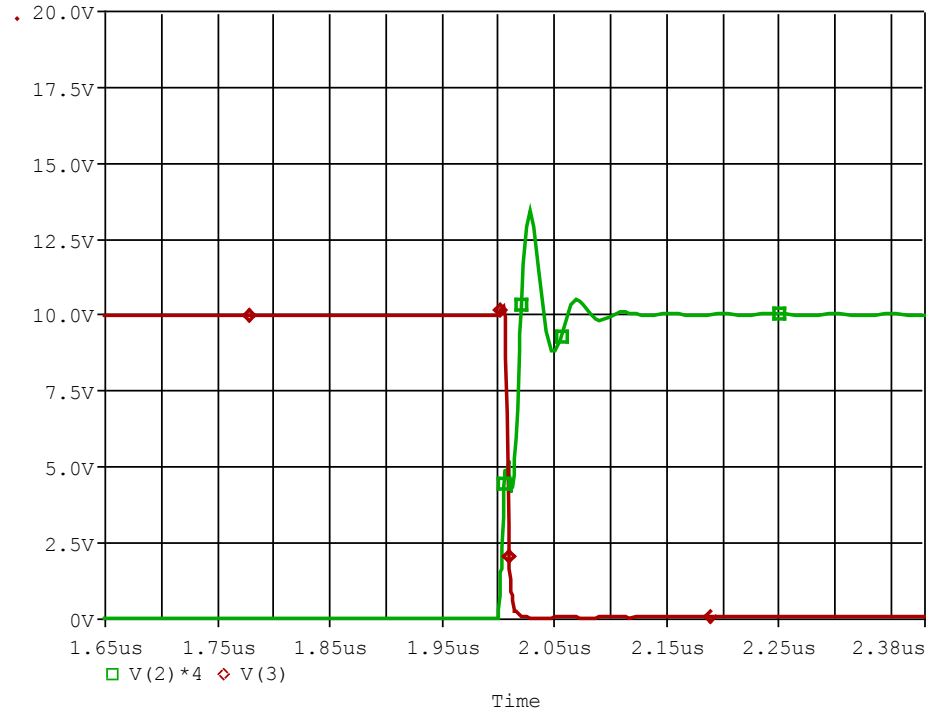


Simulation Result

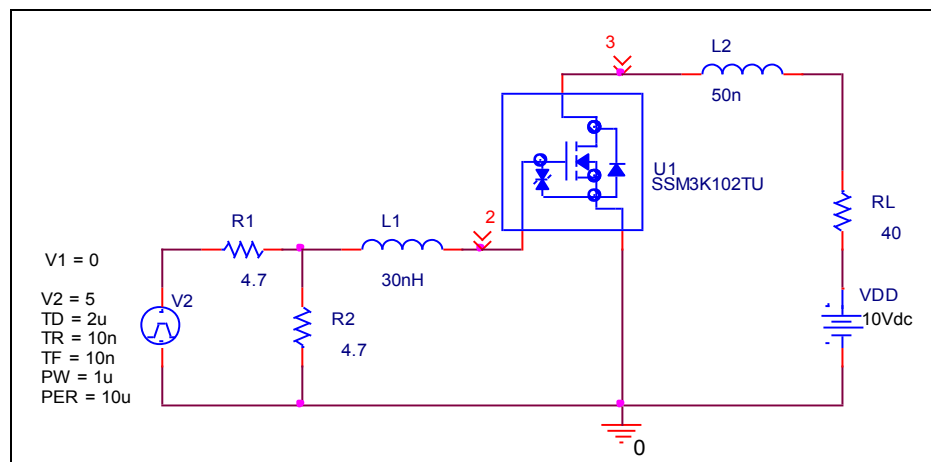
V_{DS} (V)	Cbd(pF)		Error(%)
	Measurement	Simulation	
0.100	36.300	36.200	-0.275
0.200	33.600	33.650	0.149
0.500	28.600	28.620	0.070
1.000	23.900	24.000	0.418
2.000	19.300	19.400	0.518
5.000	14.100	14.050	-0.355
10.000	10.800	10.830	0.278
20.000	8.400	8.350	-0.595

Switching Time Characteristic

Circuit Simulation result



Evaluation circuit

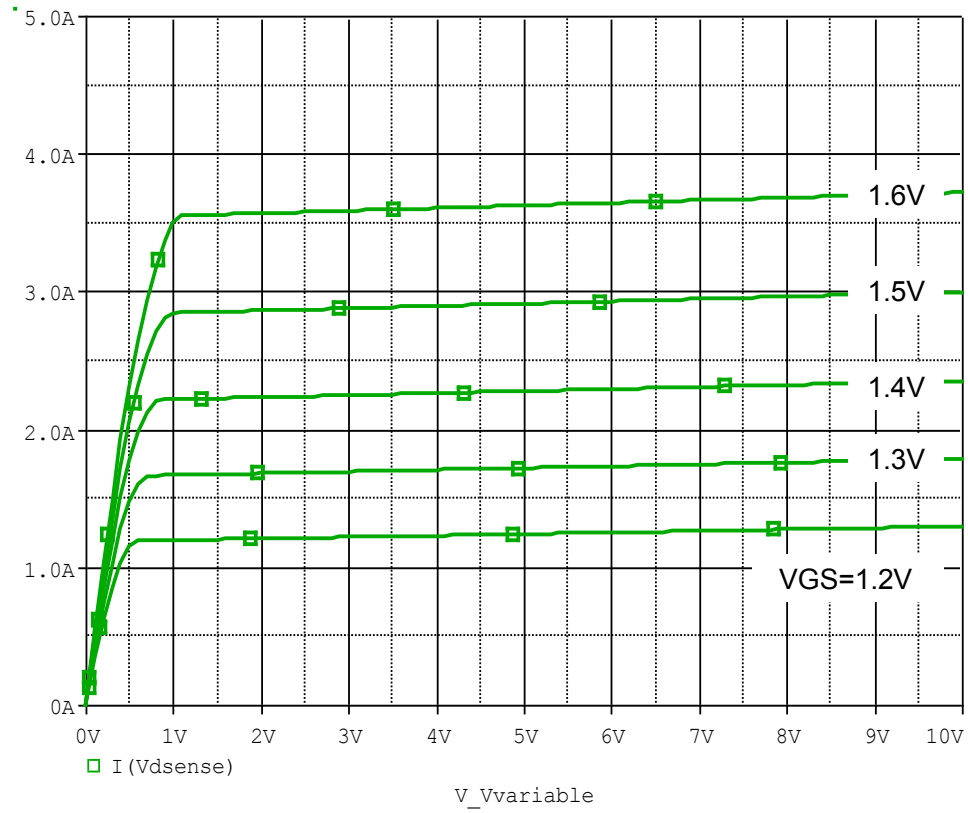


Simulation Result

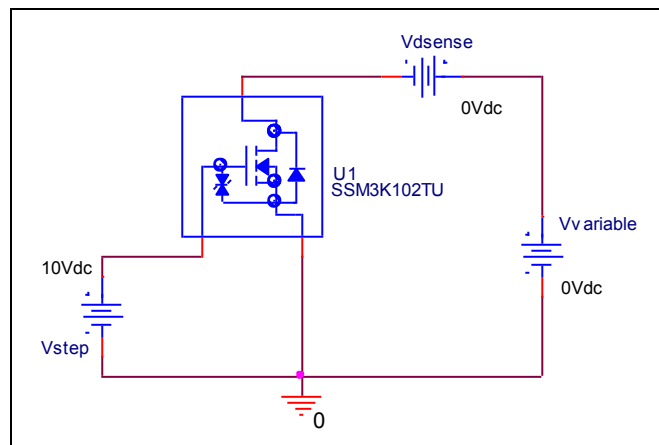
$I_D=0.25A$, $V_{DD}=10V$ $V_{GS}=2.5V$	Measurement	Simulation	Error(%)
Ton(ns)	9.000	8.993	-0.078

Output Characteristic

Circuit Simulation result

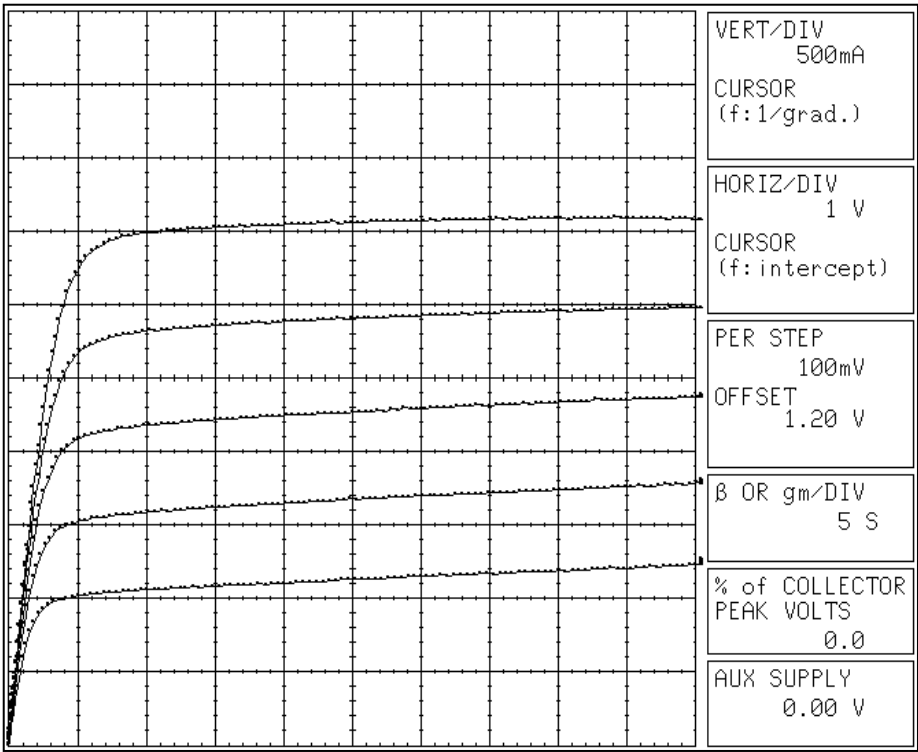


Evaluation circuit



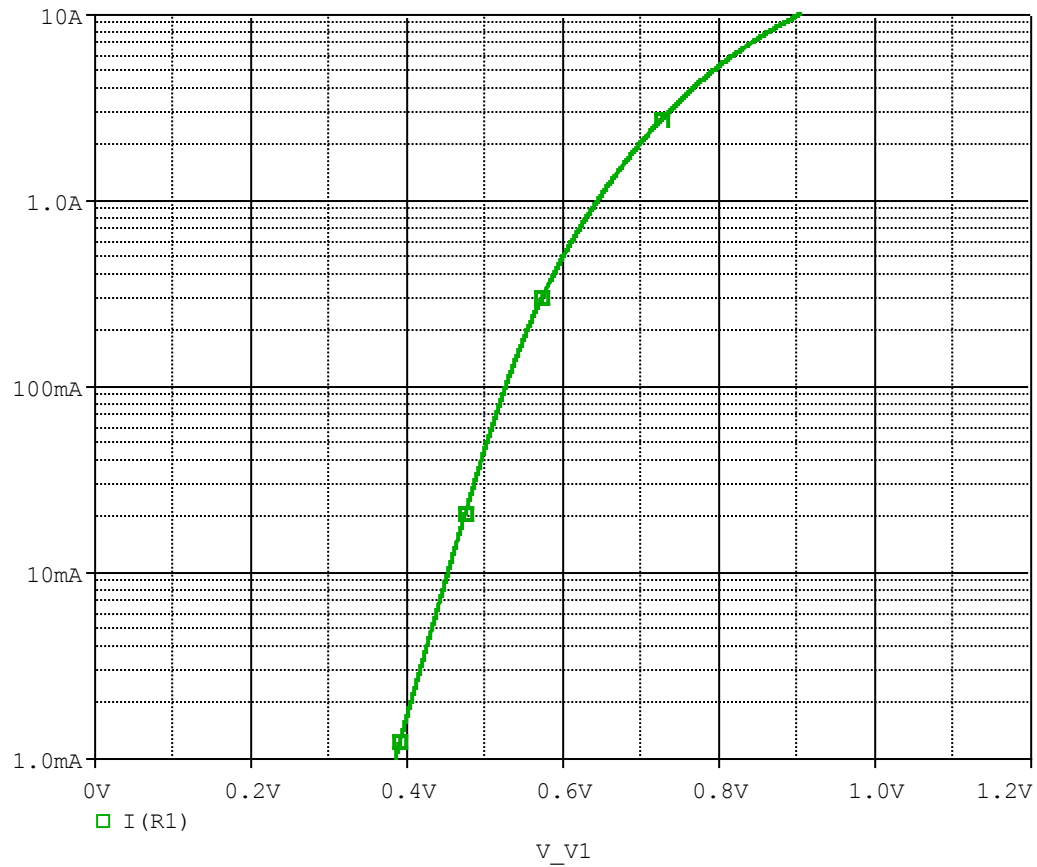
Output Characteristic

Reference

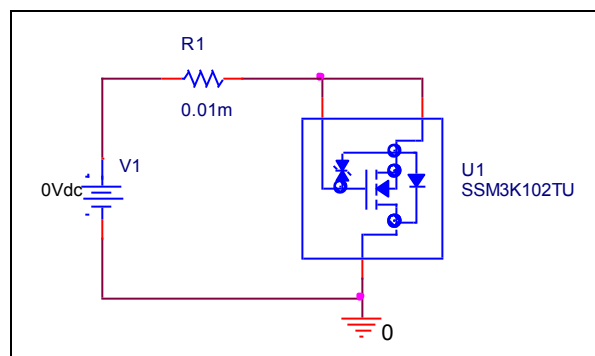


Forward Current Characteristic

Circuit Simulation Result

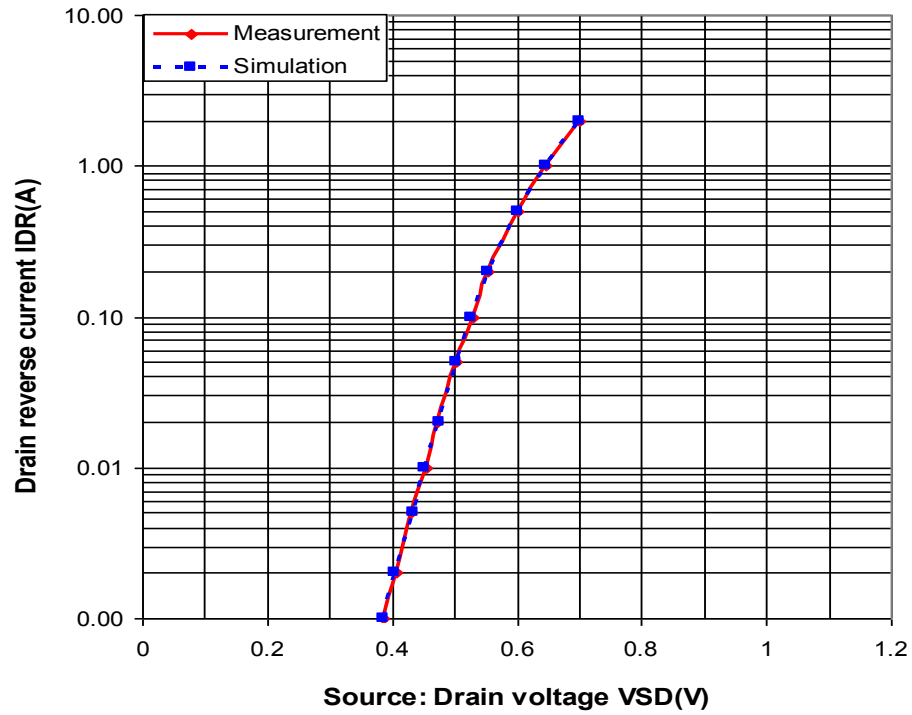


Evaluation Circuit



Comparison Graph

Circuit Simulation Result

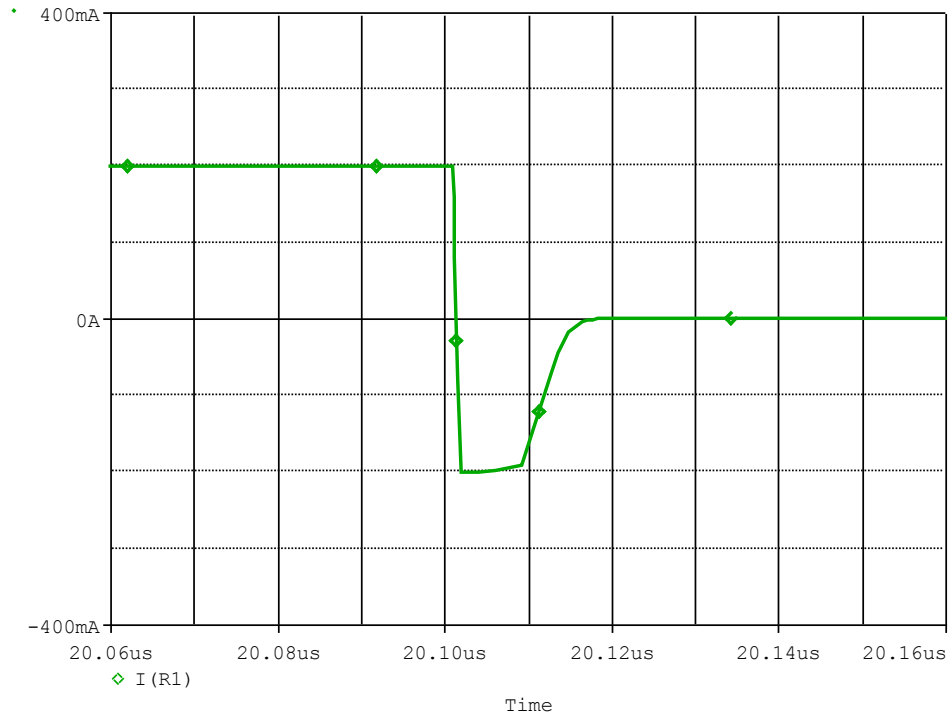


Simulation Result

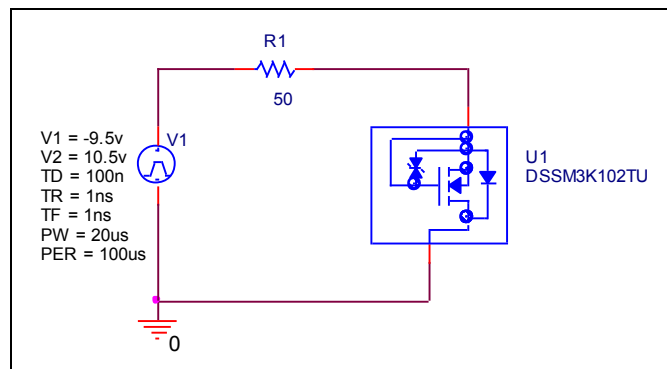
IDR(A)	VSD(V) Measurement	VSD(V) Simulation	%Error
0.001	0.387	0.386	-0.258
0.002	0.406	0.405	-0.246
0.005	0.432	0.433	0.231
0.010	0.454	0.452	-0.441
0.020	0.474	0.475	0.211
0.050	0.504	0.503	-0.198
0.100	0.530	0.528	-0.377
0.200	0.554	0.555	0.181
0.500	0.602	0.601	-0.166
1.000	0.646	0.647	0.155
2.000	0.700	0.701	0.143

Reverse Recovery Characteristic

Circuit Simulation Result



Evaluation Circuit

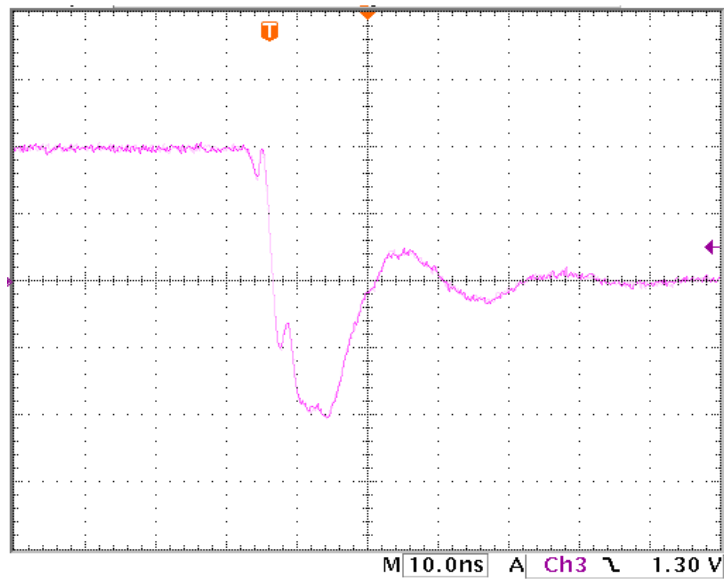


Compare Measurement vs. Simulation

	Measurement	Simulation	Error (%)
Trj(ns)	7.400	7.399	-0.014
trb(ns)	5.800	5.802	0.034
trr(ns)	13.200	13.201	0.008

Reverse Recovery Characteristic

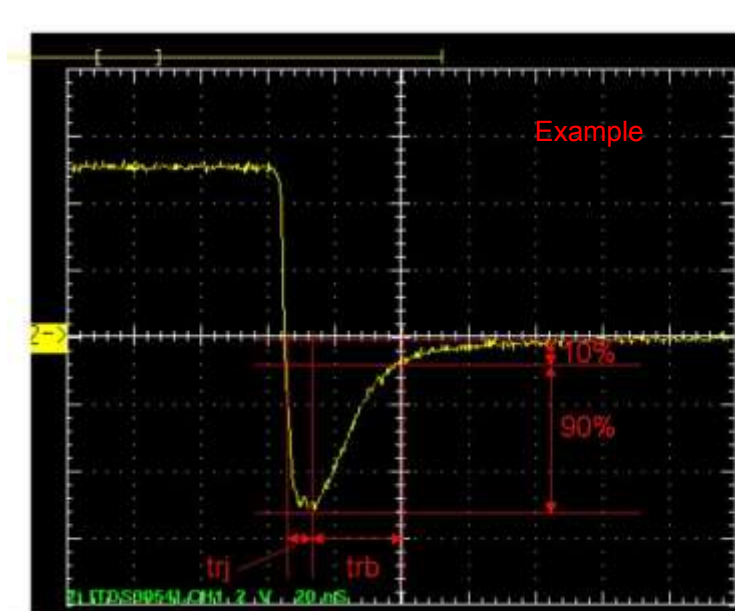
Reference



$T_{rr}=7.4(\text{ns})$

$T_{rb}=5.8(\text{ns})$

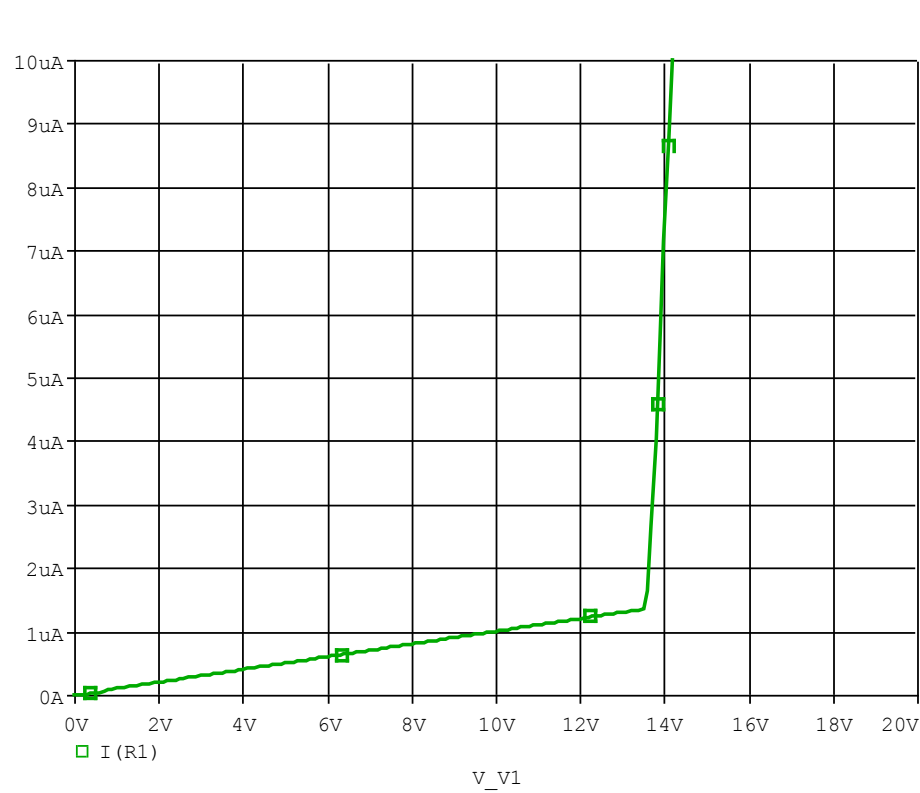
Conditions: $I_{fwd}=I_{rev}=0.2(\text{A})$, $R_I=50$



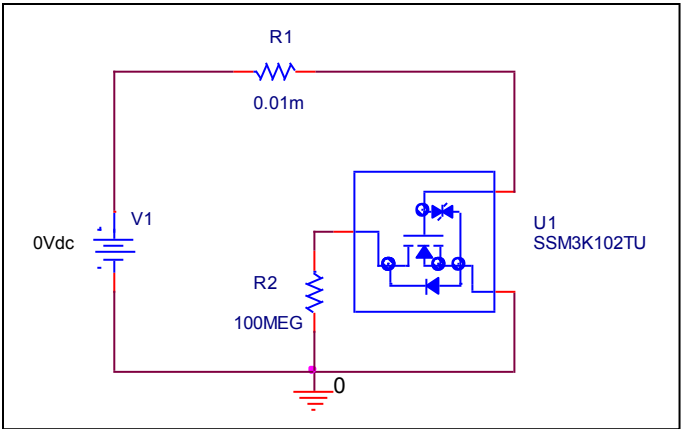
Relation between t_{rr} and t_{rb}

Zener Voltage Characteristic

Circuit Simulation Result



Evaluation Circuit



Zener Voltage Characteristic

Reference

