

# Device Modeling Report

COMPONENTS: Power MOSFET (Model Parameter)  
PART NUMBER: SSM3K116TU  
MANUFACTURER: TOSHIBA  
Body Diode (Model Parameter) / ESD Protection Diode

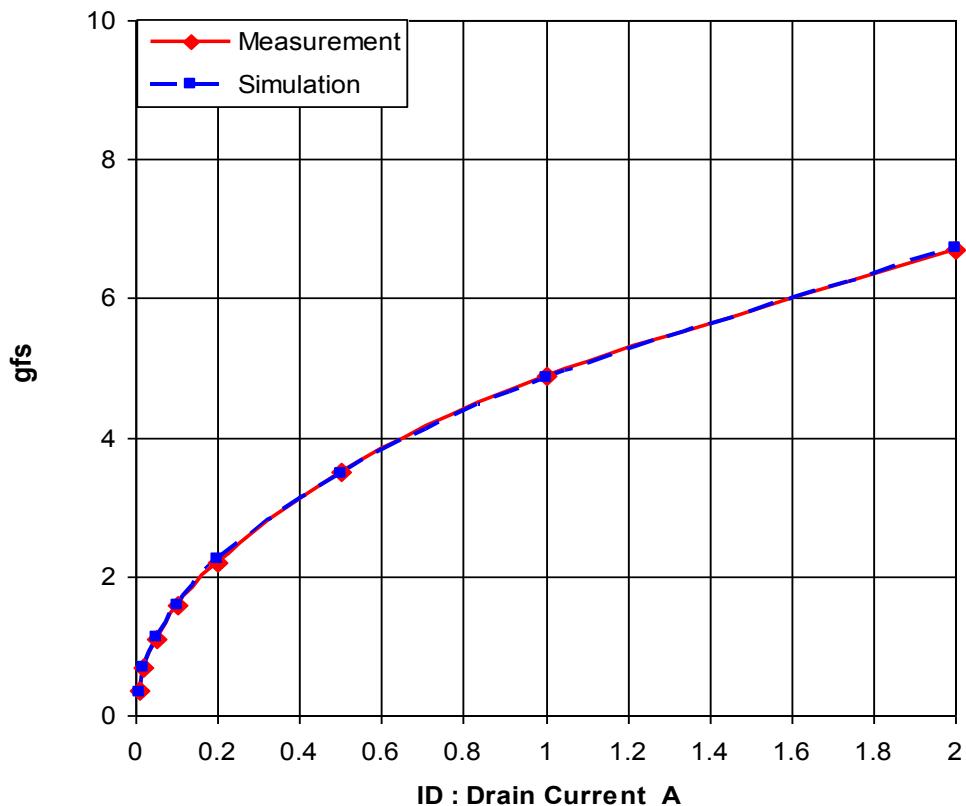


## MOSFET MODEL

Pspice model parameter	Model description
LEVEL	
L	Channel Length
W	Channel Width
KP	Transconductance
RS	Source Ohmic Resistance
RD	Ohmic Drain Resistance
VTO	Zero-bias Threshold Voltage
RDS	Drain-Source Shunt Resistance
TOX	Gate Oxide Thickness
CGSO	Zero-bias Gate-Source Capacitance
CGDO	Zero-bias Gate-Drain Capacitance
CBD	Zero-bias Bulk-Drain Junction Capacitance
MJ	Bulk Junction Grading Coefficient
PB	Bulk Junction Potential
FC	Bulk Junction Forward-bias Capacitance Coefficient
RG	Gate Ohmic Resistance
IS	Bulk Junction Saturation Current
N	Bulk Junction Emission Coefficient
RB	Bulk Series Resistance
PHI	Surface Inversion Potential
GAMMA	Body-effect Parameter
DELTA	Width effect on Threshold Voltage
ETA	Static Feedback on Threshold Voltage
THETA	Modility Modulation
KAPPA	Saturation Field Factor
VMAX	Maximum Drift Velocity of Carriers
XJ	Metallurgical Junction Depth
UO	Surface Mobility

## Transconductance Characteristic

Circuit Simulation Result

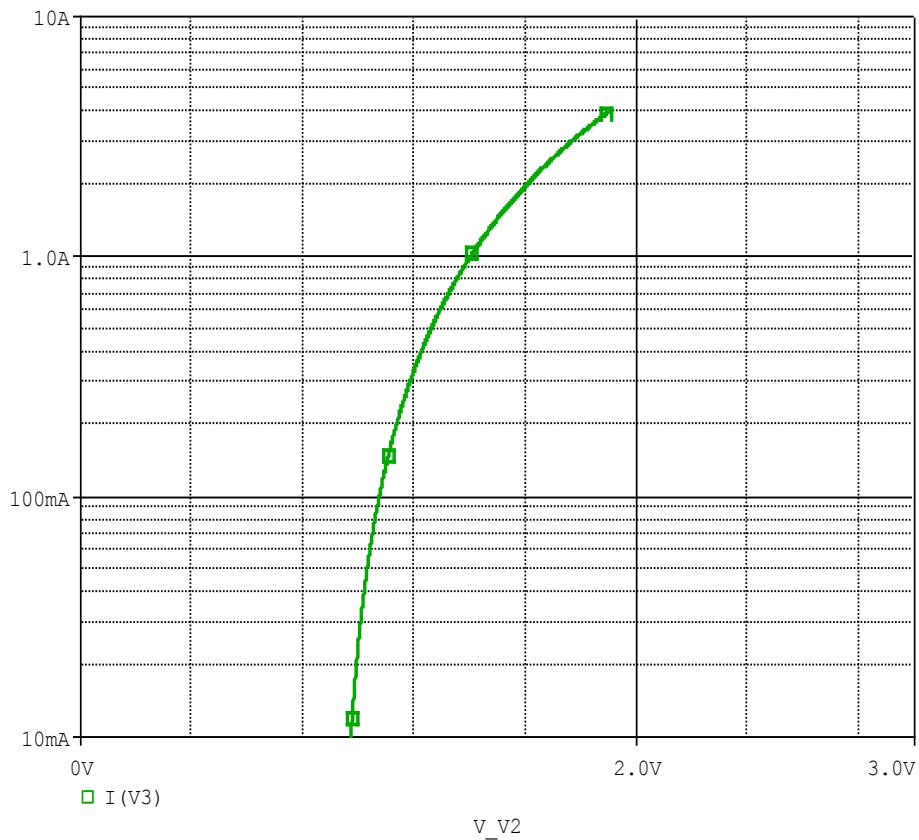


Comparison table

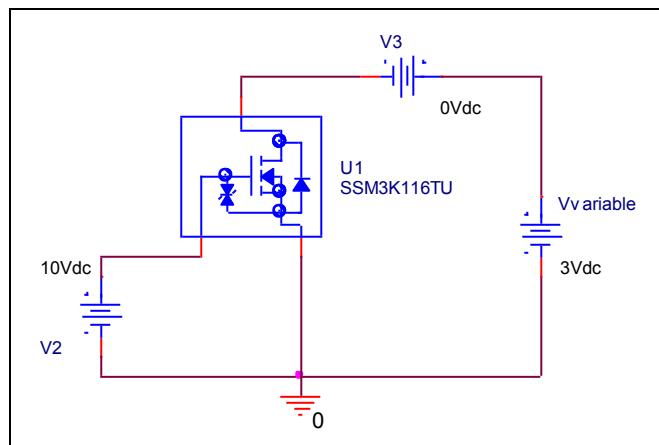
Id(A)	gfs		Error(%)
	Measurement	Simulation	
0.010	0.350	0.340	-2.857
0.020	0.700	0.686	-2.000
0.050	1.100	1.130	2.727
0.100	1.580	1.598	1.139
0.200	2.200	2.240	1.818
0.500	3.500	3.480	-0.571
1.000	4.880	4.850	-0.615
2.000	6.700	6.730	0.448

## V<sub>gs</sub>-I<sub>d</sub> Characteristic

Circuit Simulation result

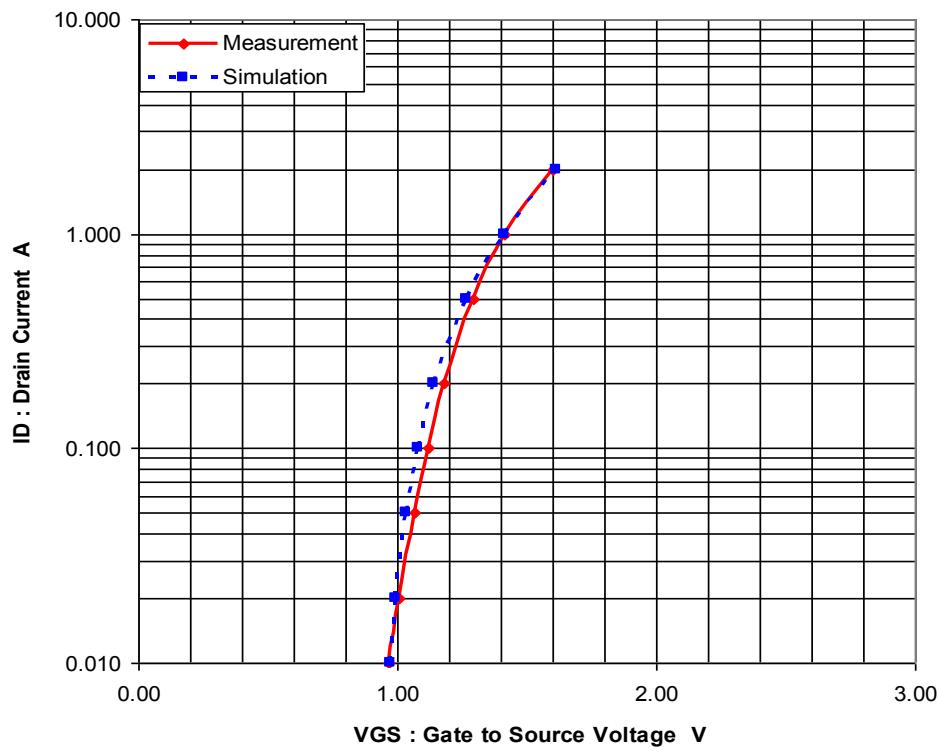


Evaluation circuit



## Comparison Graph

Circuit Simulation Result

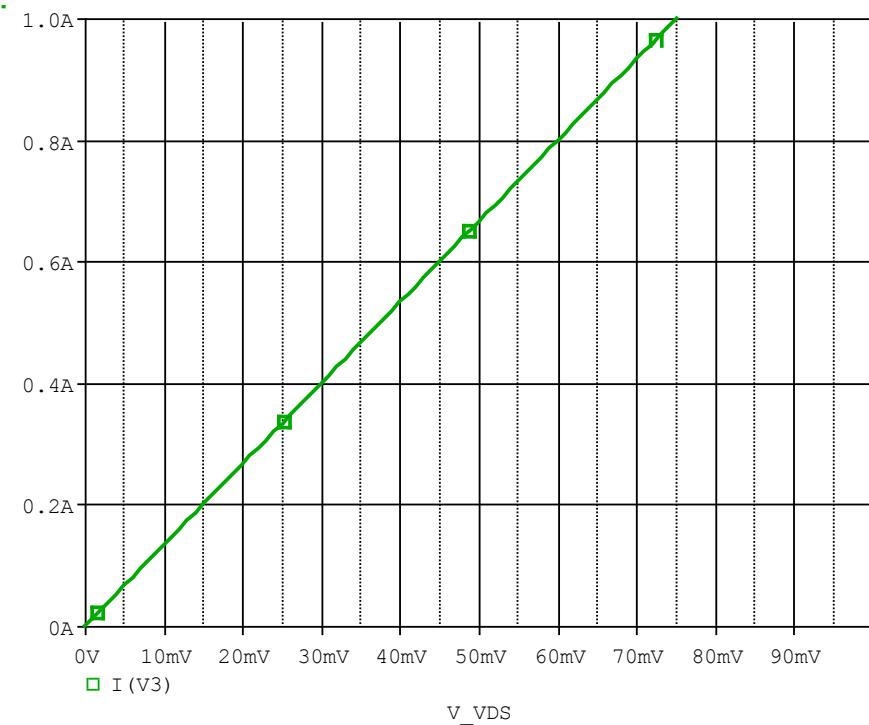


Simulation Result

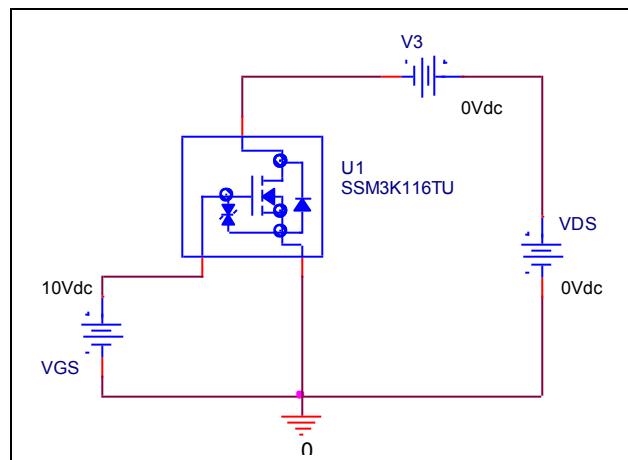
$I_D$ (A)	$V_{GS}$ (V)		Error (%)
	Measurement	Simulation	
0.010	0.968	0.976	0.826
0.020	1.008	0.995	-1.290
0.050	1.066	1.034	-3.002
0.100	1.120	1.078	-3.750
0.200	1.180	1.138	-3.559
0.500	1.290	1.267	-1.783
1.000	1.415	1.410	-0.353
2.000	1.600	1.612	0.750

## Rds(on) Characteristic

### Circuit Simulation result



### Evaluation circuit

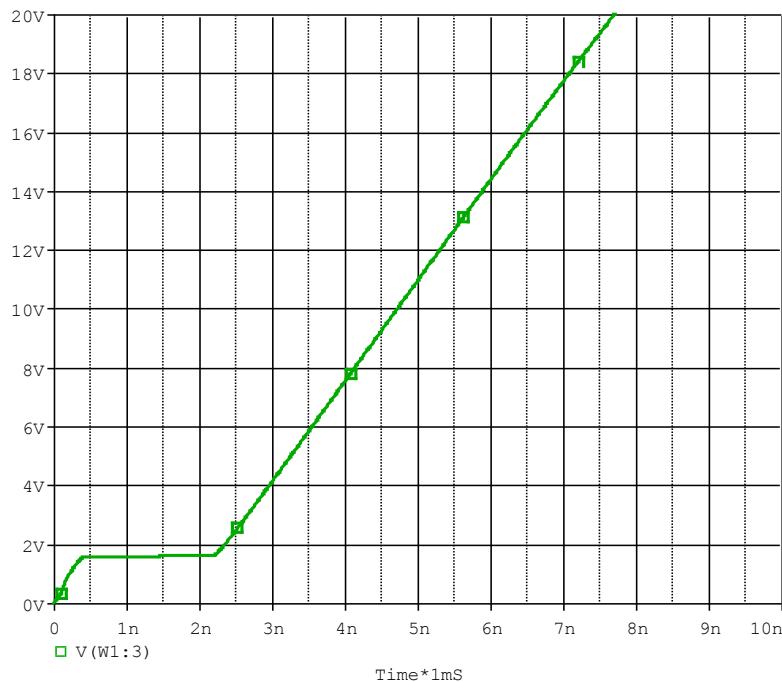


### Simulation Result

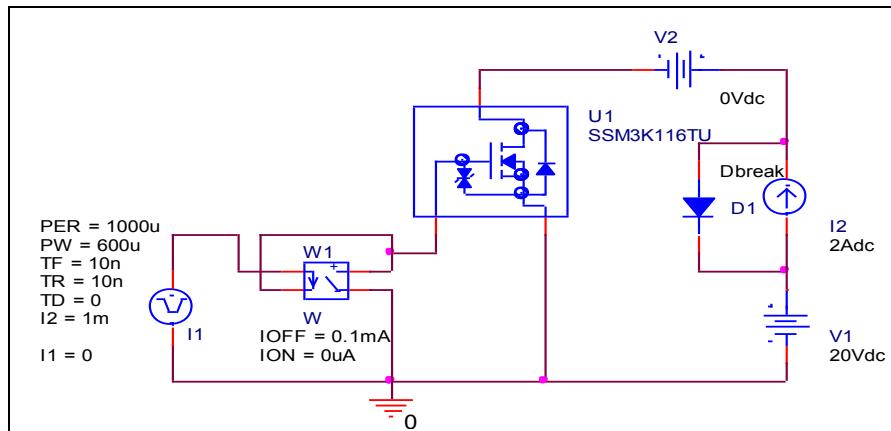
$I_D=0.5A, V_{GS}=4.5V$	Measurement	Simulation	Error (%)
$R_{DS(\text{on})} (\text{m}\Omega)$	75.000	75.095	0.127

## Gate Charge Characteristic

### Circuit Simulation result



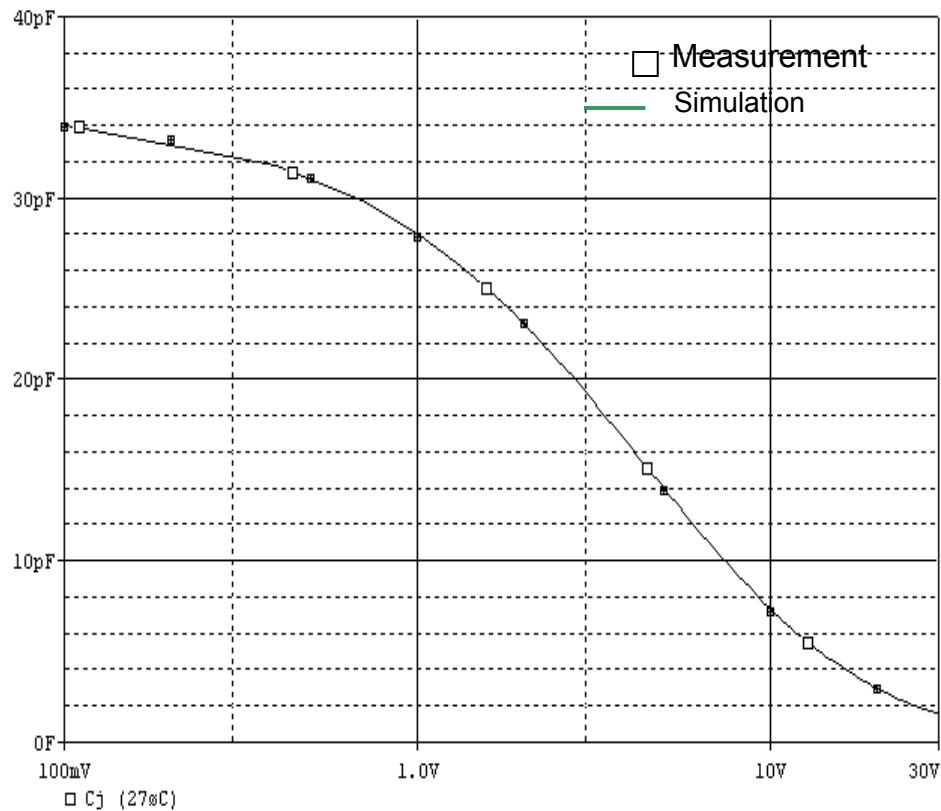
### Evaluation circuit



### Simulation Result

$V_{DD}=20V, I_D=2A$ , $V_{GS}=10V$	Measurement	Simulation	Error (%)
$Q_{gs}(nc)$	0.400	0.401	0.250
$Q_{gd}(nc)$	1.800	1.808	0.444
$Q_g(nc)$	7.000	4.730	-32.429

## Capacitance Characteristic

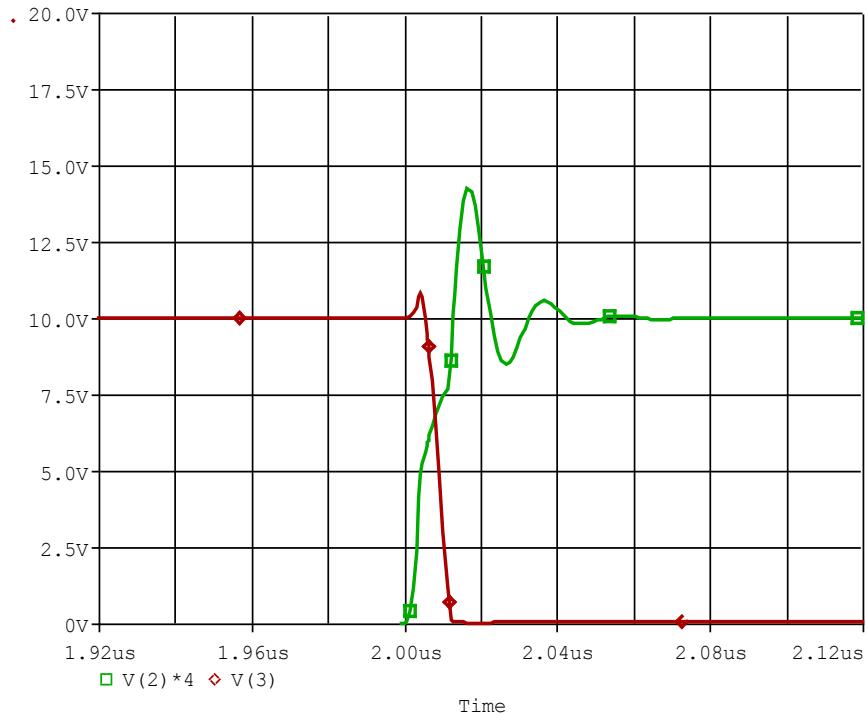


**Simulation Result**

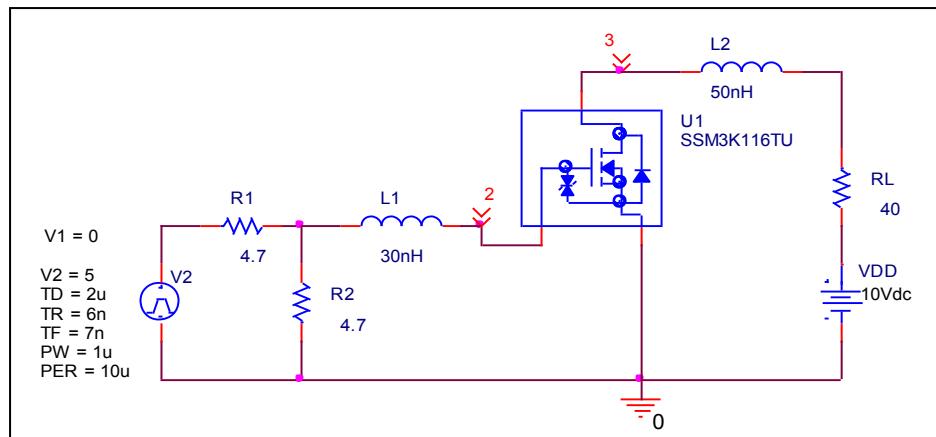
V <sub>DS</sub> (V)	C <sub>bd</sub> (pF)		Error(%)
	Measurement	Simulation	
0.100	34.000	34.005	0.015
0.200	33.300	33.250	-0.150
0.500	31.200	31.160	-0.128
1.000	27.900	28.060	0.573
2.000	23.200	23.080	-0.517
5.000	14.000	13.995	-0.036
10.000	7.300	7.350	0.685
20.000	3.000	2.998	-0.067

## Switching Time Characteristic

Circuit Simulation result



Evaluation circuit

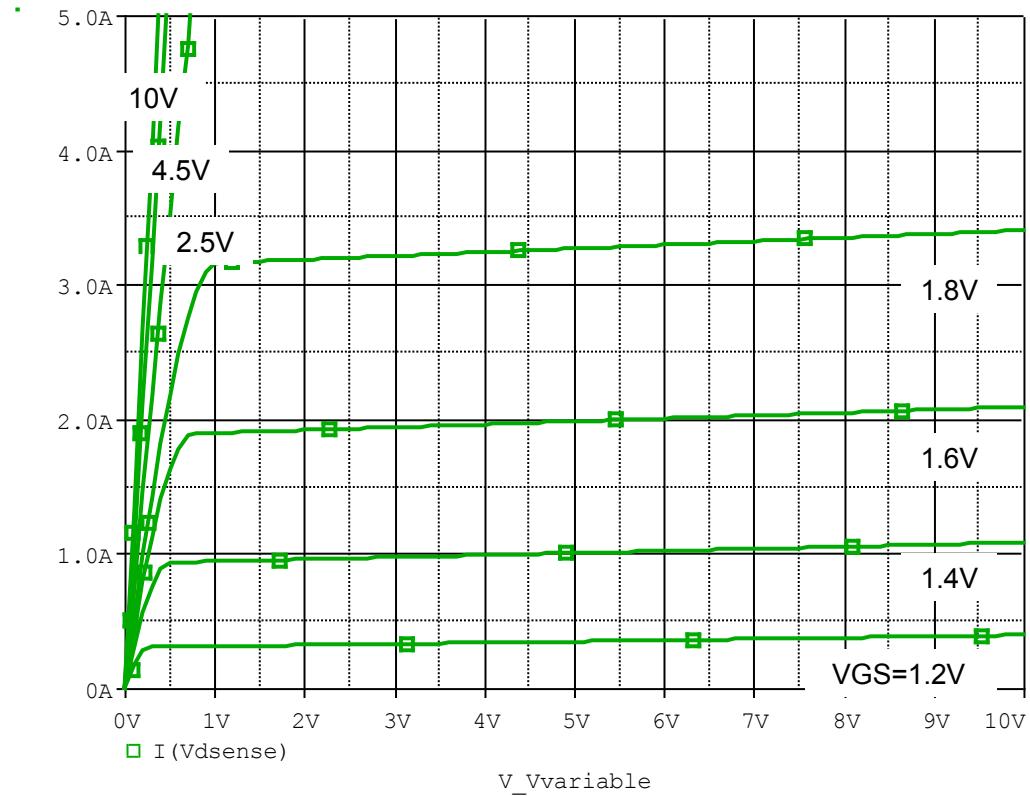


Simulation Result

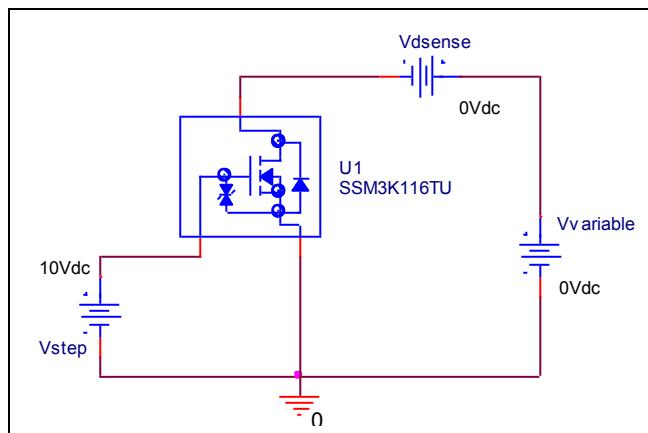
$I_D=0.25A, V_{DD}=10V$ $V_{GS}=2.5V$	Measurement	Simulation	Error(%)
Ton(ns)	9.000	9.032	0.356

## Output Characteristic

Circuit Simulation result

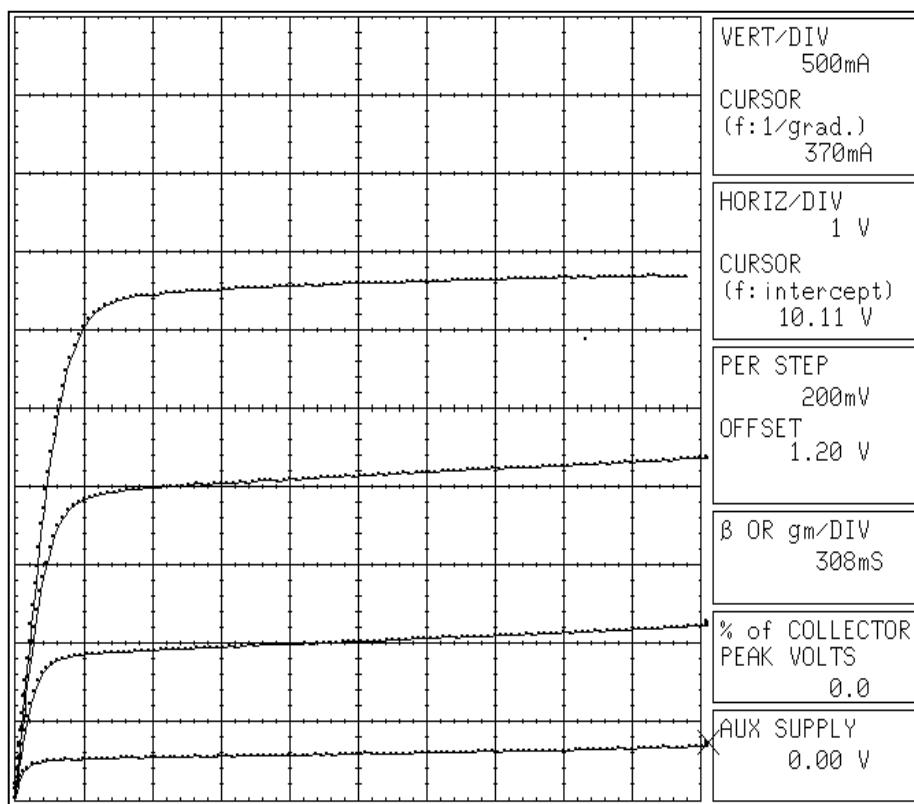


Evaluation circuit



## Output Characteristic

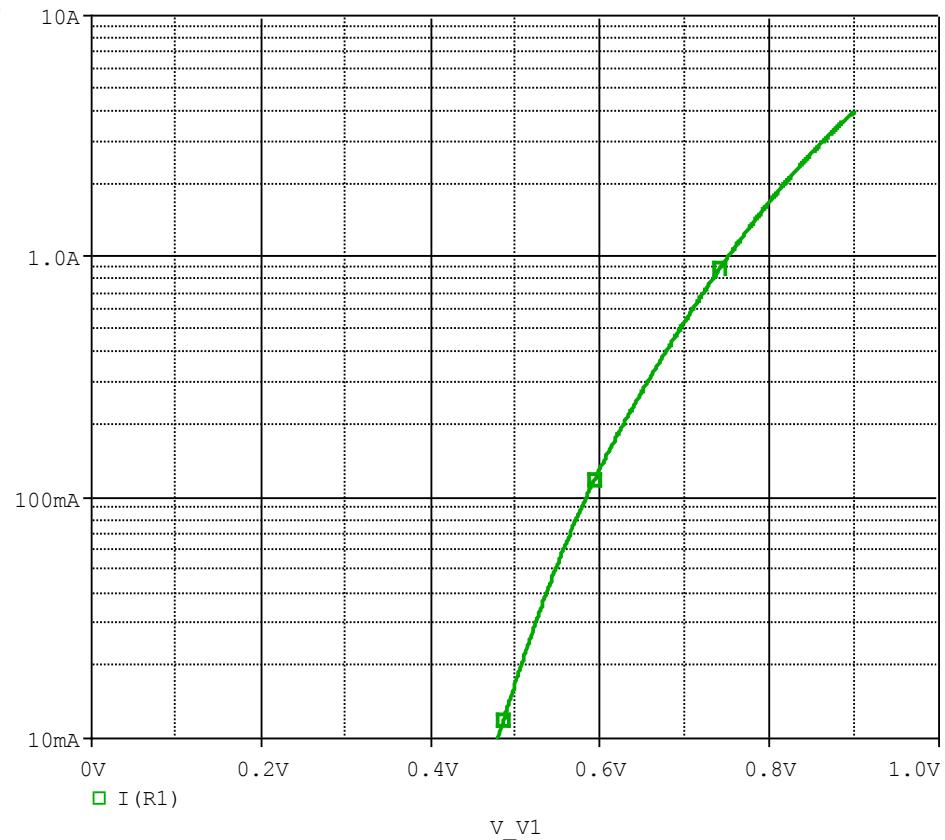
## Reference



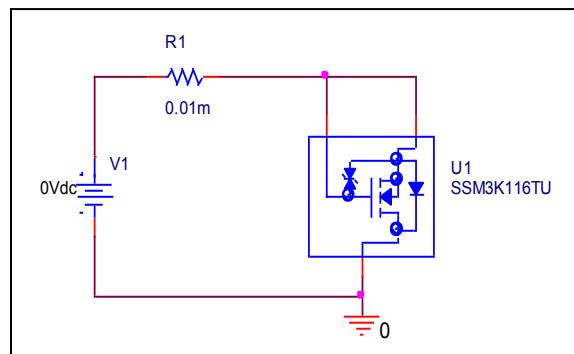
## BODY DIODE SPICE MODEL

### Forward Current Characteristic

Circuit Simulation Result

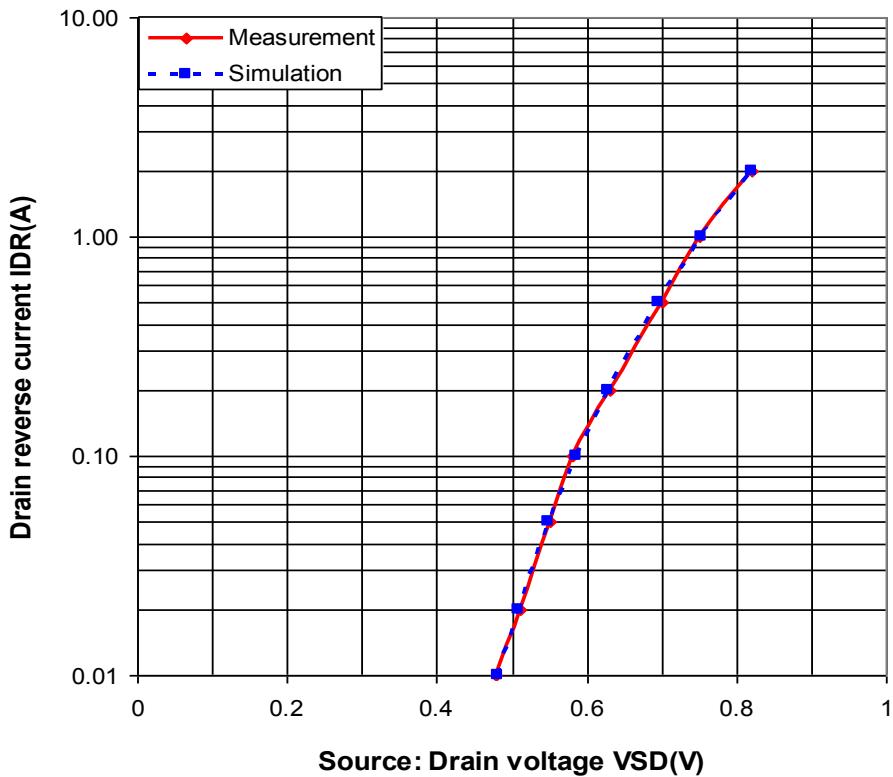


Evaluation Circuit



## Comparison Graph

Circuit Simulation Result

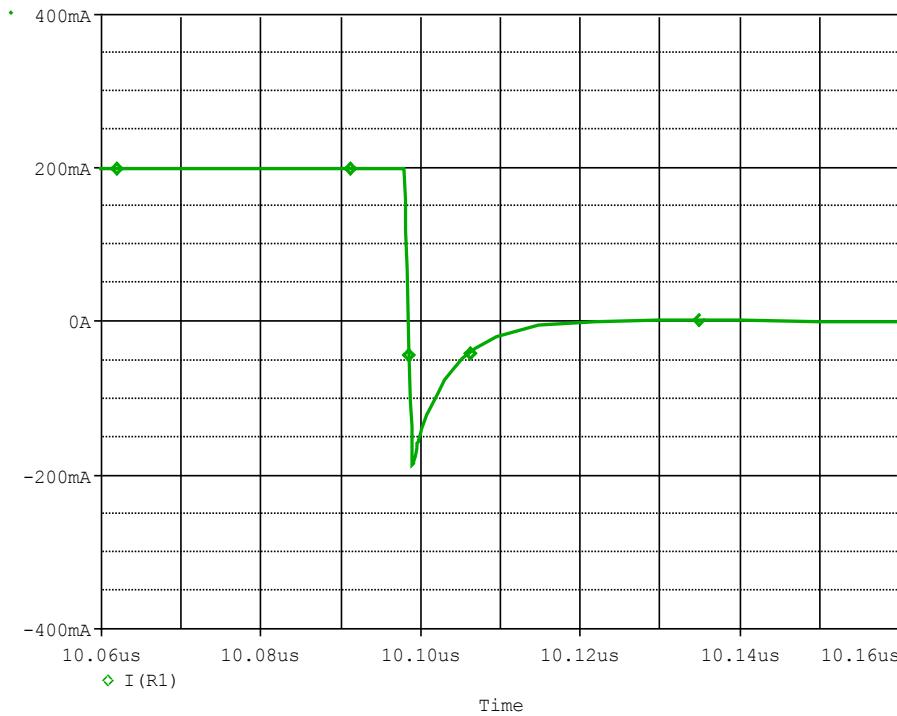


Simulation Result

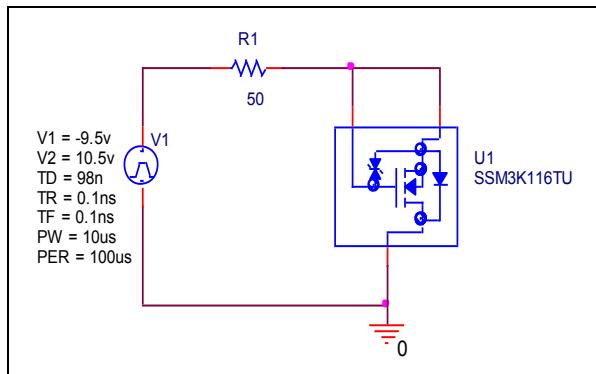
IDR(A)	VSD(V) Measurement	VSD(V) Simulation	%Error
0.010	0.480	0.481	0.208
0.020	0.510	0.509	-0.196
0.050	0.550	0.549	-0.182
0.100	0.580	0.585	0.862
0.200	0.630	0.629	-0.159
0.500	0.700	0.695	-0.714
1.000	0.750	0.754	0.533
2.000	0.820	0.821	0.122

## Reverse Recovery Characteristic

### Circuit Simulation Result



### Evaluation Circuit

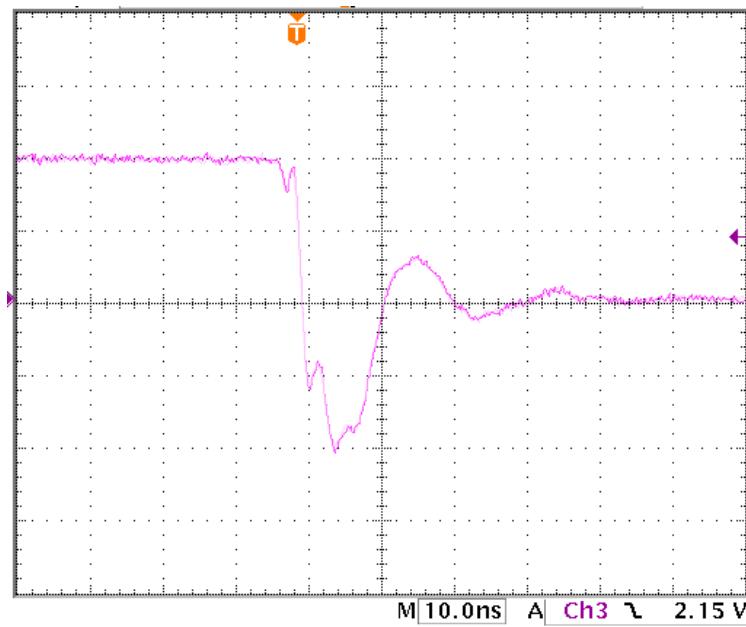


### Compare Measurement vs. Simulation

	Measurement	Simulation	Error (%)
$\text{Trr}(\text{ns})$	10.600	11.090	4.623

## Reverse Recovery Characteristic

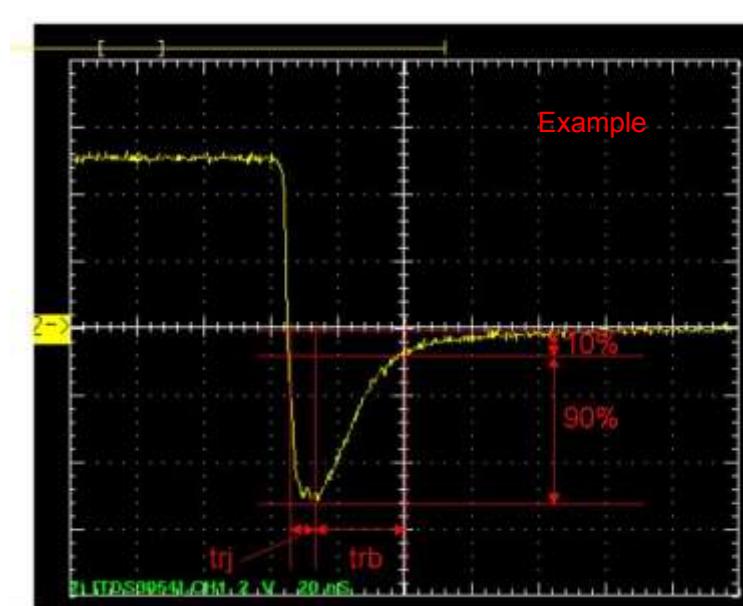
## Reference



Trj=4.2(ns)

Trb=6.4(ns)

Conditions: Ifwd=Irev=0.2(A), RI=50

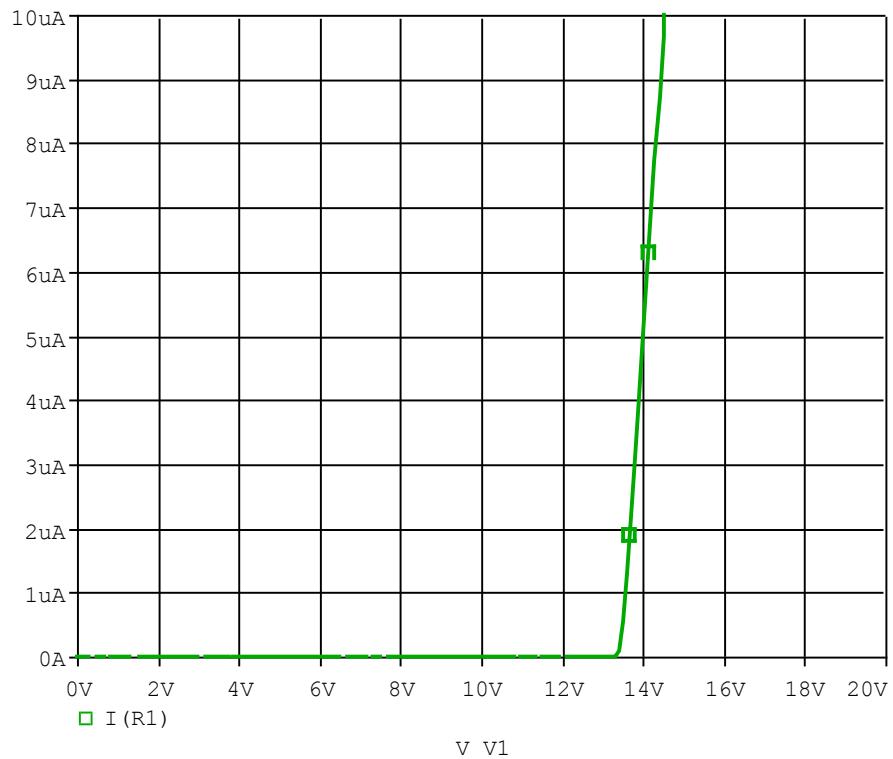


Relation between trj and trb

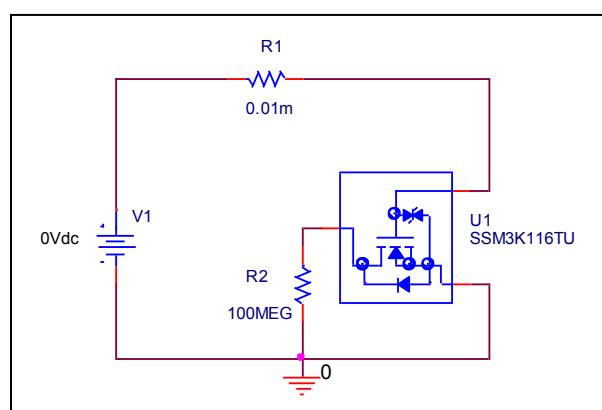
## ESD PROTECTION DIODE SPICE MODEL

### Zener Voltage Characteristic

Circuit Simulation Result



Evaluation Circuit



## Zener Voltage Characteristic

## Reference

