Device Modeling Report

COMPONENTS: Power MOSFET (Professional)
PART NUMBER: TPC6005
MANUFACTURER: TOSHIBA
Body Diode (Professional) / ESD Protection Diode

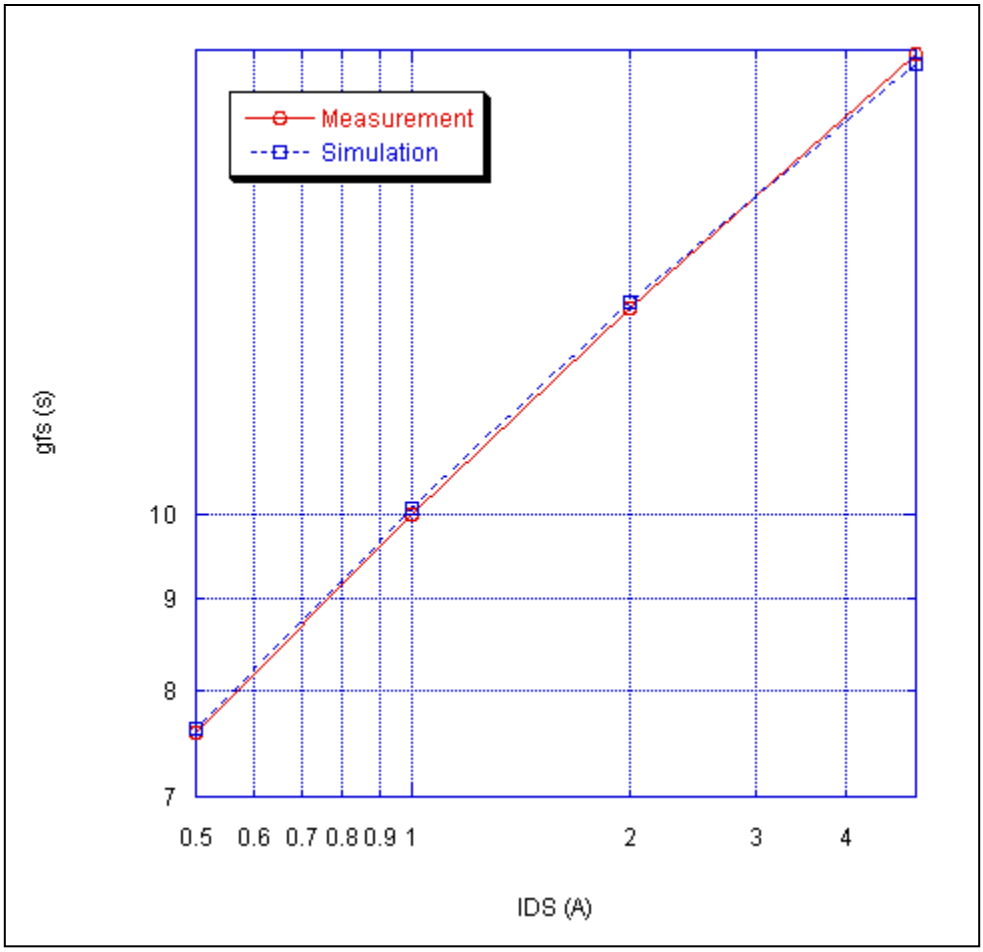
Bee Technologies Inc.

POWER MOSFET MODEL

Pspice model parameter	Model description
LEVEL	
L	Channel Length
W	Channel Width
KP	Transconductance
RS	Source Ohmic Resistance
RD	Ohmic Drain Resistance
VTO	Zero-bias Threshold Voltage
RDS	Drain-Source Shunt Resistance
TOX	Gate Oxide Thickness
CGSO	Zero-bias Gate-Source Capacitance
CGDO	Zero-bias Gate-Drain Capacitance
CBD	Zero-bias Bulk-Drain Junction Capacitance
MJ	Bulk Junction Grading Coefficient
PB	Bulk Junction Potential
FC	Bulk Junction Forward-bias Capacitance Coefficient
RG	Gate Ohmic Resistance
IS	Bulk Junction Saturation Current
N	Bulk Junction Emission Coefficient
RB	Bulk Series Resistance
PHI	Surface Inversion Potential
GAMMA	Body-effect Parameter
DELTA	Width effect on Threshold Voltage
ETA	Static Feedback on Threshold Voltage
THETA	Modility Modulation
KAPPA	Saturation Field Factor
VMAX	Maximum Drift Velocity of Carriers
XJ	Metallurgical Junction Depth
UO	Surface Mobility

Transconductance Characteristic

Circuit Simulation Result

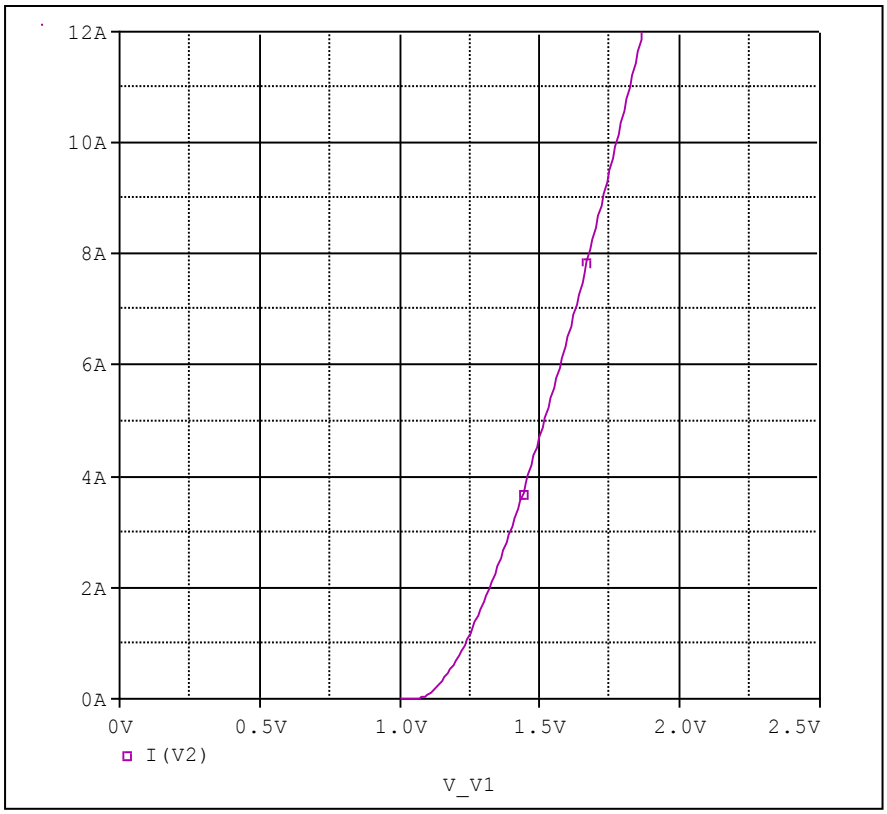


Comparison table

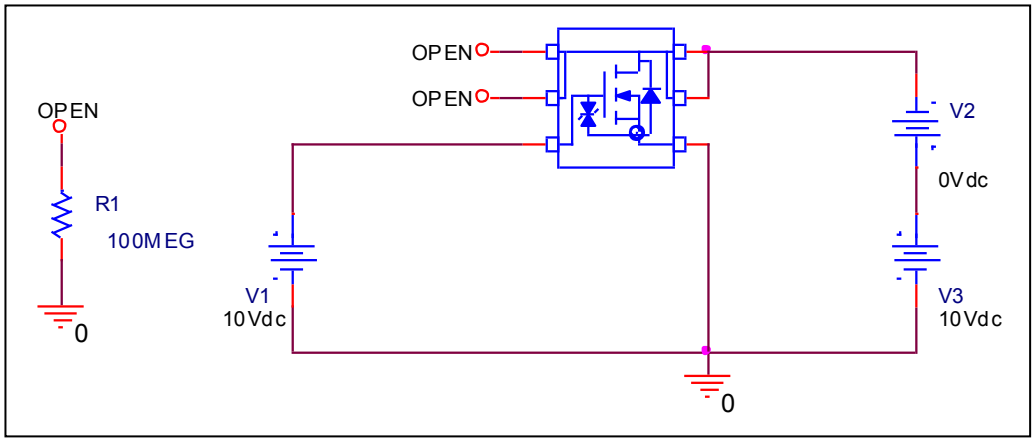
I_D(A)	V_{GS}(V)		Error (%)
	Measurement	Simulation	
0.50	7.60	7.63	0.39
1.00	10.00	10.09	0.90
2.00	13.00	13.10	0.77
5.00	17.90	17.70	-1.12

Vgs-Id Characteristic

Circuit Simulation result

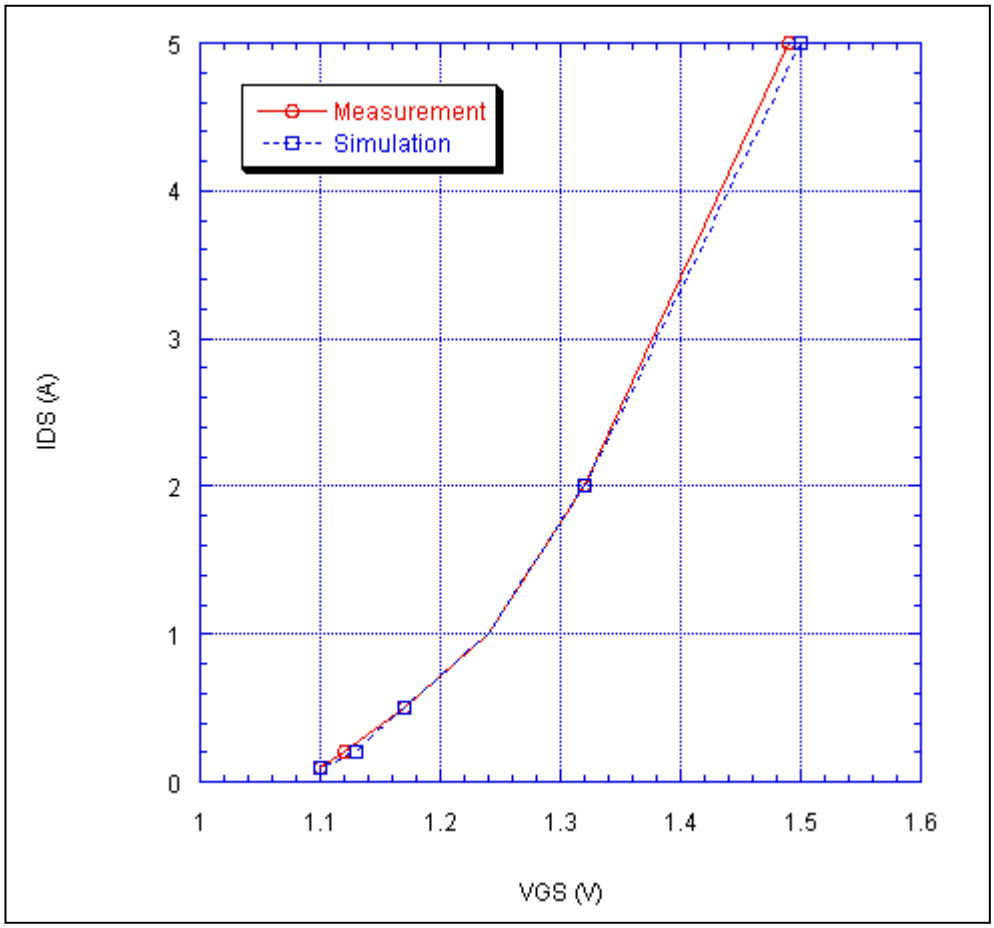


Evaluation circuit



Comparison Graph

Circuit Simulation Result

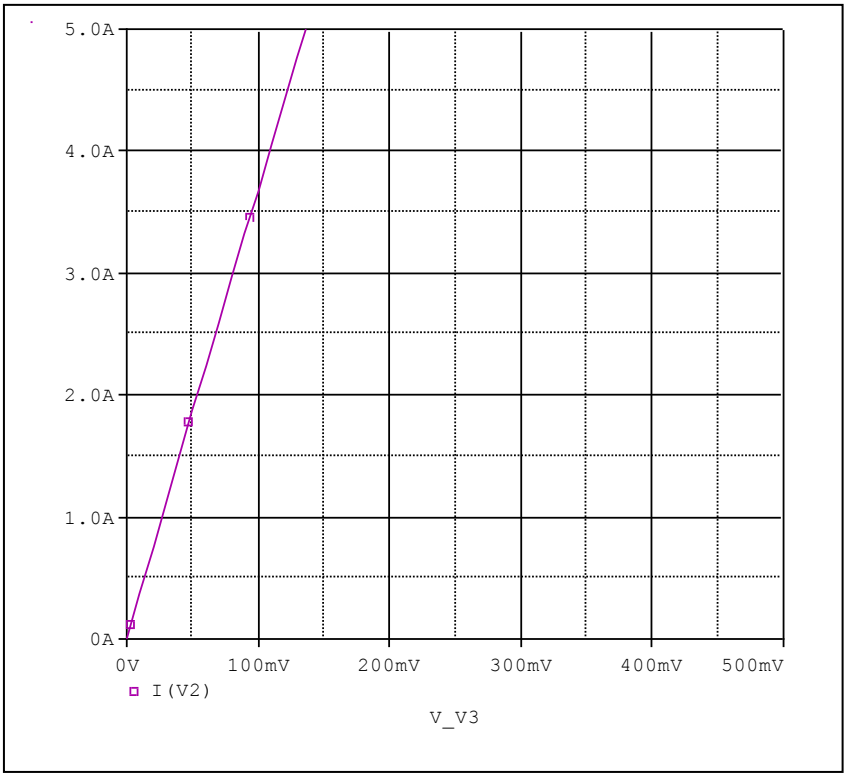


Simulation Result

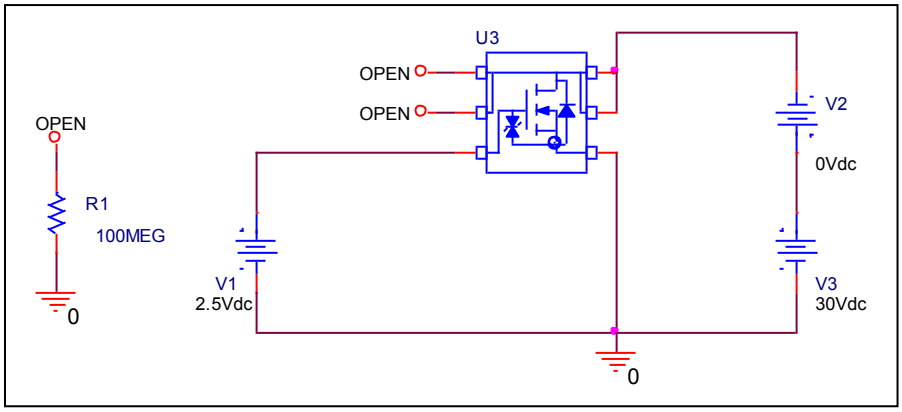
I_D(A)	V_{GS}(V)		Error (%)
	Measurement	Simulation	
0.10	1.10	1.10	0.00
0.20	1.12	1.13	0.89
0.50	1.17	1.17	0.00
1.00	1.24	1.24	-0.40
2.00	1.32	1.32	0.00
5.00	1.49	1.50	0.67

Id-Rds(on) Characteristic

Circuit Simulation result



Evaluation circuit

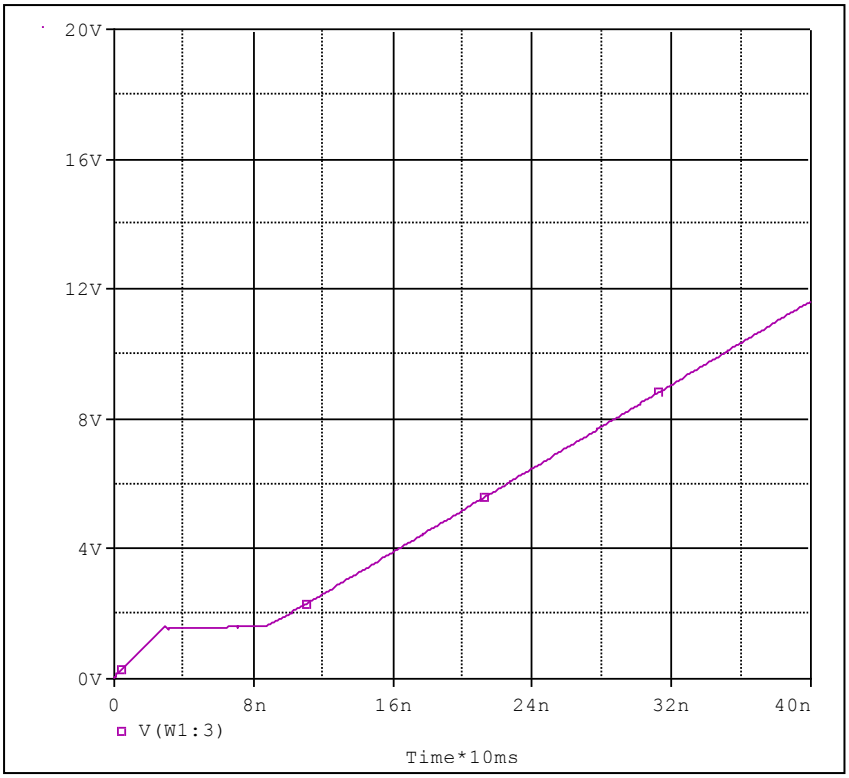


Simulation Result

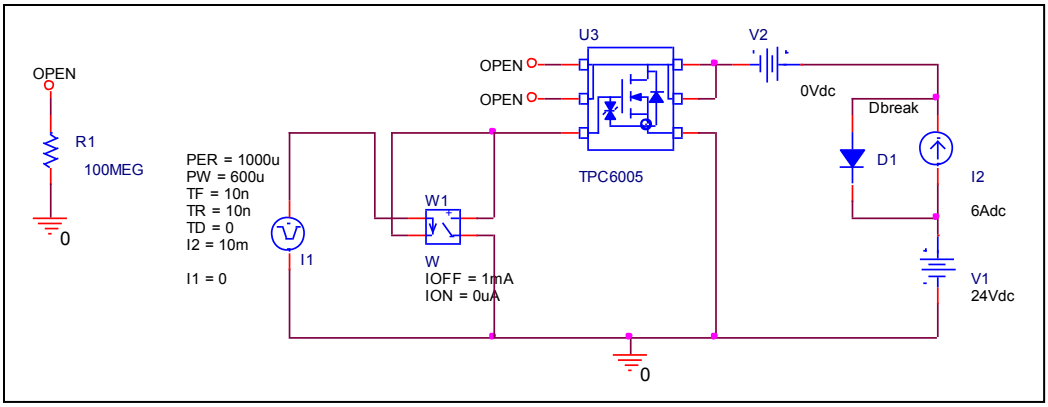
I_D=3, V_{GS}=2.5V	Measurement		Simulation		Error (%)
R_{DS} (on)	27.00	mΩ	27.00	mΩ	0.00

Gate Charge Characteristic

Circuit Simulation result



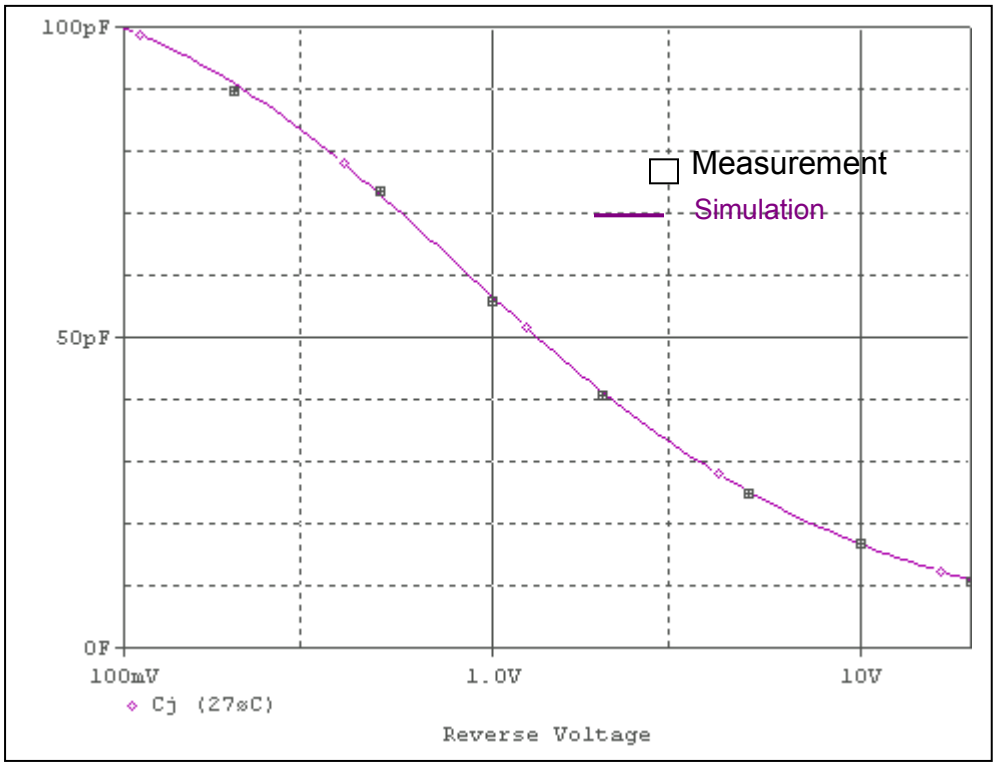
Evaluation circuit



Simulation Result

V_{DD}=24V,I_D=6A ,V_{GS}=5V	Measurement		Simulation		Error (%)
Qgs	3.5	nC	3.4066	nC	-2.74174
Qgd	5.5	nC	5.4945	nC	-0.1001
Qg	19	nC	19.344	nC	1.778329

Capacitance Characteristic

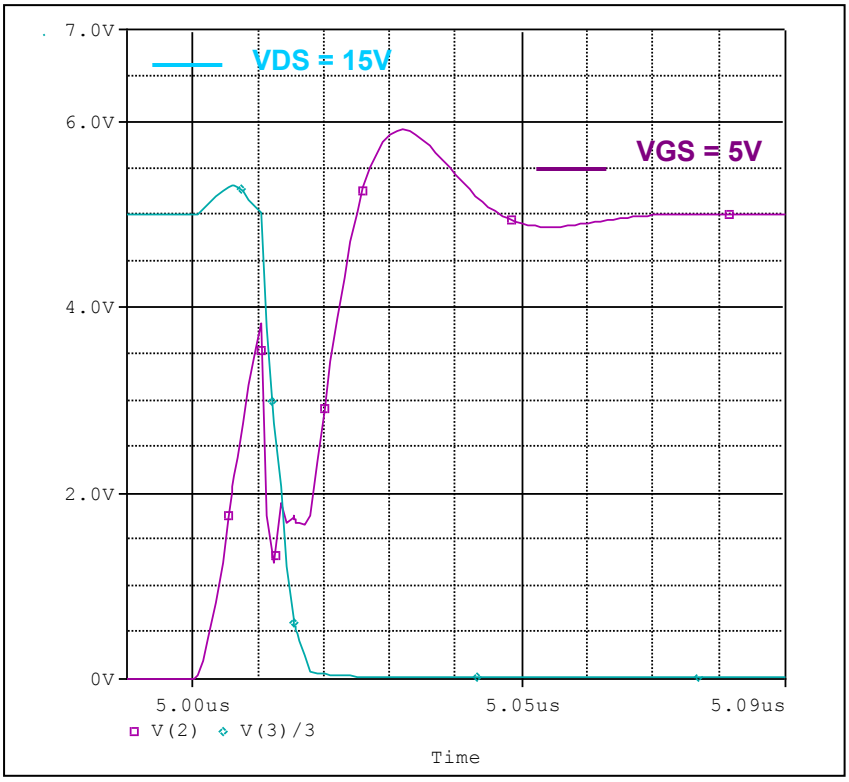


Simulation Result

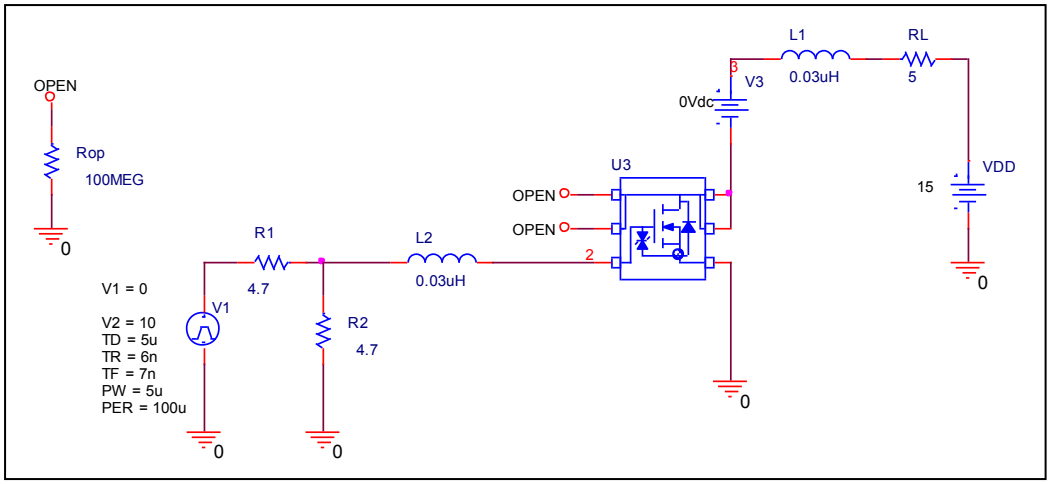
V_{DS}(V)	Cbd(pF)		Error(%)
	Measurement	Simulation	
0.10	100.00	100.00	0.00
0.20	90.00	91.00	1.11
0.50	74.00	74.00	0.00
1.00	56.00	57.00	1.79
2.00	41.00	42.00	2.44
5.00	25.00	25.50	2.00
10.00	17.00	17.00	0.00
20.00	11.00	11.30	2.73

Switching Time Characteristic

Circuit Simulation result



Evaluation circuit

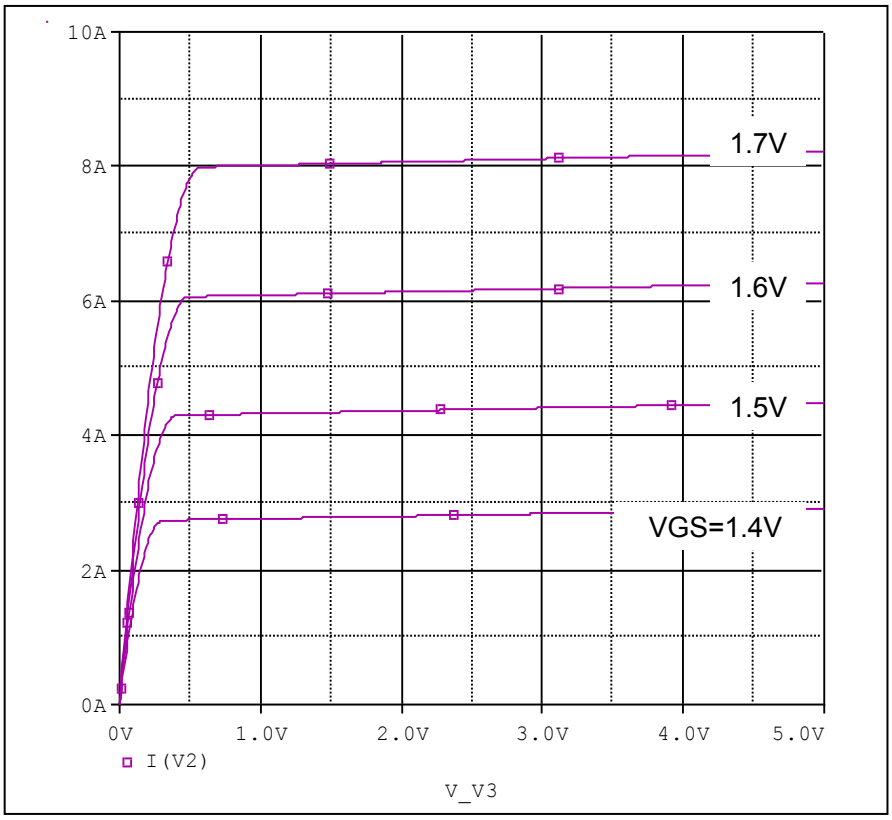


Simulation Result

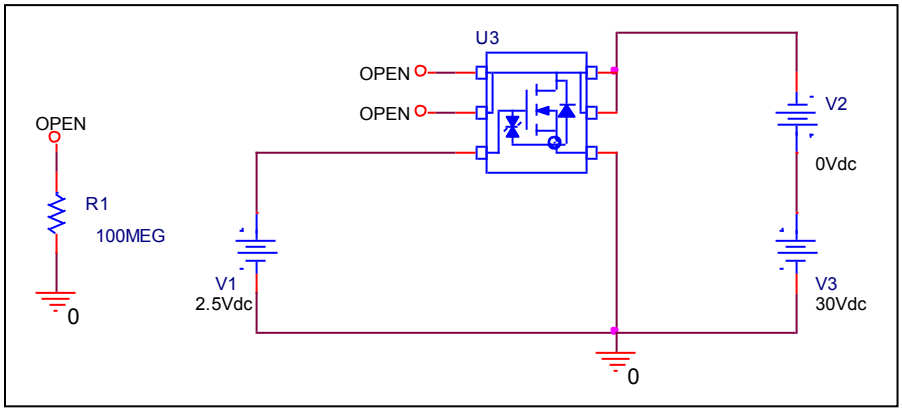
I_D=3A, V_{DD}=15V V_{GS}=0/5V	Measurement		Simulation		Error(%)
td (on)	13.00	ns	13.01	ns	0.04

Output Characteristic

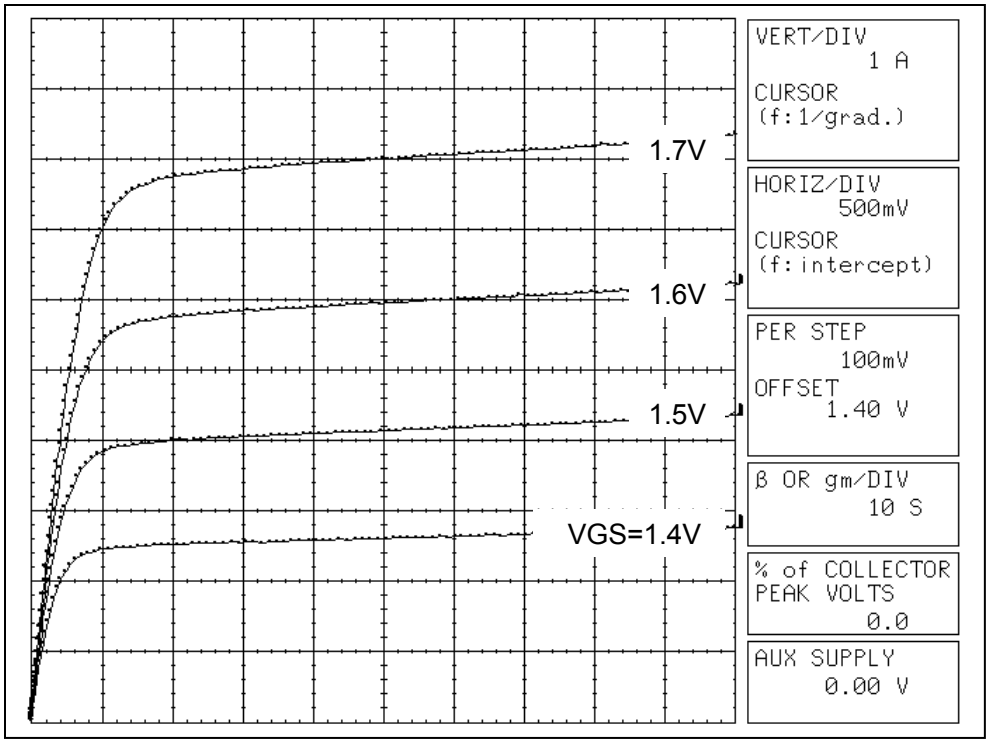
Circuit Simulation result



Evaluation circuit

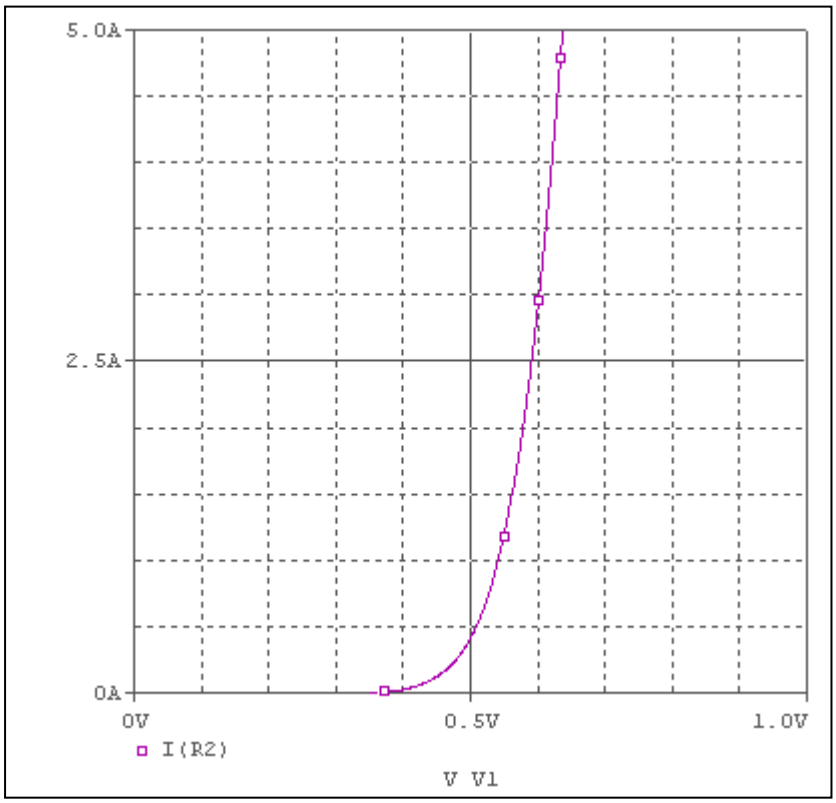


Output Characteristic Reference

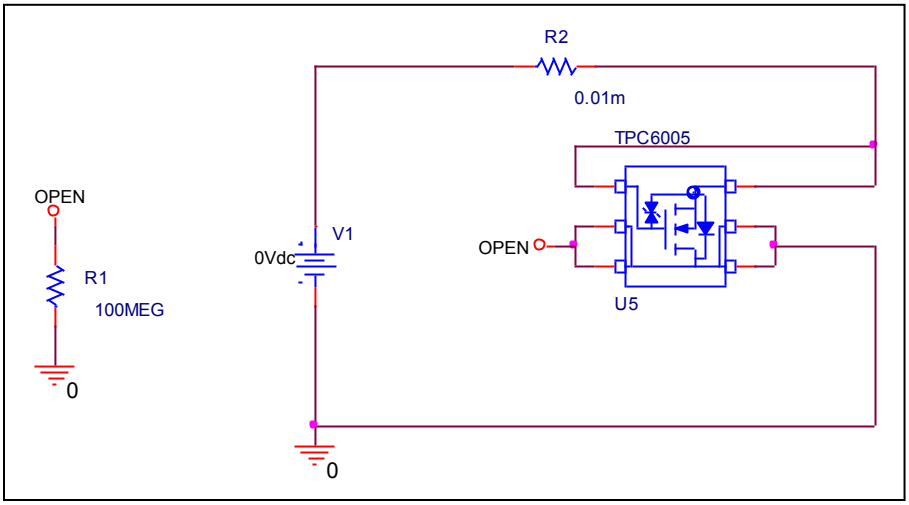


Forward Current Characteristic

Circuit Simulation Result

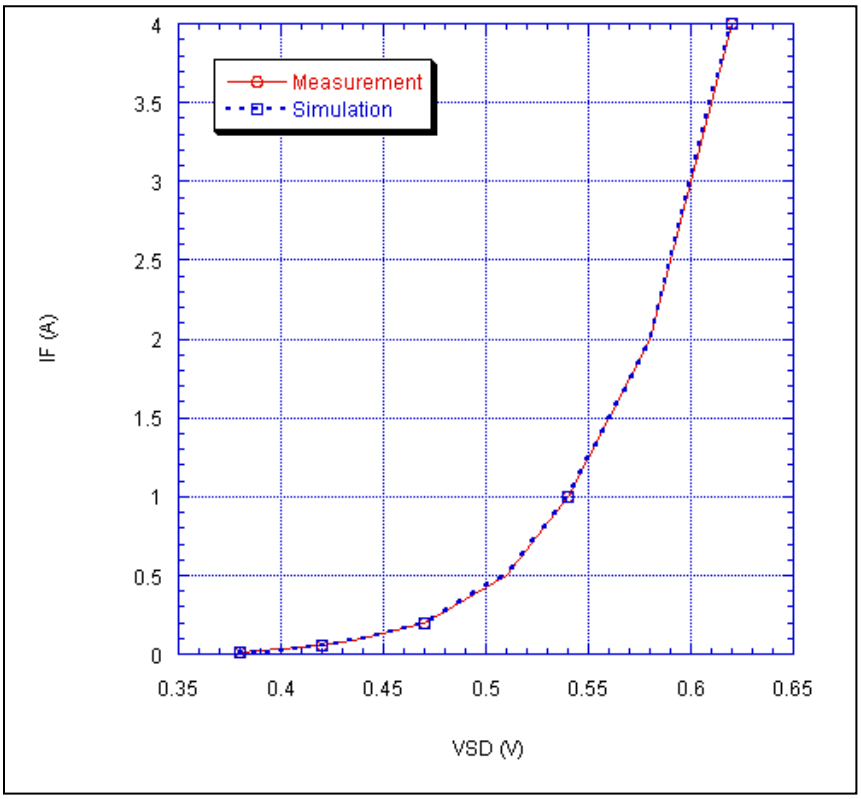


Evaluation Circuit



Comparison Graph

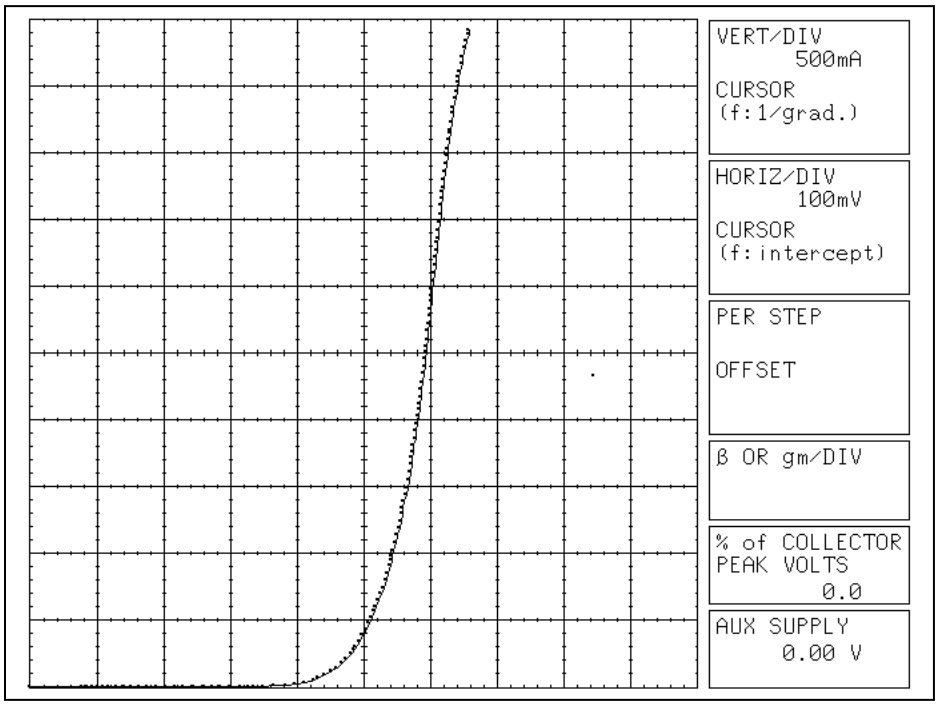
Circuit Simulation Result



Simulation Result

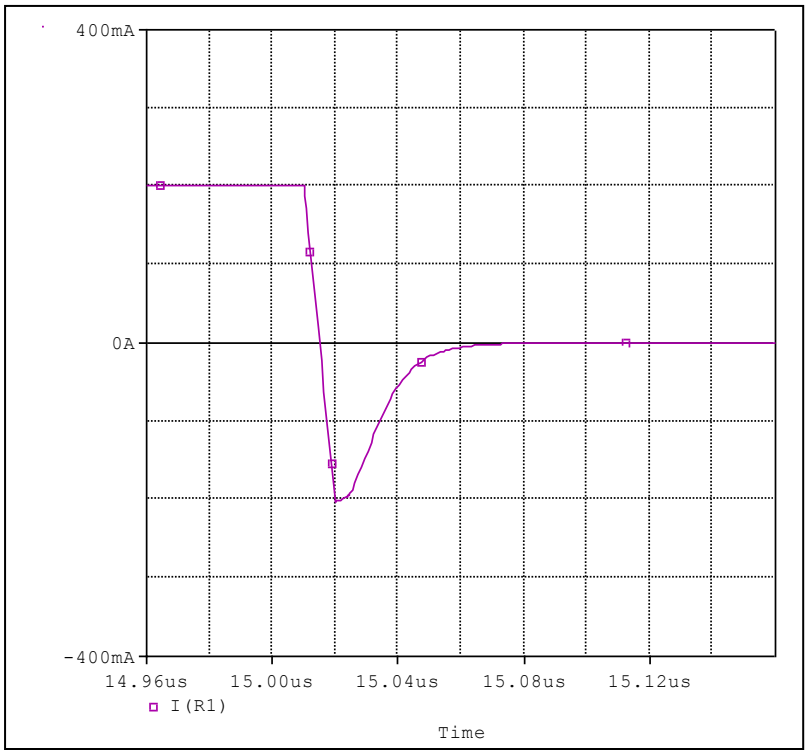
Ifwd(A)	Vfwd(V) Measurement	Vfwd(V) Simulation	%Error
0.01	0.38	0.38	-1.30
0.02	0.39	0.40	0.51
0.05	0.42	0.42	0.00
0.10	0.44	0.44	0.45
0.20	0.47	0.47	-0.21
0.50	0.51	0.51	-0.20
1.00	0.54	0.54	0.19
2.00	0.58	0.58	-0.52
4.00	0.62	0.62	0.00

Forward Current Characteristic Reference

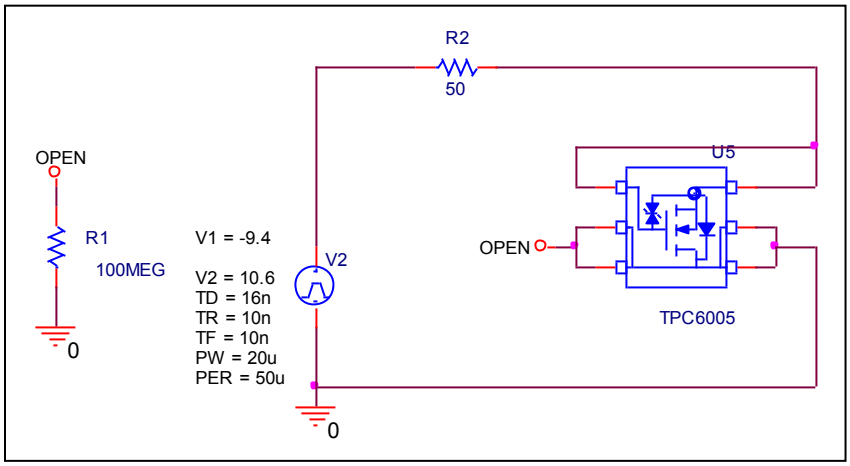


Reverse Recovery Characteristic

Circuit Simulation Result



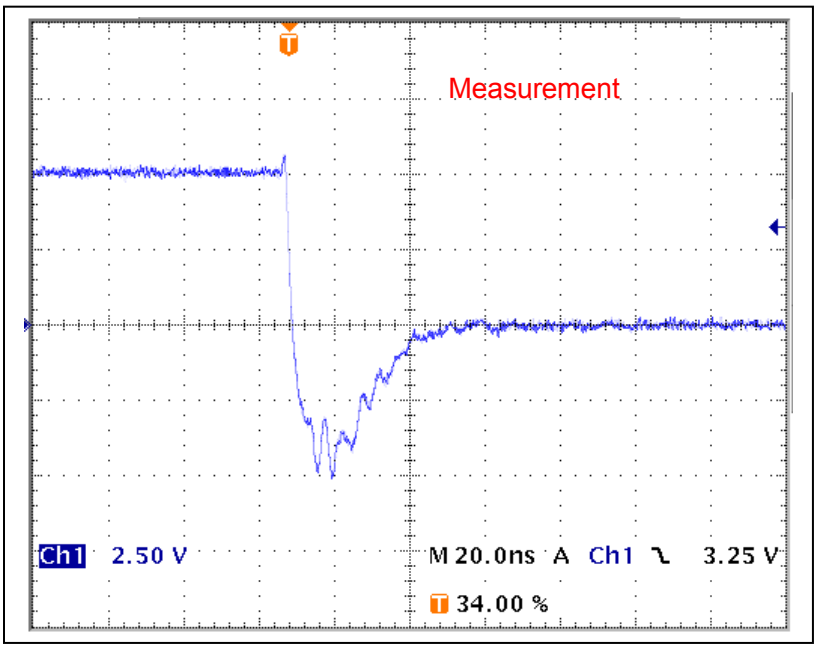
Evaluation circuit



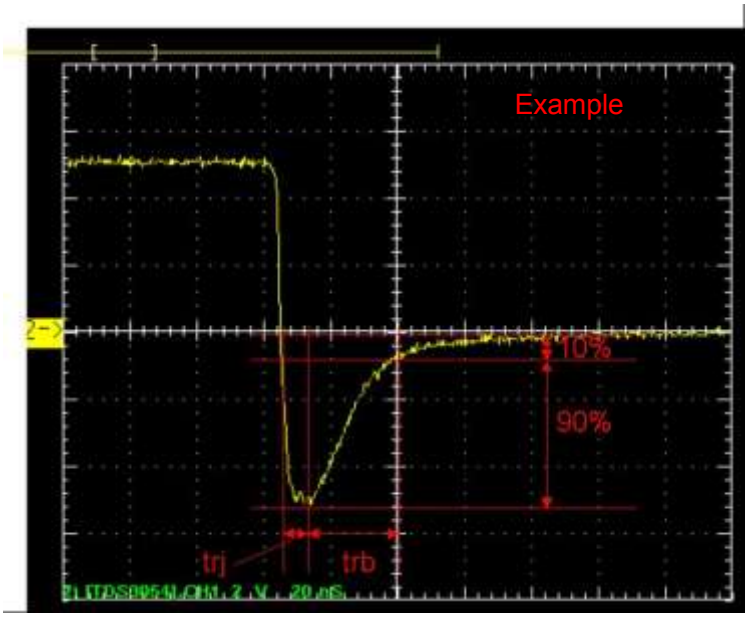
Compare Measurement vs. Simulation

	Measurement		Simulation		Error(%)
trj	10.2	ns	10.173	ns	-0.26541
trb	21.8	ns	22.798	ns	4.377577
trr	32.0	ns	32.971	ns	2.945012

Reverse Recovery Characteristic Reference



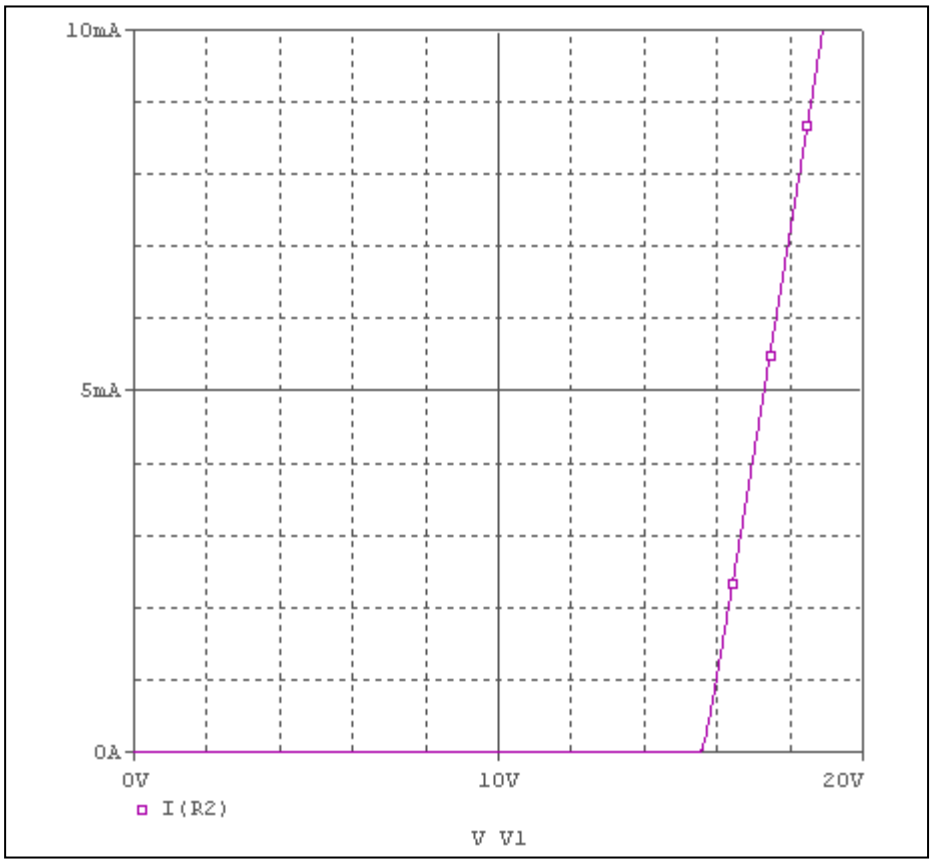
trj=10.2(ns)
trb=21.8(ns)
Conditions:Ifwd=Irev=0.2(A),Rl=50



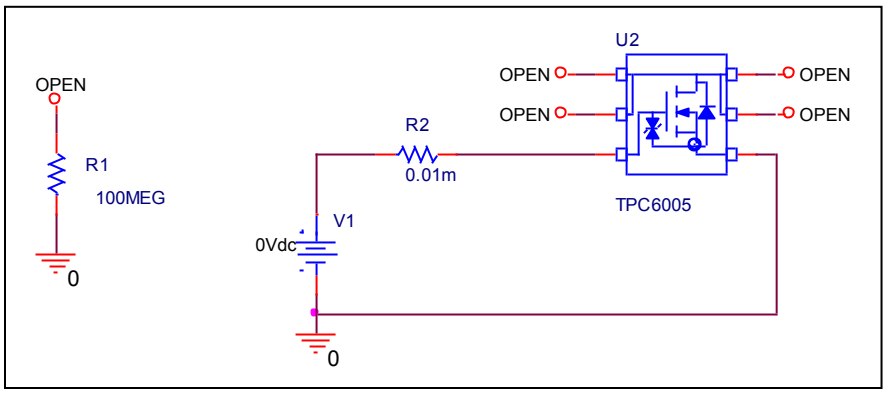
Relation between trj and trb

Zener Voltage Characteristic

Circuit Simulation Result



Evaluation Circuit



Zener Voltage Characteristic Reference