Device Modeling Report

COMPONENTS: Power MOSFET (Professional)
PART NUMBER: TPC8024-H
MANUFACTURER: TOSHIBA
Body Diode (Professional) / ESD Protection Diode

Bee Technologies Inc.

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MOSFET MODEL

Pspice model parameter	Model description
LEVEL	
L	Channel Length
W	Channel Width
KP	Transconductance
RS	Source Ohmic Resistance
RD	Ohmic Drain Resistance
VTO	Zero-bias Threshold Voltage
RDS	Drain-Source Shunt Resistance
TOX	Gate Oxide Thickness
CGSO	Zero-bias Gate-Source Capacitance
CGDO	Zero-bias Gate-Drain Capacitance
CBD	Zero-bias Bulk-Drain Junction Capacitance
MJ	Bulk Junction Grading Coefficient
PB	Bulk Junction Potential
FC	Bulk Junction Forward-bias Capacitance Coefficient
RG	Gate Ohmic Resistance
IS	Bulk Junction Saturation Current
N	Bulk Junction Emission Coefficient
RB	Bulk Series Resistance
PHI	Surface Inversion Potential
GAMMA	Body-effect Parameter
DELTA	Width effect on Threshold Voltage
ETA	Static Feedback on Threshold Voltage
THETA	Modility Modulation
KAPPA	Saturation Field Factor
VMAX	Maximum Drift Velocity of Carriers
XJ	Metallurgical Junction Depth
UO	Surface Mobility

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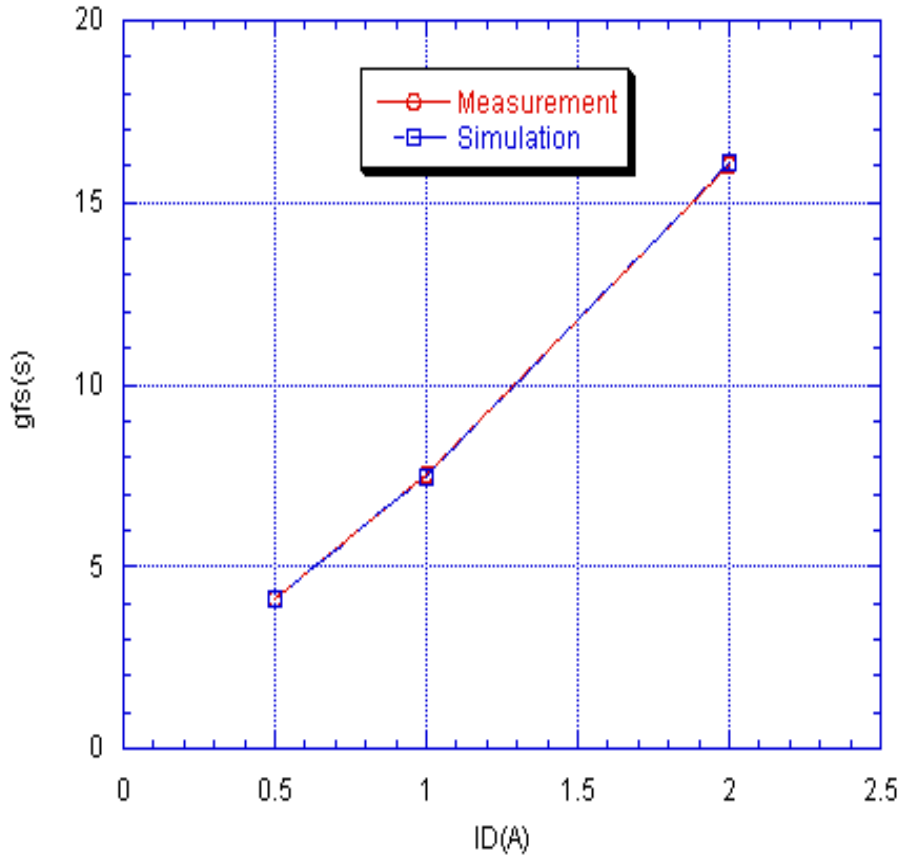
Transconductance Characteristic

Circuit Simulation Result



Comparison table

Id(A)	gfs		Error(%)
	Measurement	Simulation	
0.5	4.1	4.120	0.487
1	7.5	7.48	-0.266
2	16	16.1	0.625

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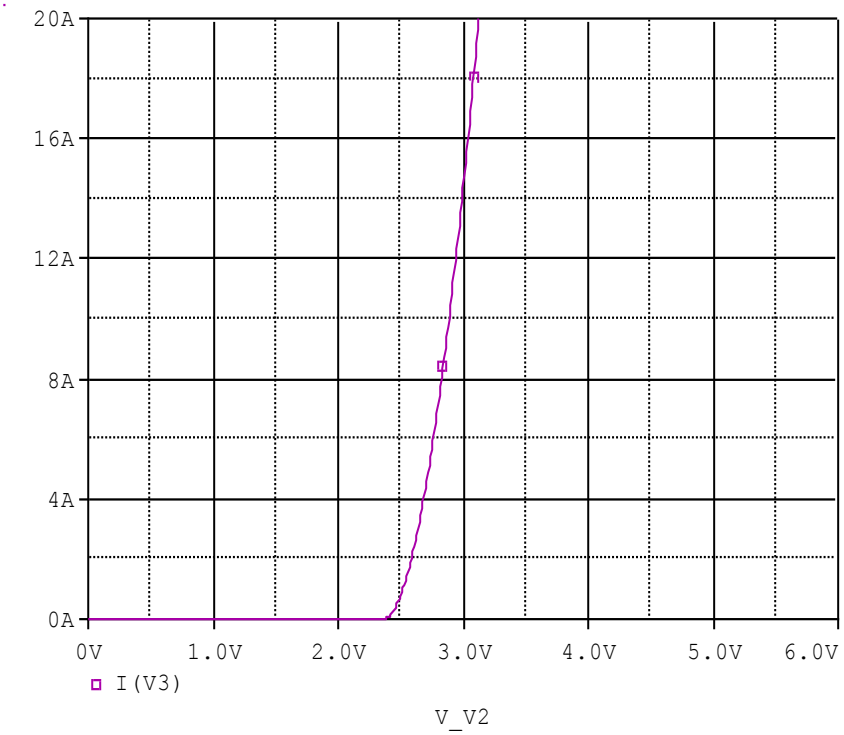
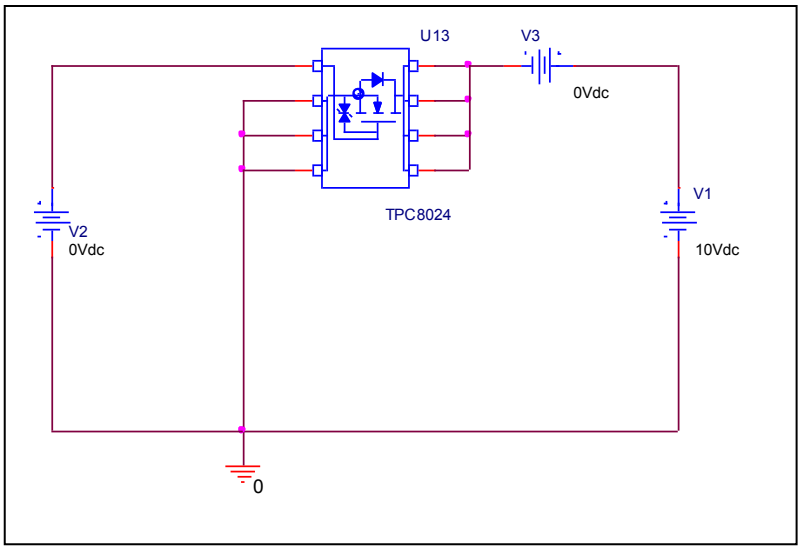
Vgs-Id Characteristic

Circuit Simulation result



Evaluation circuit



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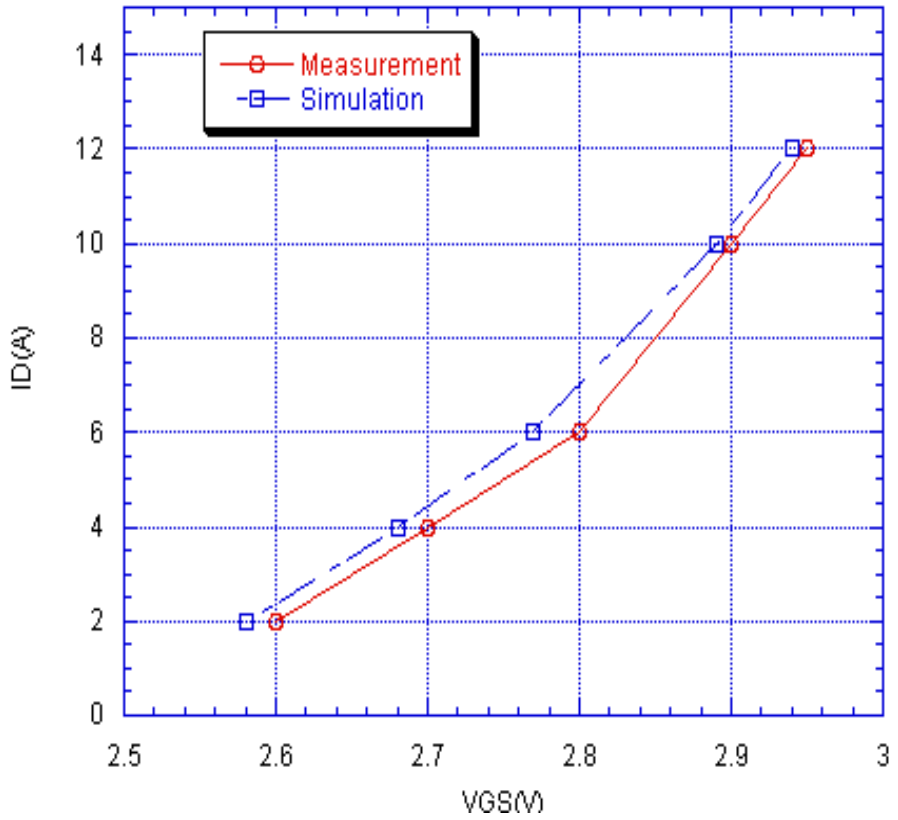
Comparison Graph

Circuit Simulation Result



Simulation Result

I_D(A)	V_{GS}(V)		Error (%)
	Measurement	Simulation	
2	2.6	2.58	-0.769
4	2.7	2.68	-0.741
6	2.8	2.77	-1.071
8	2.85	2.83	-0.701
10	2.9	2.89	-0.344
12	2.95	2.94	-0.338

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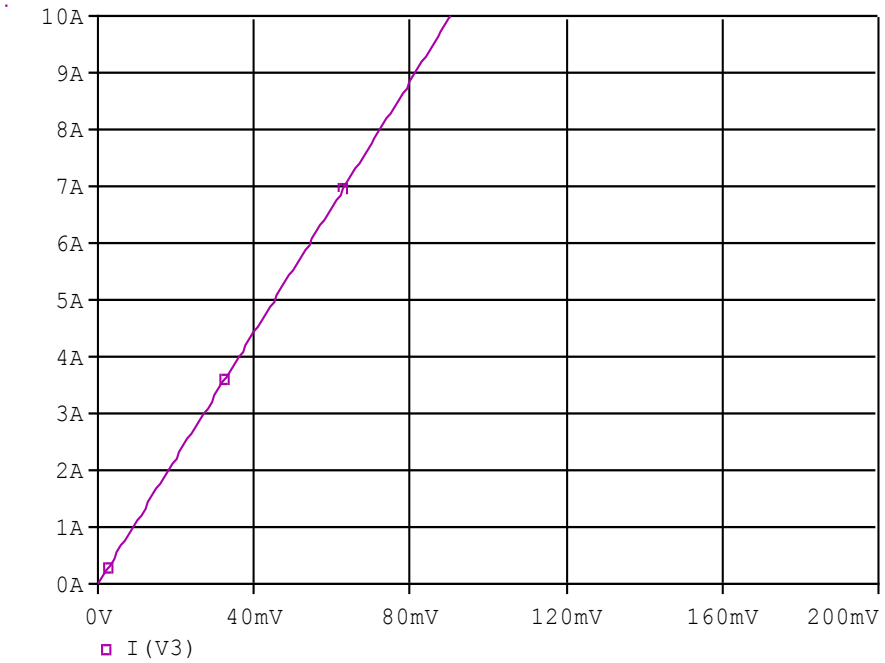
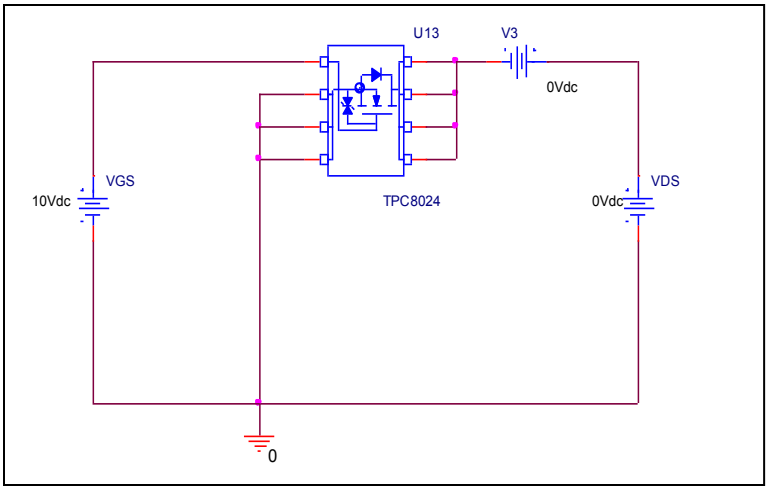
Id-Rds(on) Characteristic

Circuit Simulation result



Evaluation circuit



Simulation Result

I_D=6.5A, V_{GS}=10V	Measurement		Simulation		Error (%)
R_{DS} (on)	9	mΩ	9.03	mΩ	0.333

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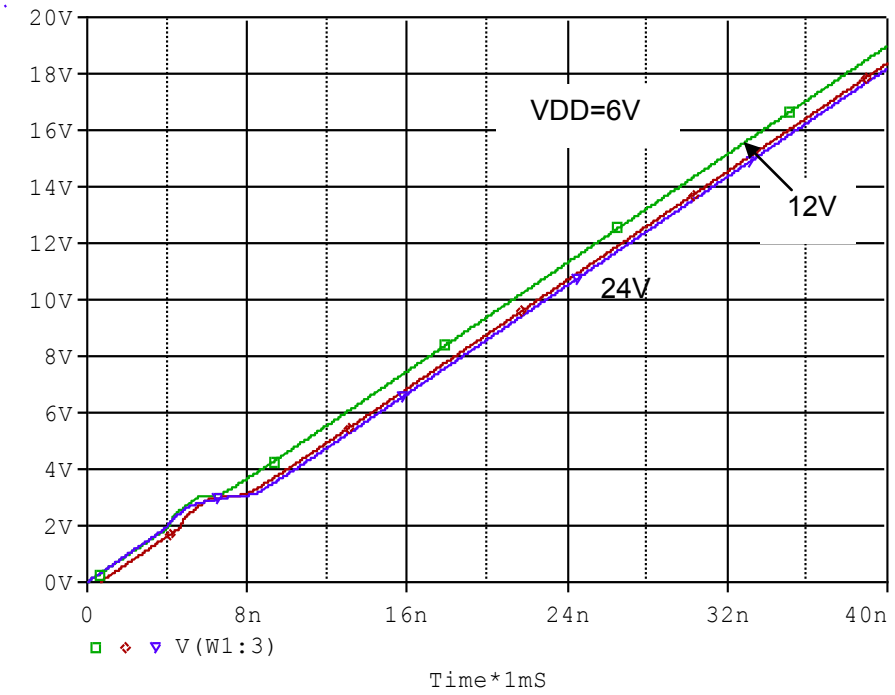
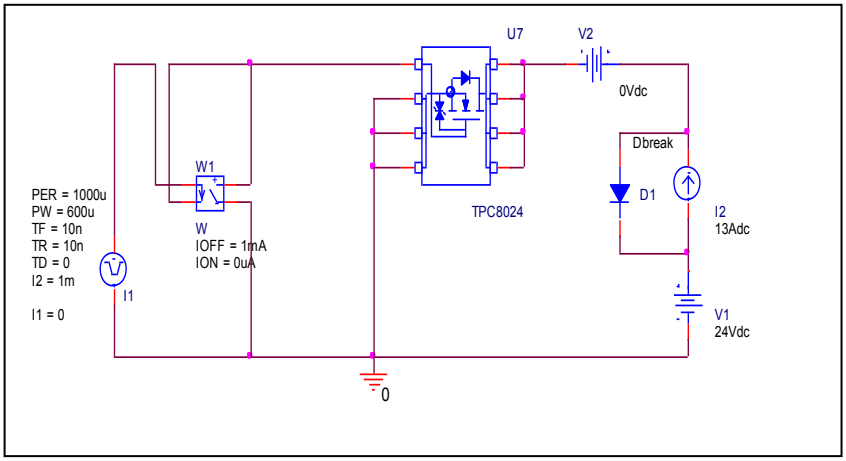
Gate Charge Characteristic

Circuit Simulation result



Evaluation circuit



Simulation Result

V_{DD}=24V,I_D=13A ,V_{GS}=10V	Measurement		Simulation		Error (%)
Qgs	4.5	nC	4.48	nC	-0.444
Qgd	4.9	nC	4.86	nC	-0.816
Qg	23	nC	23.6	nC	2.608

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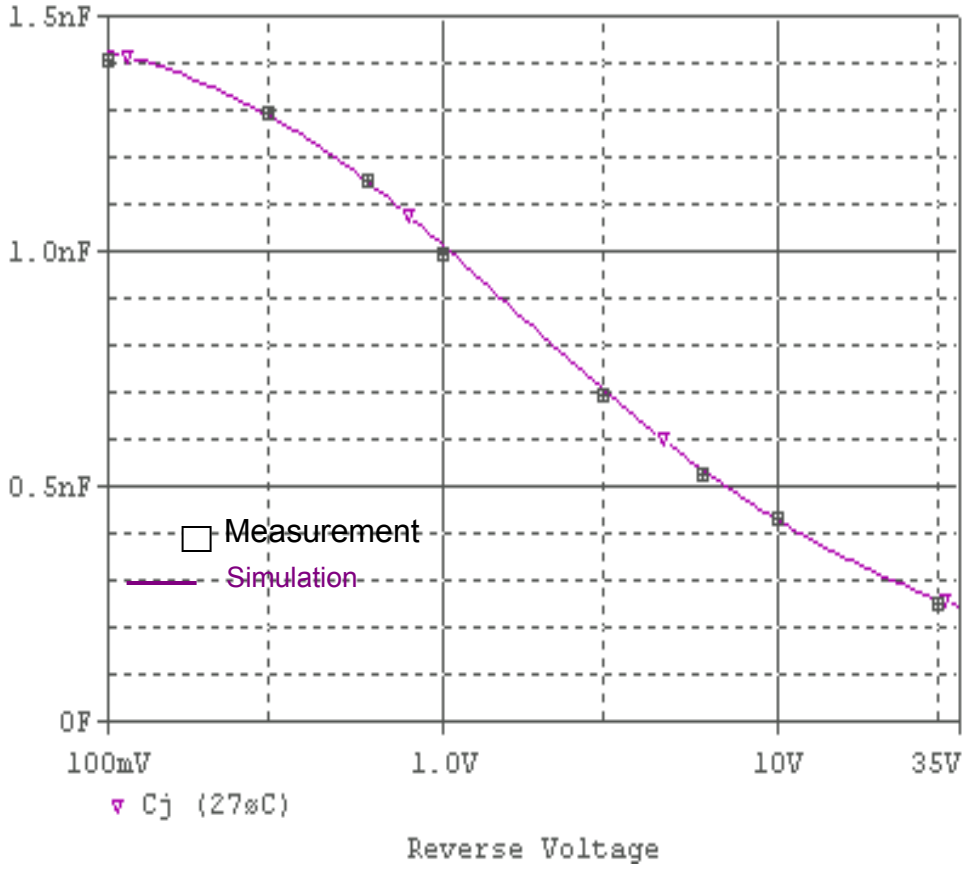
Capacitance Characteristic



Simulation Result

V_{DS}(V)	Cbd(pF)		Error(%)
	Measurement	Simulation	
0.1	1414	1420	0.424
0.3	1300	1290	-0.769
0.6	1157	1155	-0.172
1	1000	1012	1.2
3	698	705	1.002
6	529	531	0.378
10	440	440	0
30	259	264	1.930

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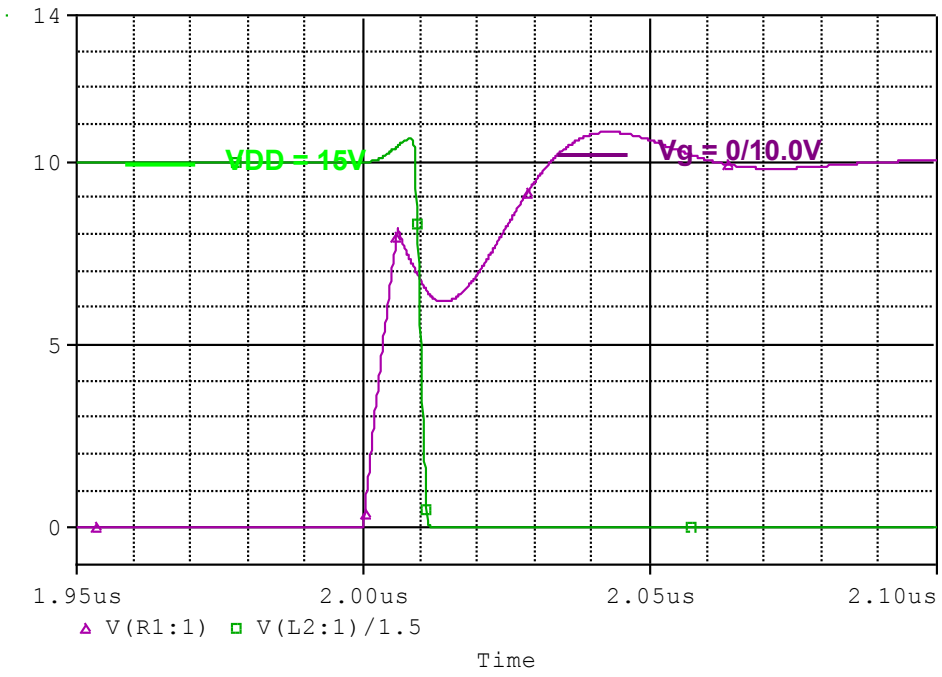
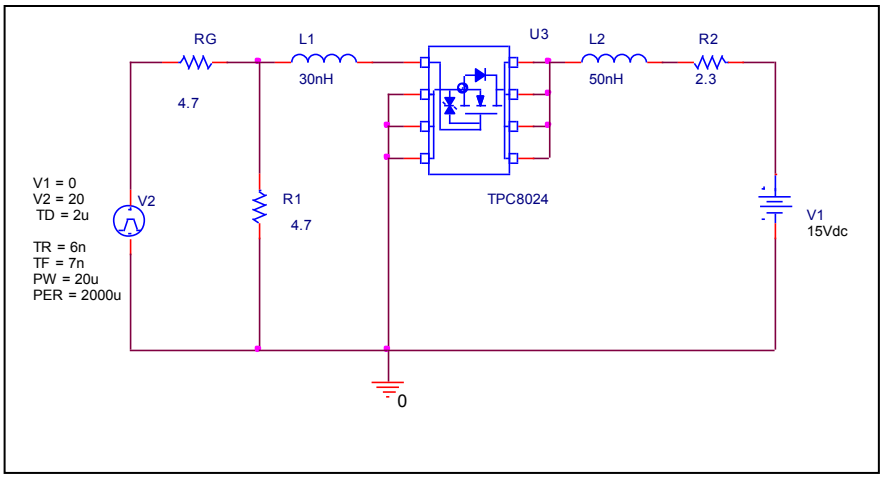
Switching Time Characteristic

Circuit Simulation result



Evaluation circuit



Simulation Result

I_D=6.5 A, V_{DD}=15V V_{GS}=0/10V	Measurement		Simulation		Error(%)
ton	11.00	ns	10.45	ns	- 5

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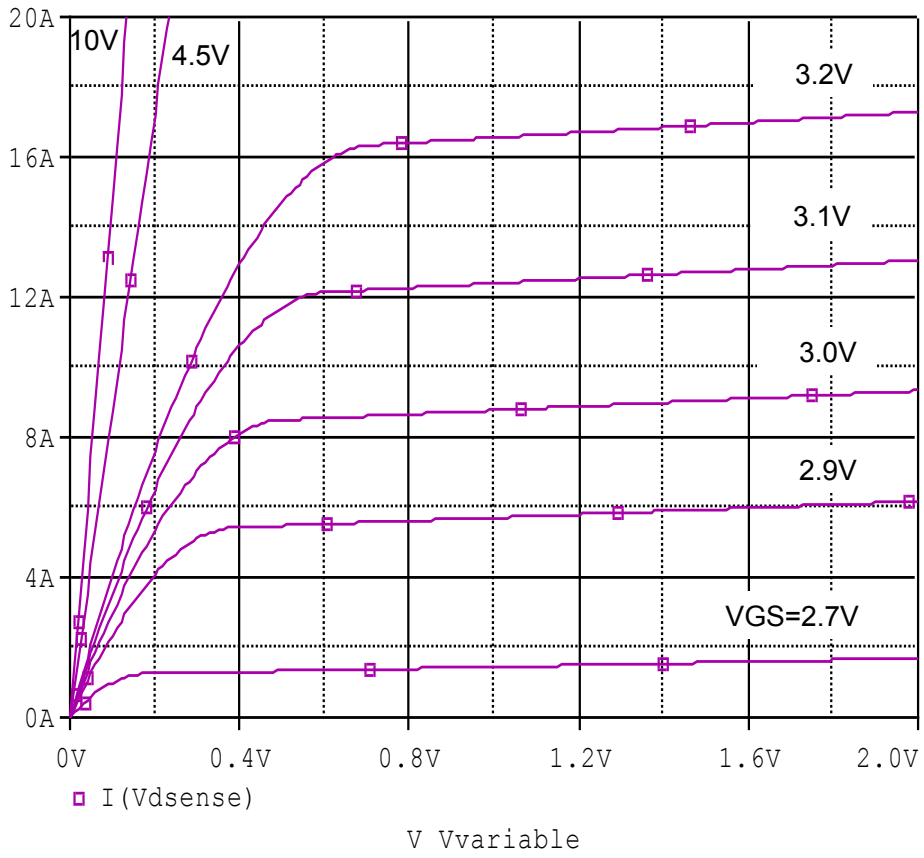
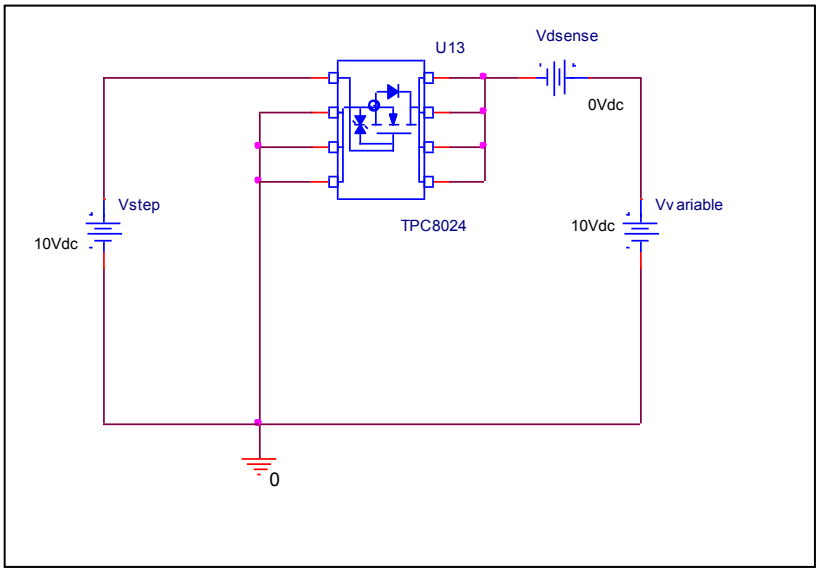
Output Characteristic

Circuit Simulation result



Evaluation circuit



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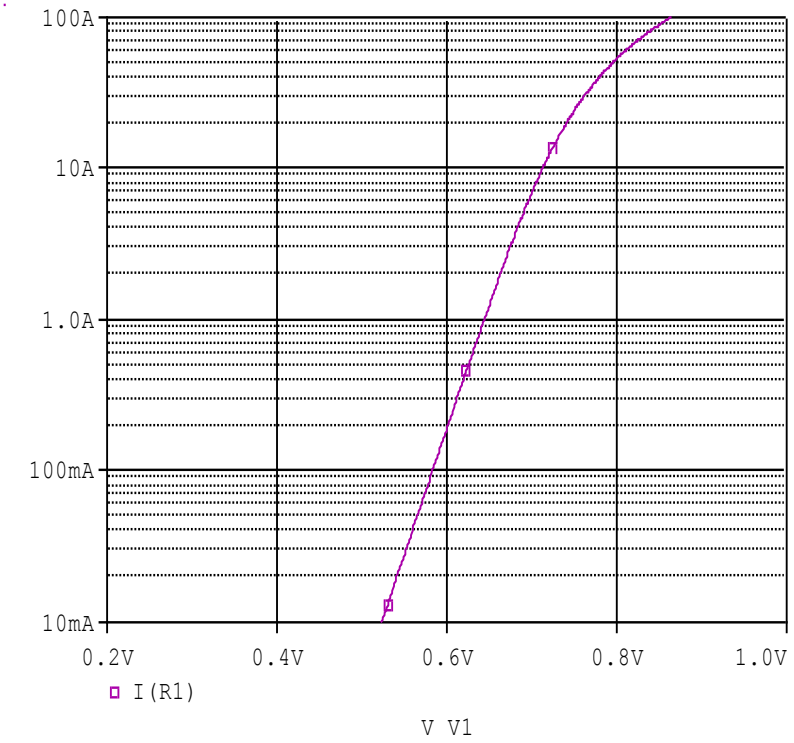
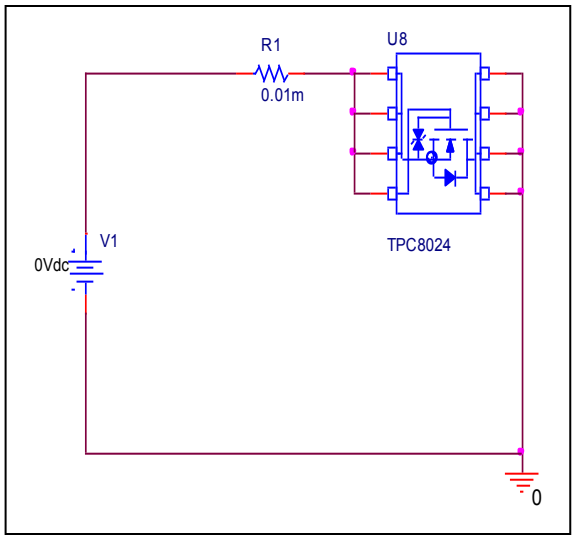
Forward Current Characteristic

Circuit Simulation Result



Evaluation Circuit



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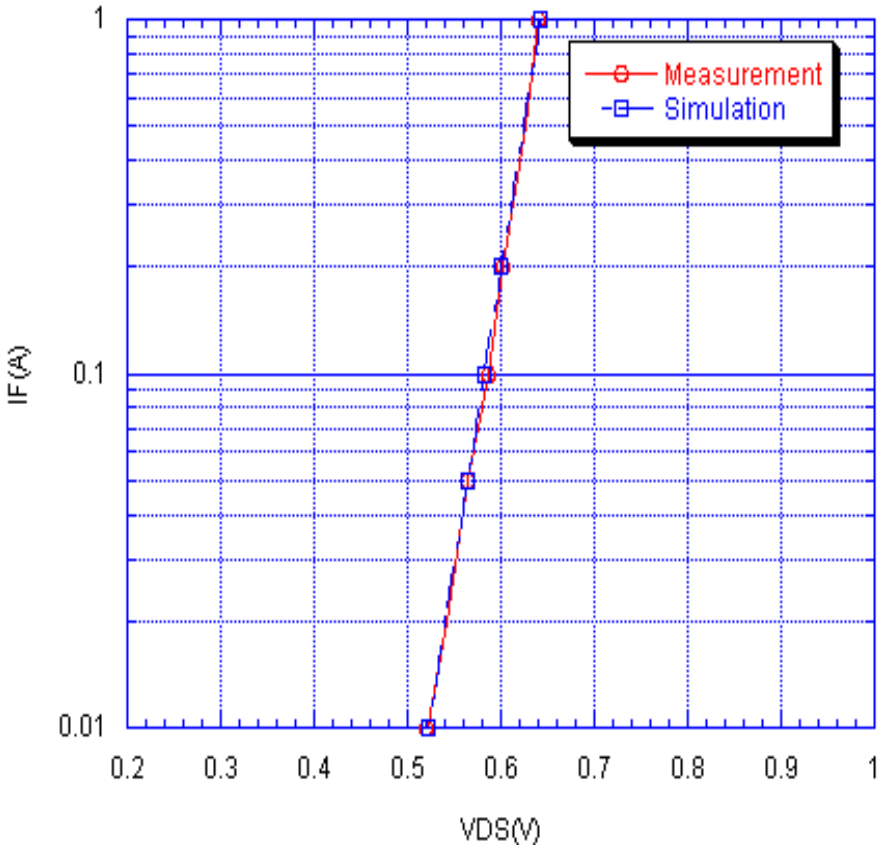
Comparison Graph

Circuit Simulation Result



Simulation Result

Ifwd(A)	Vfwd(V) Measurement	Vfwd(V) Simulation	%Error
0.01	0.52	0.522	0.384
0.02	0.542	0.541	-0.184
0.05	0.565	0.565	0
0.1	0.586	0.583	-0.511
0.2	0.603	0.601	-0.331
0.5	0.626	0.625	-0.159
1	0.64	0.643	0.468

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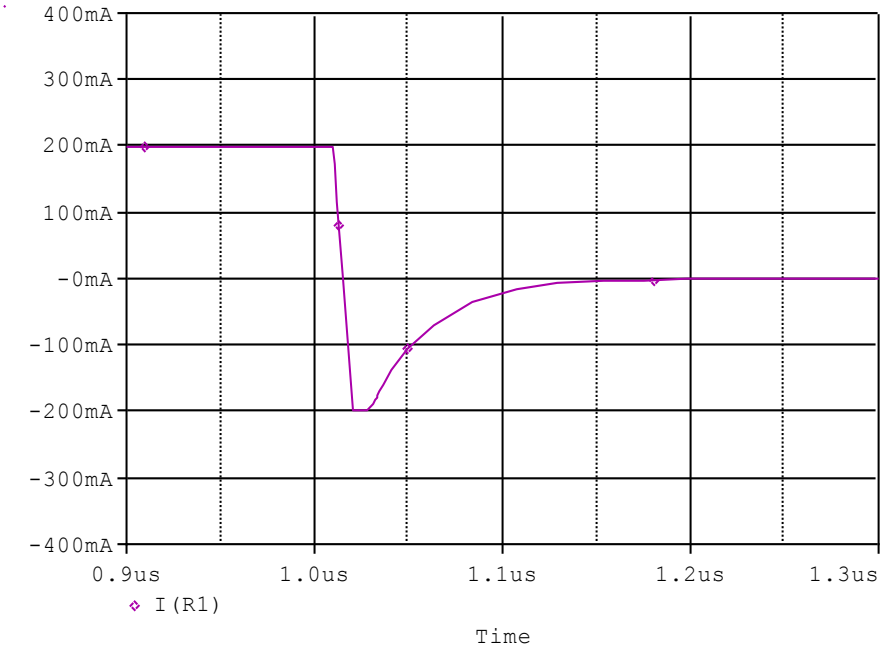
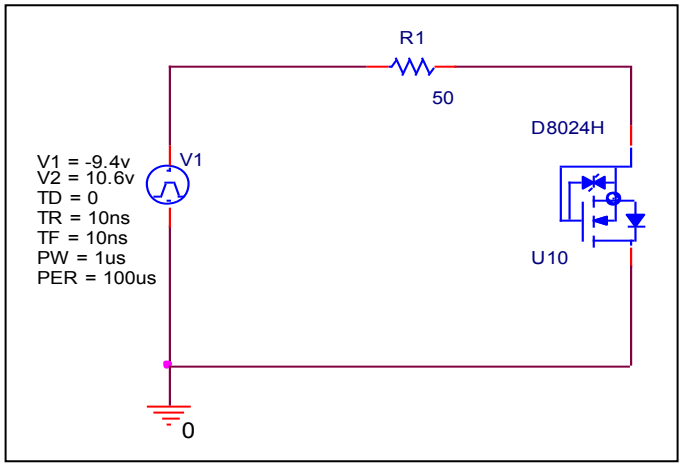
Reverse Recovery Characteristic

Circuit Simulation Result



Evaluation Circuit



Compare Measurement vs. Simulation

	Measurement		Simulation		Error(%)
trj	16.8	ns	16.87	ns	0.416
trb	71.2	ns	71	ns	-0.28

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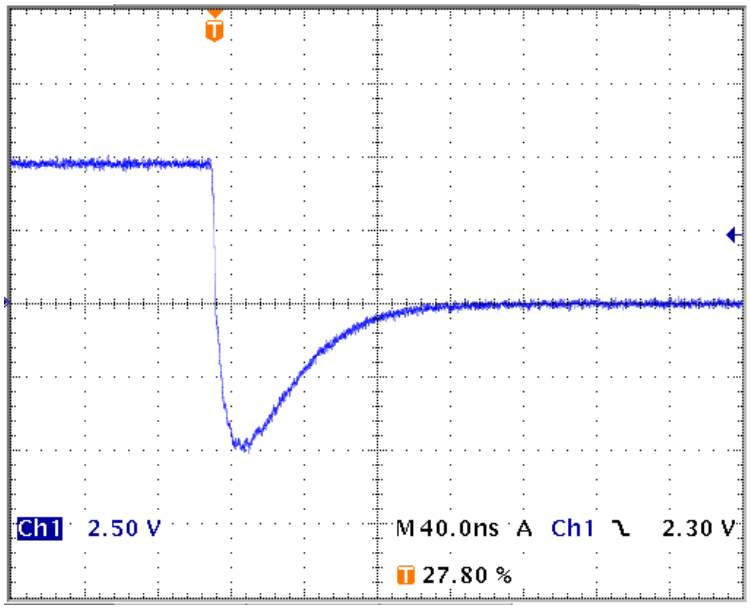
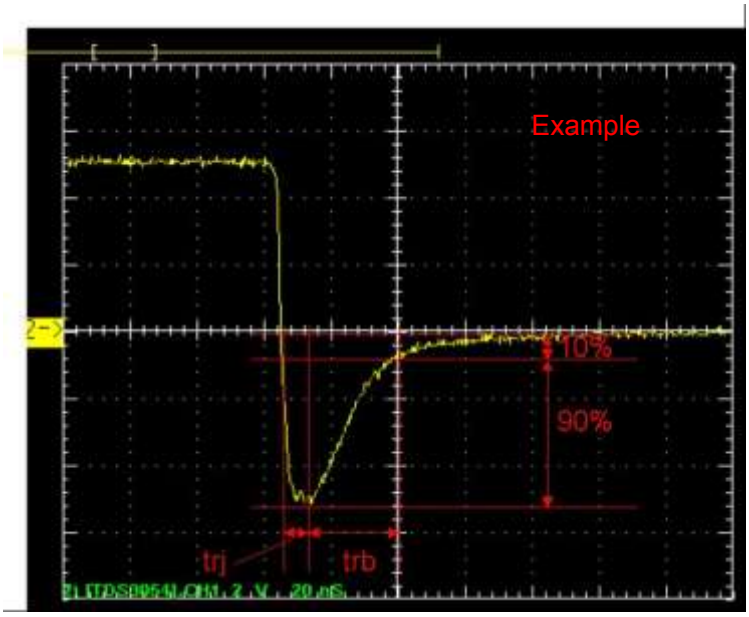
Reverse Recovery Characteristic Reference



Trj=16.8(ns)
Trb=71.2(ns)
Conditions:Ifwd=Irev=0.2(A),Rl=50



Relation between trj and trb

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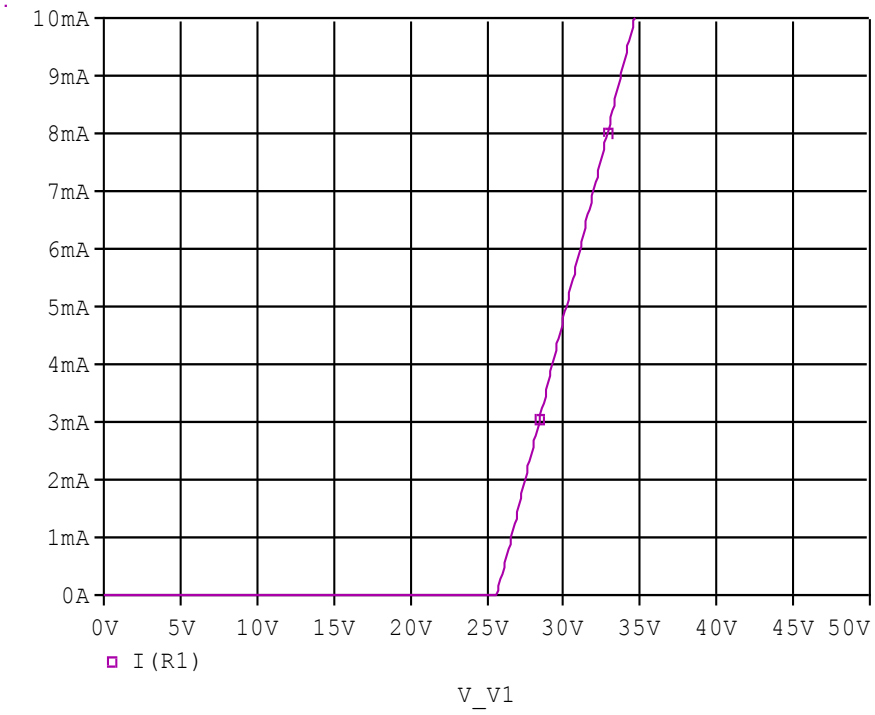
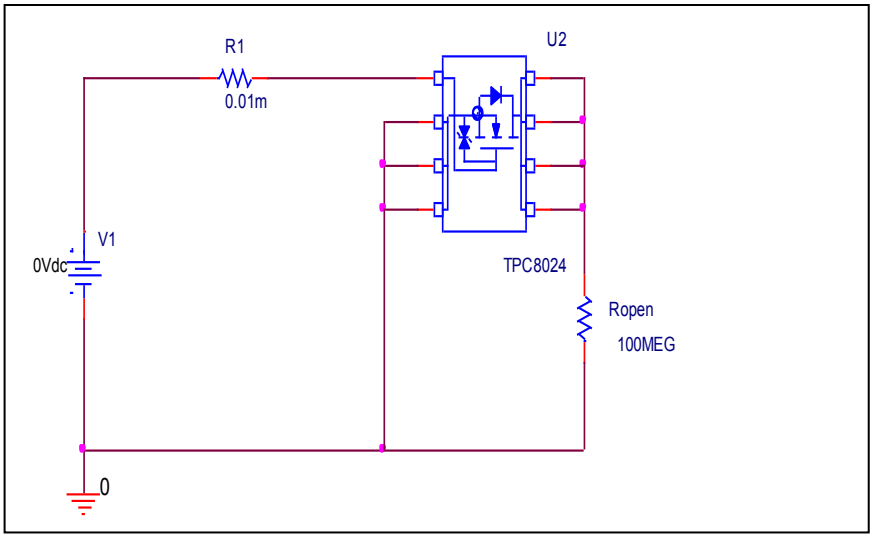
Zener Voltage Characteristic

Circuit Simulation Result



Evaluation Circuit



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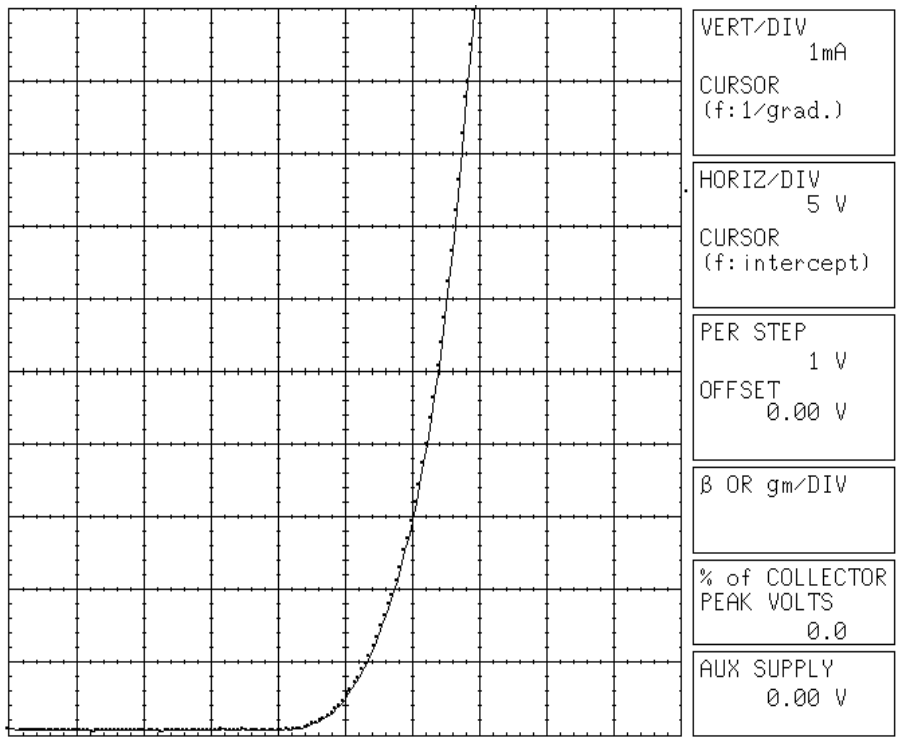
Zener Voltage Characteristic Reference



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