

Device Modeling Report

COMPONENTS: TRANSISTOR
PART NUMBER: QST7TR
MANUFACTURER: ROHM



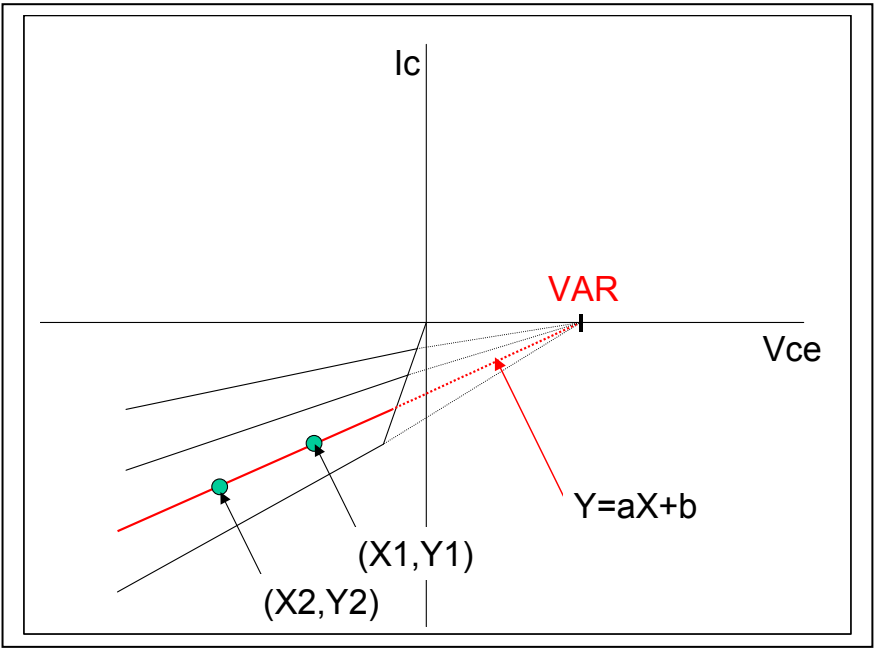
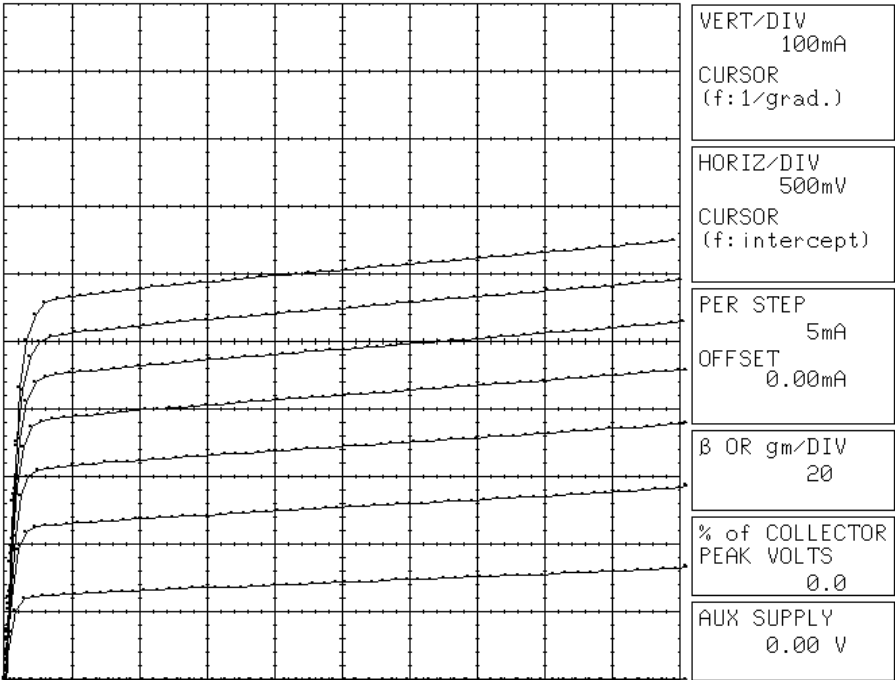
Bee Technologies Inc.

TRANSISTOR MODEL

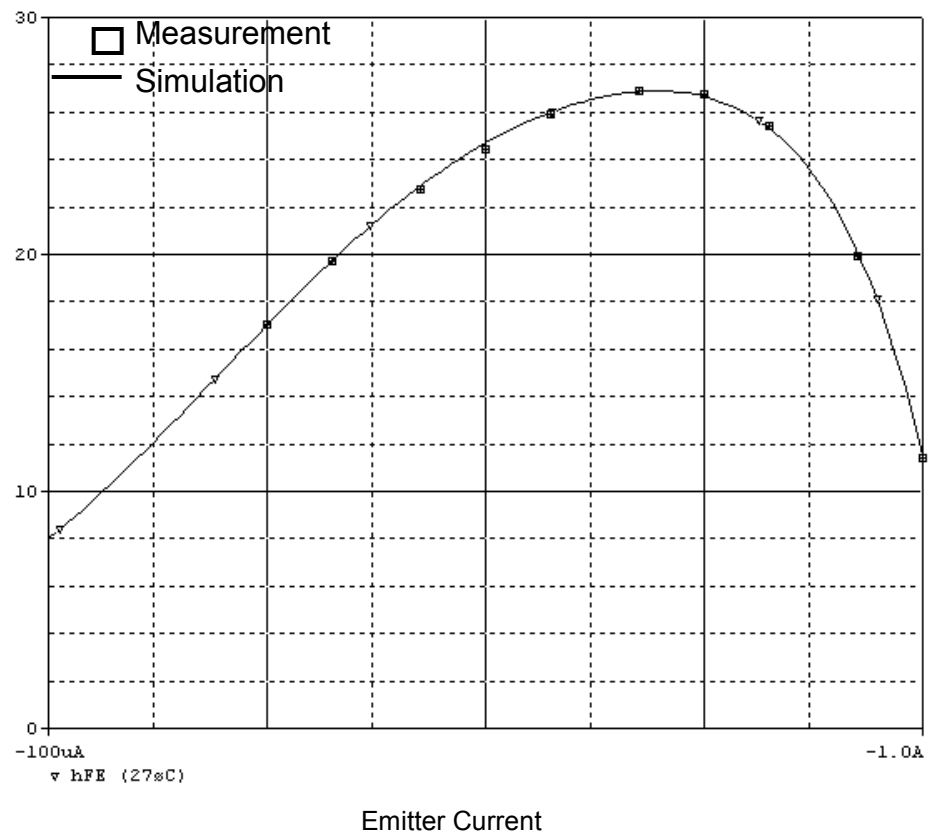
PSpice model parameter	Model description
IS	Saturation Current
BF	Ideal Maximum Forward Beta
NF	Forward Current Emission Coefficient
VAF	Forward Early Voltage
IKF	Forward Beta Roll-off Knee Current
ISE	Non-ideal Base-Emitter Diode Saturation Current
NE	Non-ideal Base-Emitter Diode Emission Coefficient
BR	Ideal Maximum Reverse Beta
NR	Reverse Emission Coefficient
VAR	Reverse Early Voltage
IKR	Reverse Beta Roll-off Knee Current
ISC	Non-ideal Base-Collector Diode Saturation Current
NC	Non-ideal Base-Collector Diode Emission Coefficient
NK	Forward Beta Roll-off Slope Exponent
RE	Emitter Resistance
RB	Base Resistance
RC	Series Collector Resistance
CJE	Zero-bias Emitter-Base Junction Capacitance
VJE	Emitter-Base Junction Potential
MJE	Emitter-Base Junction Grading Coefficient
CJC	Zero-bias Collector-Base Junction Capacitance
VJC	Collector-base Junction Potential
MJC	Collector-base Junction Grading Coefficient
FC	Coefficient for Onset of Forward-bias Depletion Capacitance
TF	Forward Transit Time
XTF	Coefficient for TF Dependency on Vce
VTF	Voltage for TF Dependency on Vce
ITF	Current for TF Dependency on Ic
PTF	Excess Phase at $f=1/2\pi*TF$
TR	Reverse Transit Time
EG	Activation Energy
XTB	Forward Beta Temperature Coefficient
XTI	Temperature Coefficient for IS

Reverse

Reverse Early Voltage Characteristic

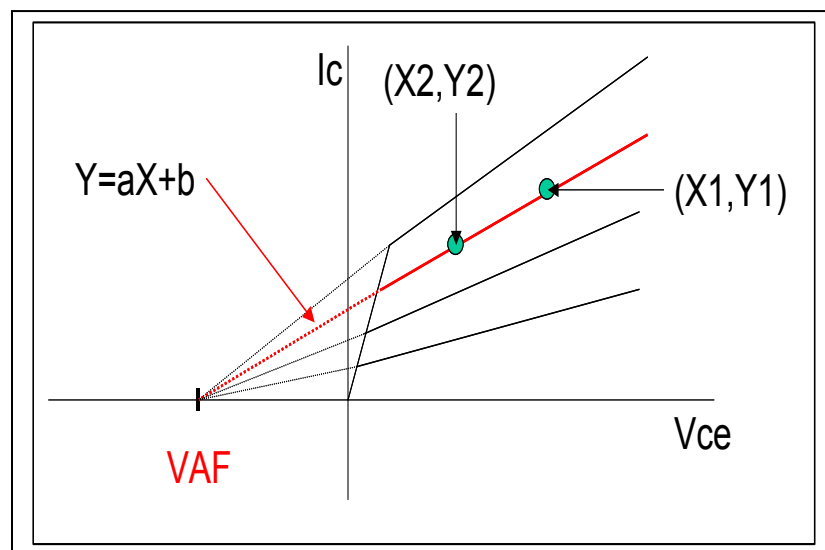
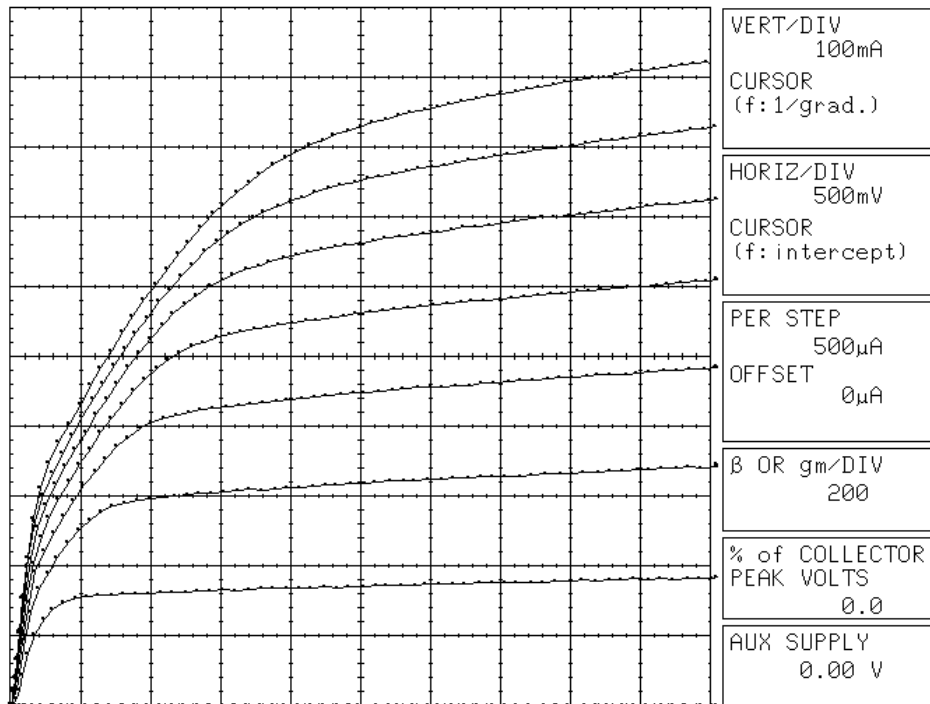


Reverse DC Beta Characteristic (I_e vs. h_{FE})

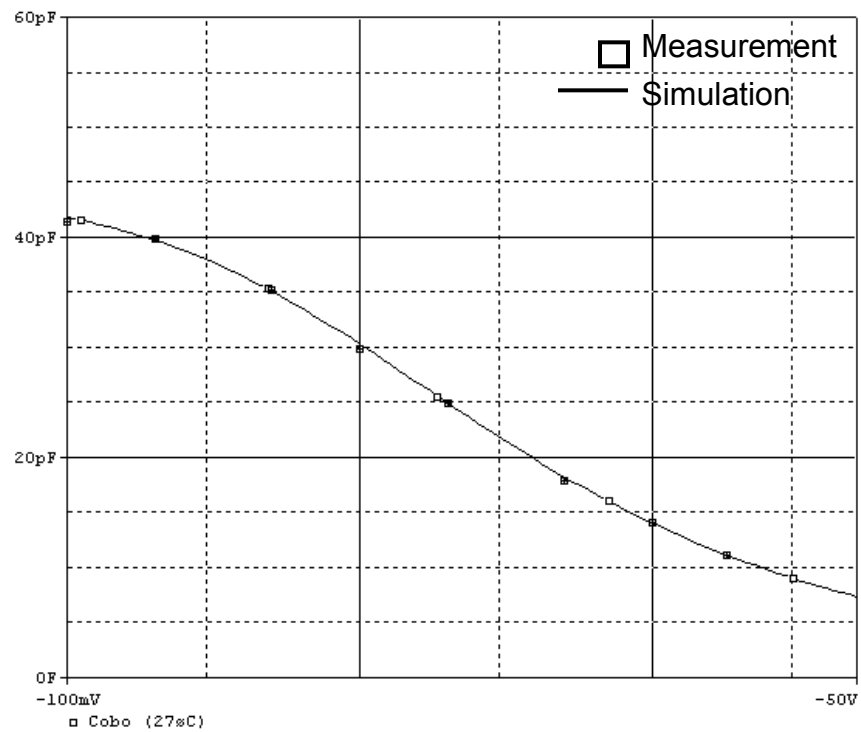


Forward

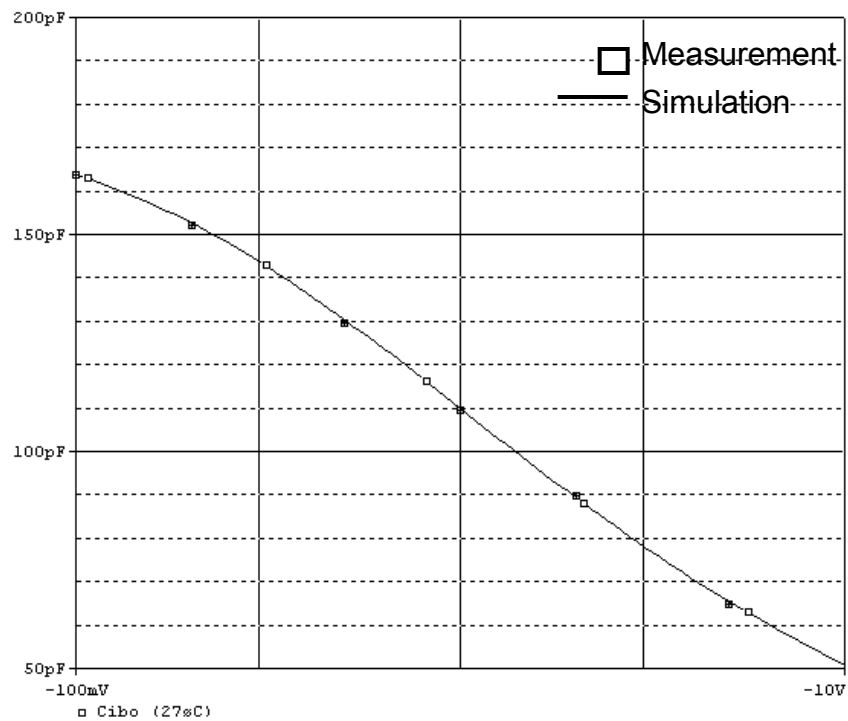
Forward Early Voltage Characteristic



C-B Capacitance Characteristics

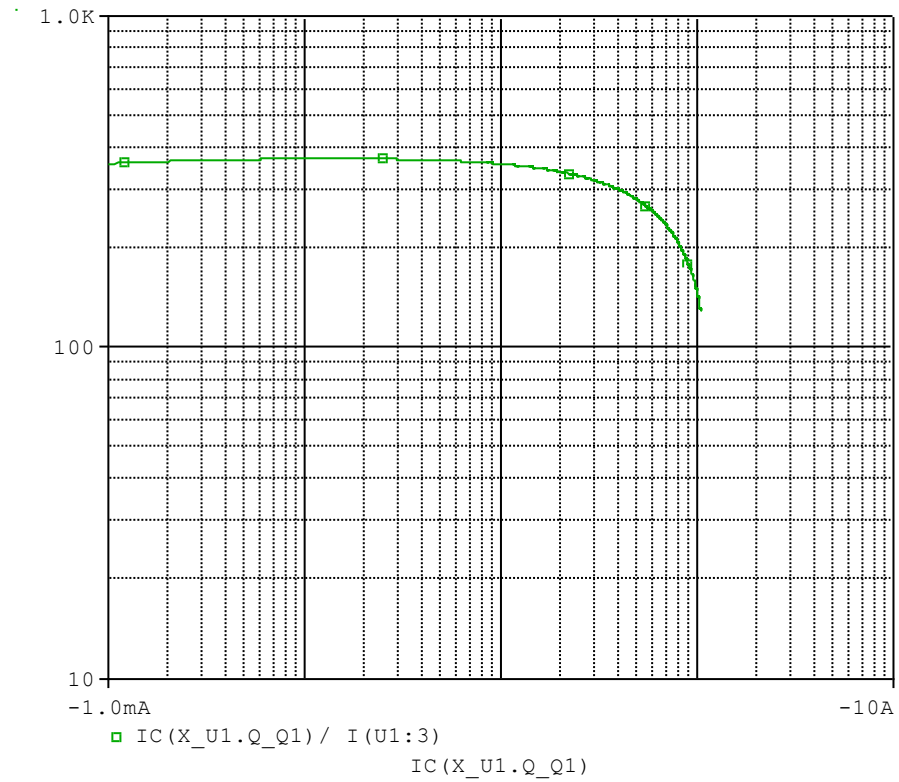


E-B Capacitance Characteristics

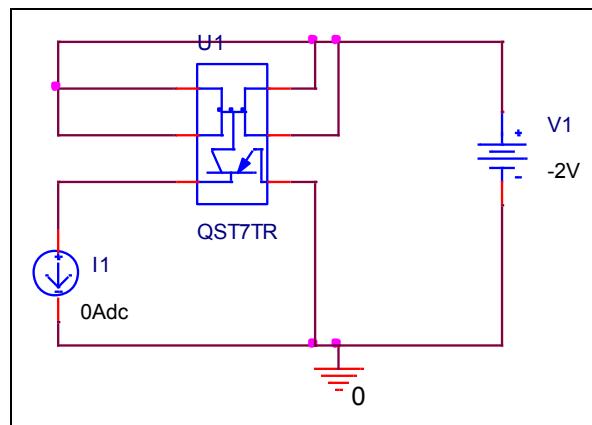


Transistor h_{FE} - I_C Characteristics

Circuit Simulation Result

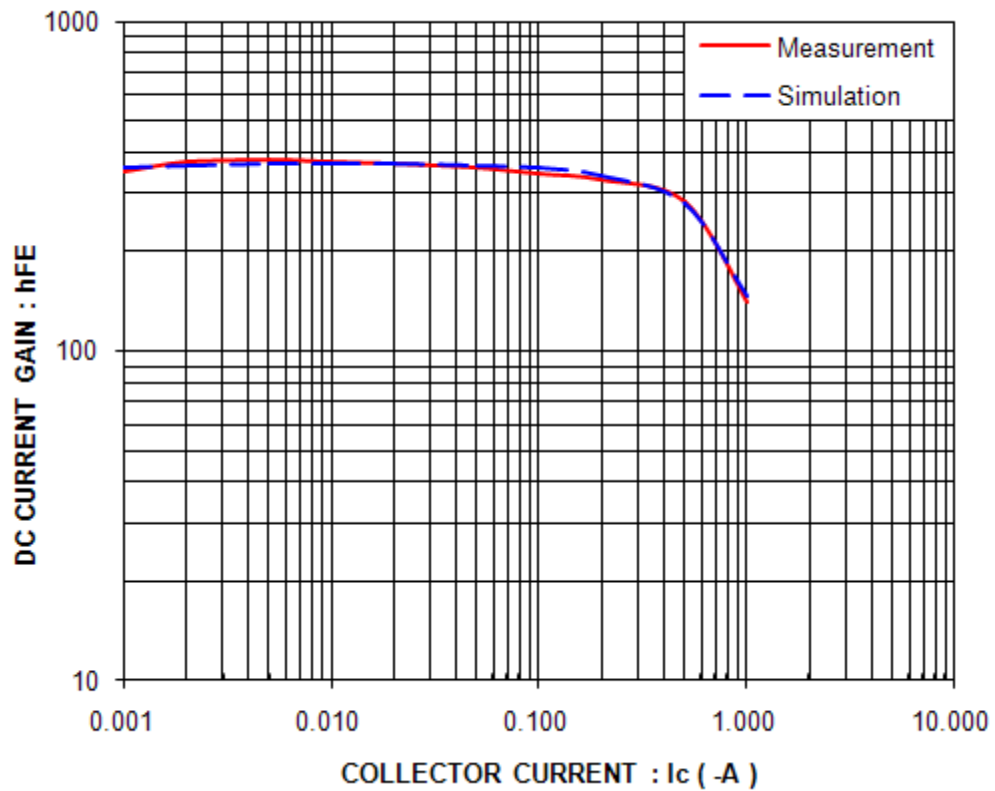


Evaluation Circuit



Comparison Graph

Circuit Simulation Result

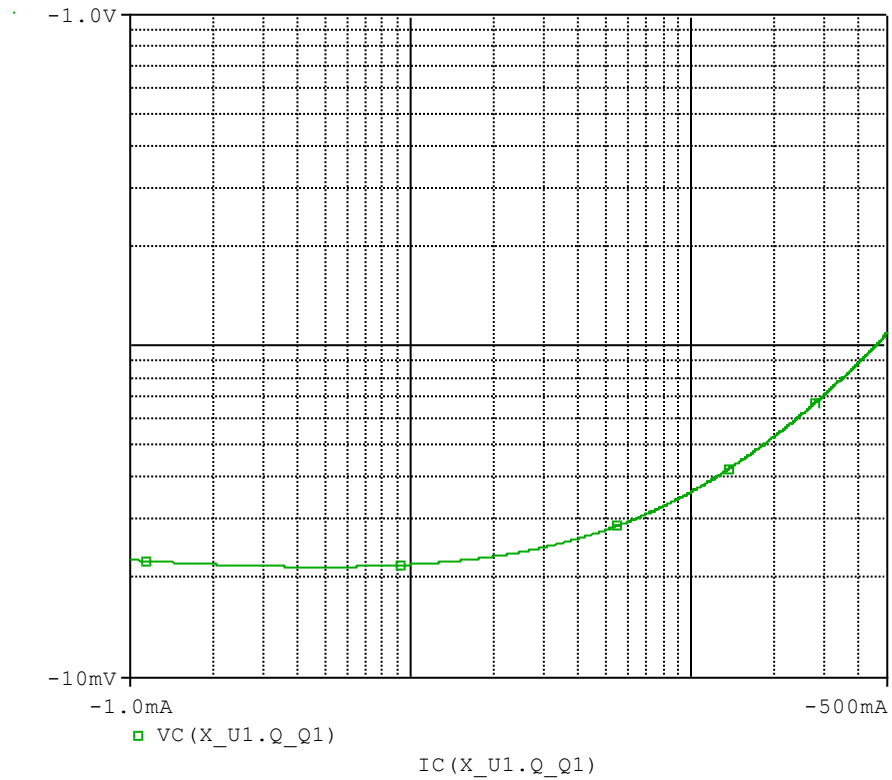


Simulation Result

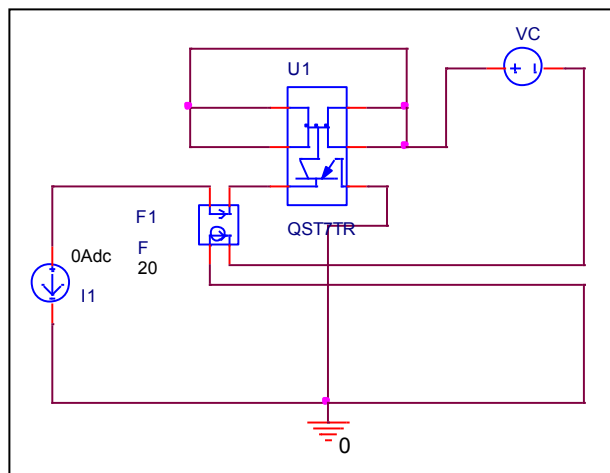
Ic(A)	hFE		Error(%)
	Measurement	Simulation	
-0.001	350.000	358.782	2.509
-0.002	373.832	364.465	-2.506
-0.005	382.263	369.210	-3.415
-0.010	375.000	370.923	-1.087
-0.020	370.000	370.863	0.233
-0.050	360.000	366.745	1.874
-0.100	345.423	358.011	3.644
-0.200	332.226	339.279	2.123
-0.500	284.091	278.243	-2.058
-1.000	140.000	146.134	4.381

$V_{CE(Sat)}$ - I_C Characteristics

Circuit Simulation Result

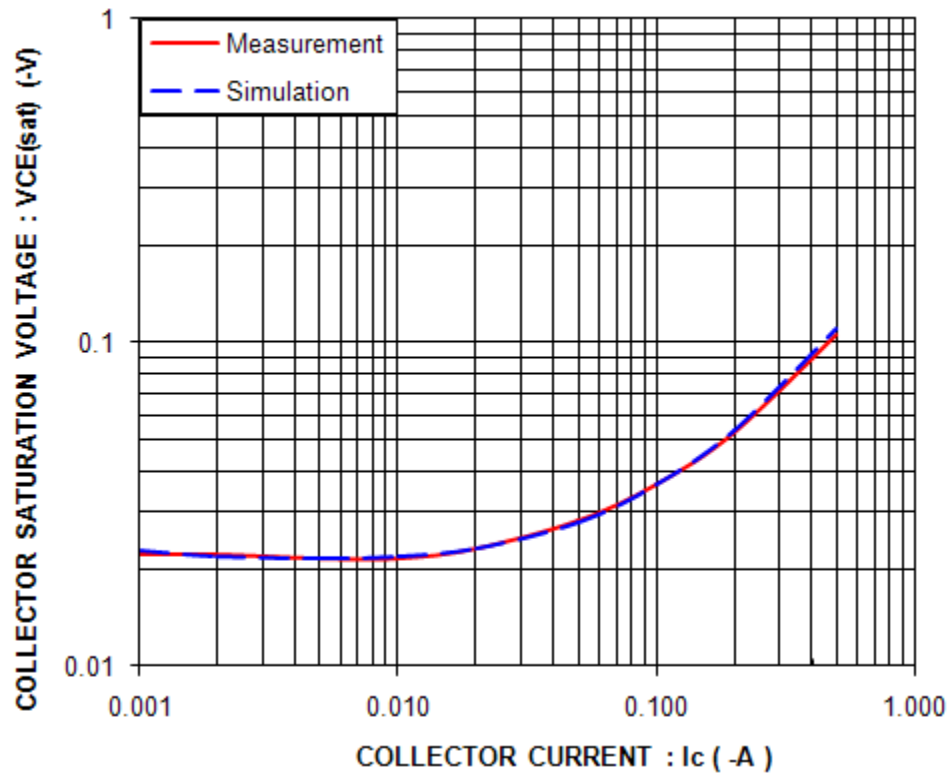


Evaluation Circuit



Comparison Graph

Circuit Simulation Result

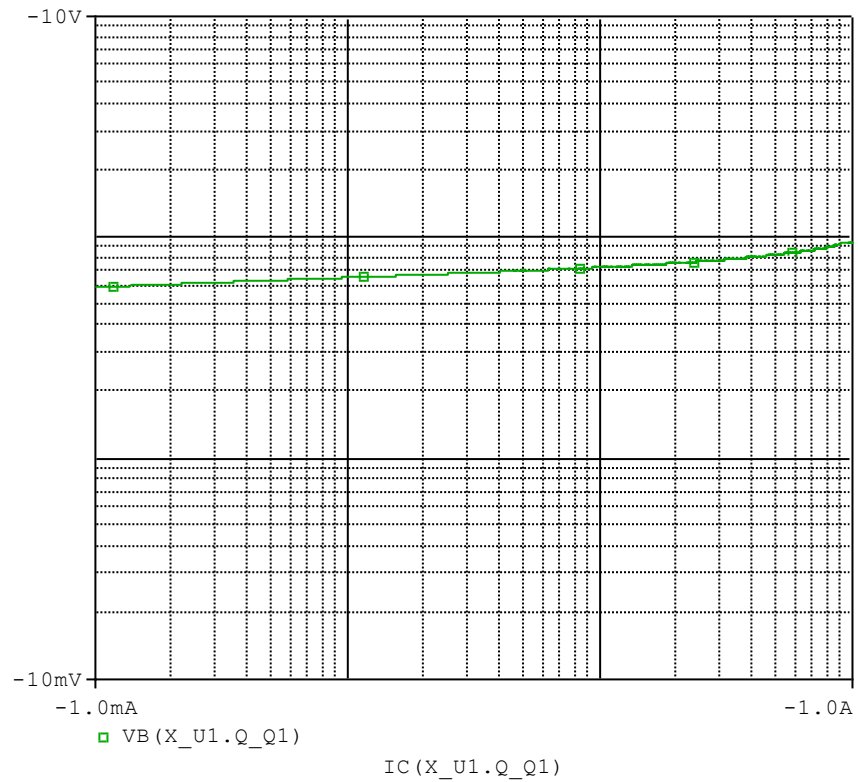


Simulation Result

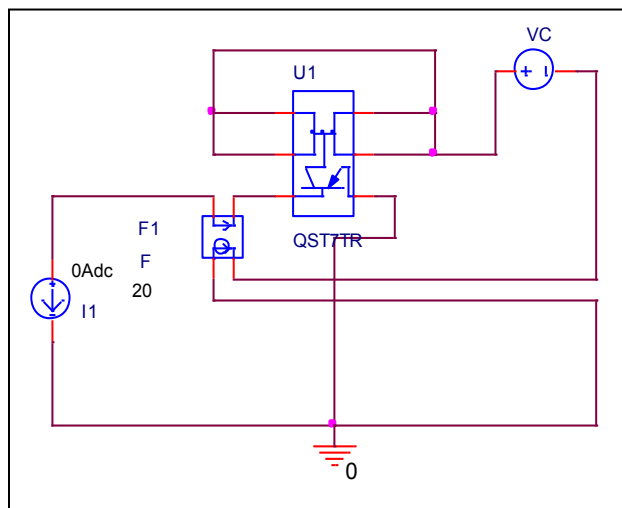
IC(A)	VCE(sat)(V)		Error(%)
	Measurement	Simulation	
-0.001	-0.0220	-0.0226	2.8045
-0.002	-0.0220	-0.0220	-0.2000
-0.005	-0.0215	-0.0216	0.4558
-0.010	-0.0215	-0.0219	2.0744
-0.020	-0.0230	-0.0233	1.0957
-0.050	-0.0280	-0.0280	-0.0821
-0.100	-0.0365	-0.0364	-0.3205
-0.200	-0.0530	-0.0538	1.4245
-0.500	-0.1060	-0.1096	3.3538

$V_{BE(Sat)}-I_C$ Characteristics

Circuit Simulation Result

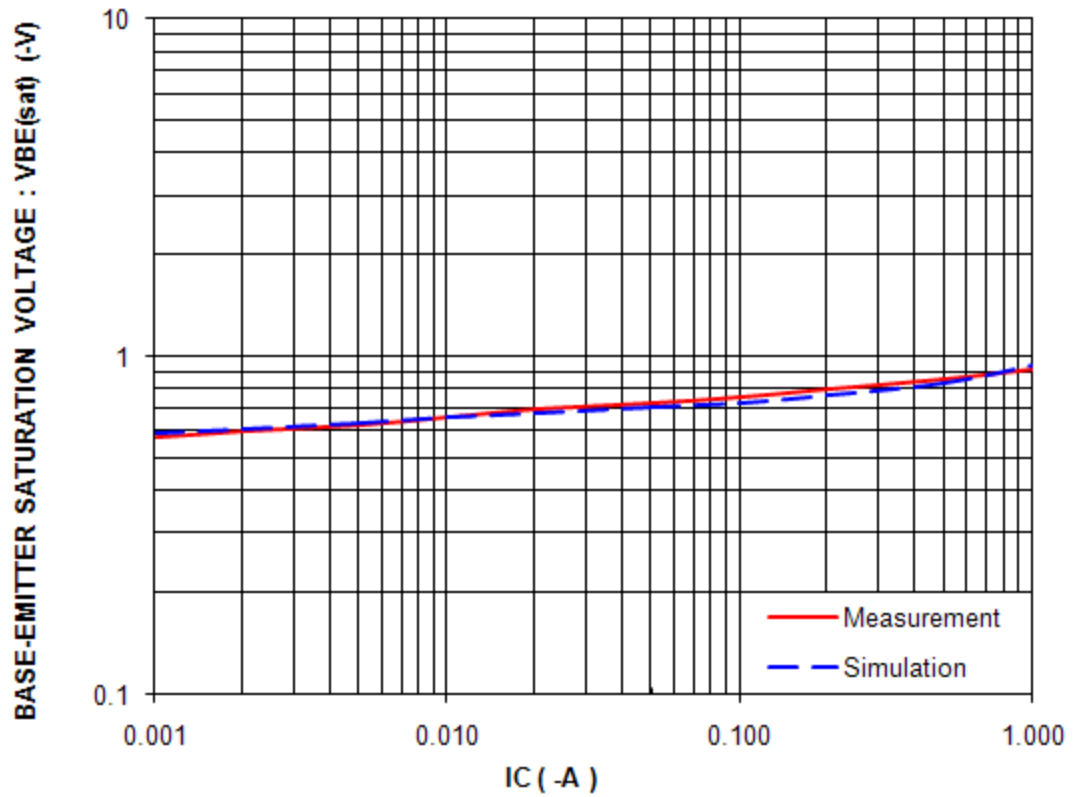


Evaluation Circuit



Comparison Graph

Circuit Simulation Result

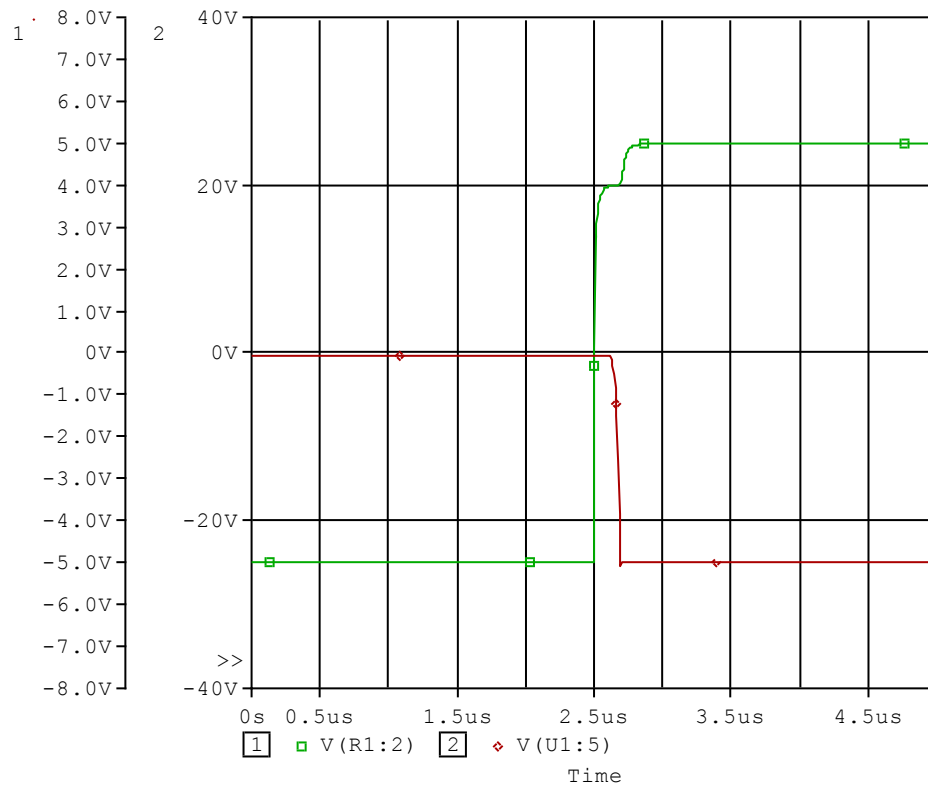


Simulation Result

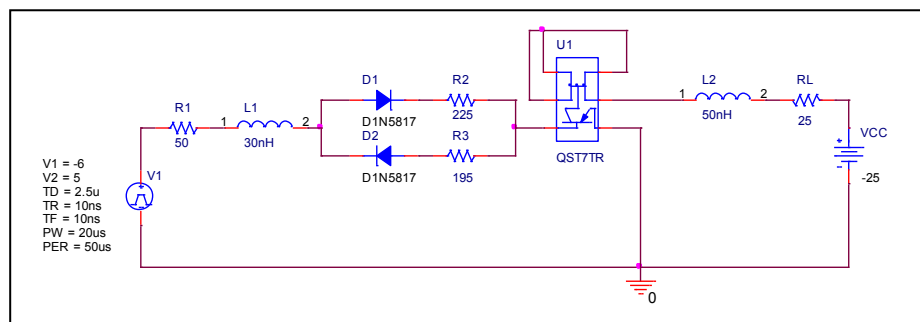
IC(A)	VBE(sat)(V)		Error(%)
	Measurement	Simulation	
-0.001	-0.580	-0.596	2.736
-0.002	-0.600	-0.614	2.348
-0.005	-0.630	-0.639	1.423
-0.010	-0.660	-0.658	-0.292
-0.020	-0.695	-0.678	-2.472
-0.050	-0.730	-0.706	-3.257
-0.100	-0.760	-0.732	-3.725
-0.200	-0.800	-0.765	-4.430
-0.500	-0.860	-0.835	-2.916
-1.000	-0.920	-0.950	3.252

Switching Characteristics

Circuit simulation result

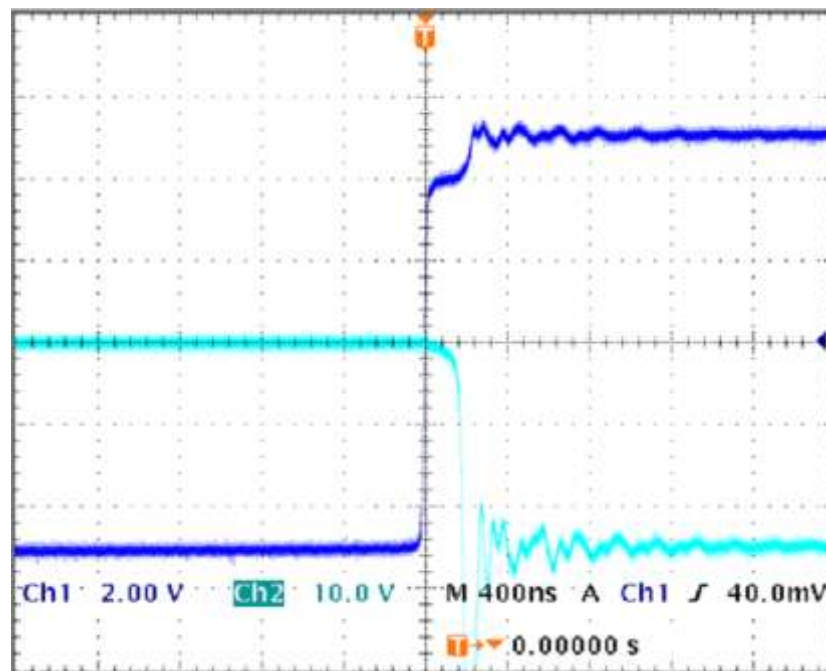


Evaluation circuit



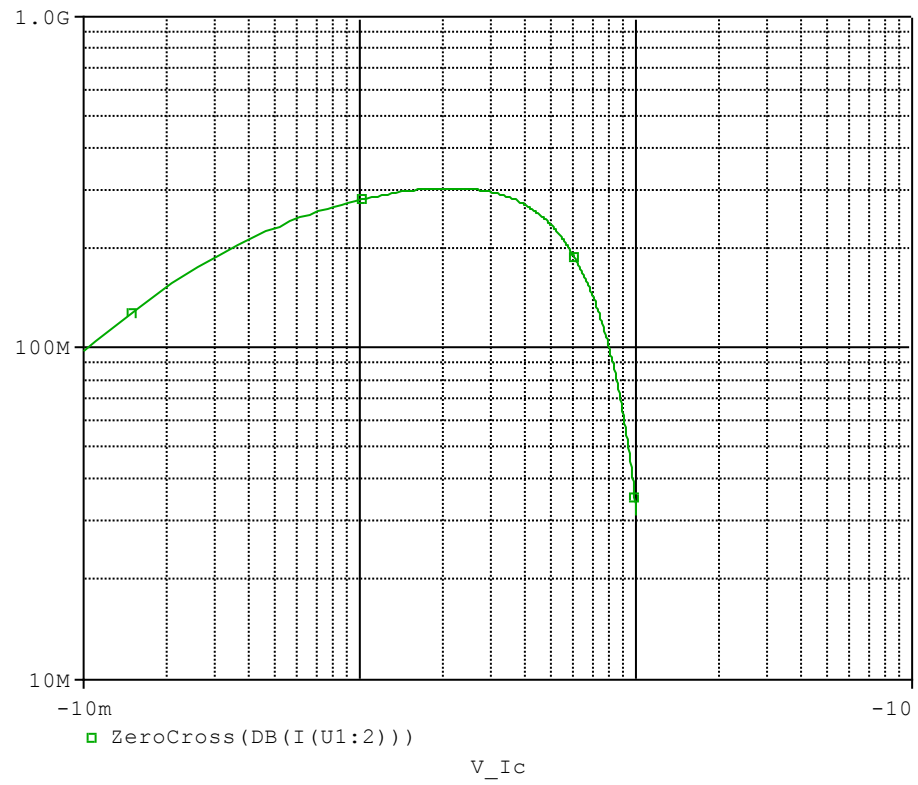
Simulation result

	Measurement	Simulation	%Error
t_f (ns)	45.000	45.014	0.031
t_{stg} (ns)	144.000	144.495	0.344

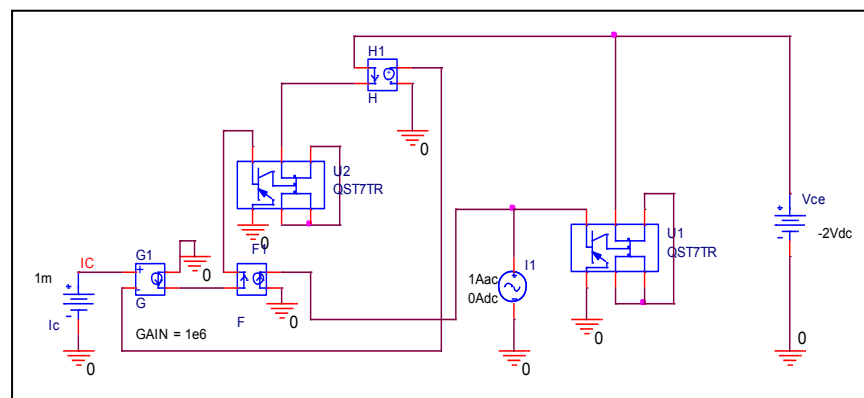


F_t - I_C Characteristics

Circuit Simulation Result

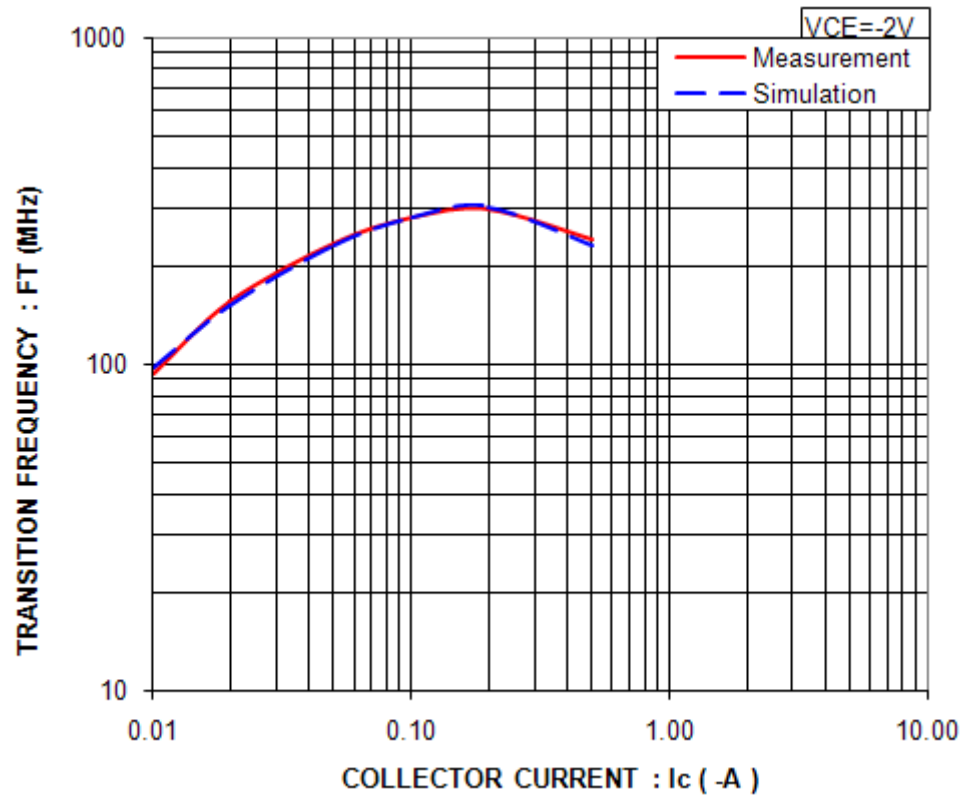


Evaluation Circuit



Comparison Graph

Circuit Simulation Result

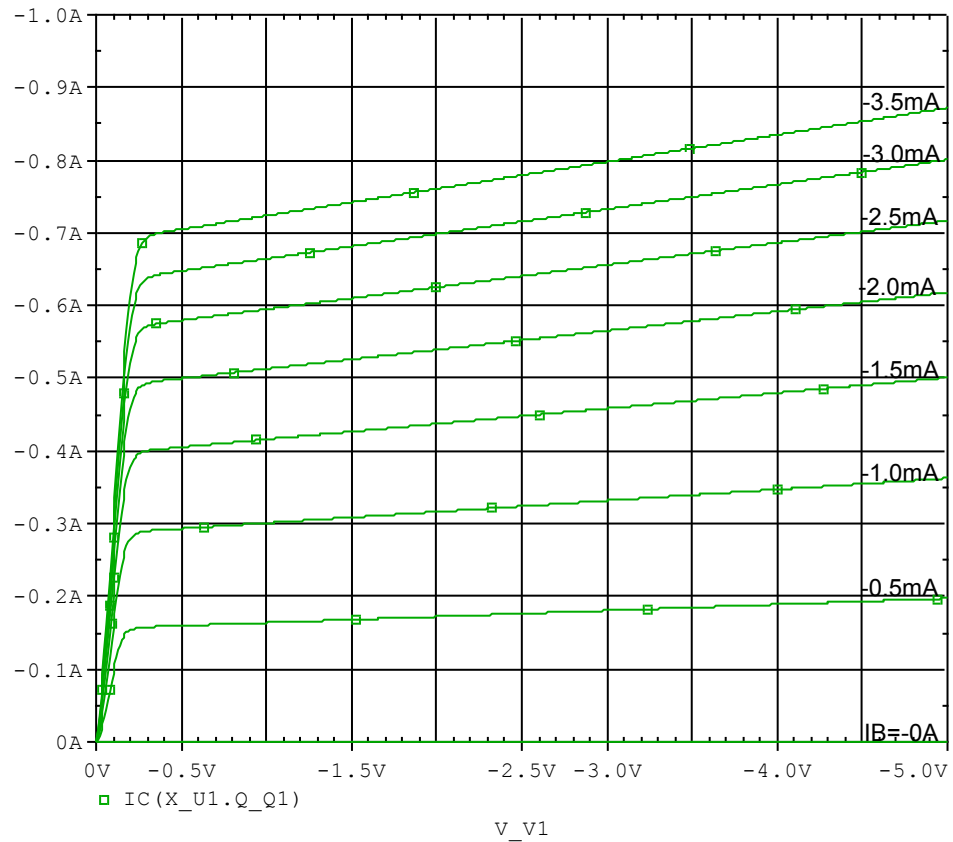


Simulation Result

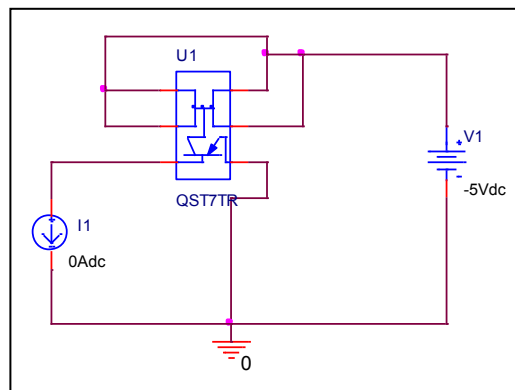
-Ic(A)	FT(MHz)		Error(%)
	Measurement	Simulation	
0.010	94.000	96.960	3.149
0.020	156.000	152.299	-2.372
0.050	235.000	231.575	-1.457
0.100	280.000	280.147	0.052
0.200	300.000	303.851	1.284
0.500	240.000	232.172	-3.262

Output Characteristics

Circuit Simulation Result



Evaluation Circuit



Output Characteristics

Reference

