

# **Device Modeling Report**

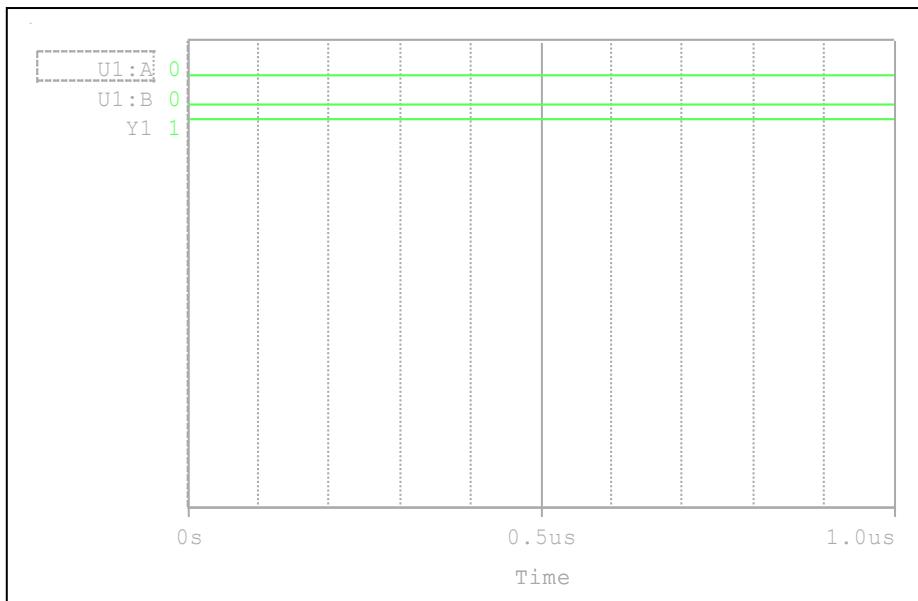
COMPONENTS : CMOS DIGITAL INTEGRATED CIRCUIT  
PART NUMBER : TC7S00FU  
MANUFACTURER : TOSHIBA



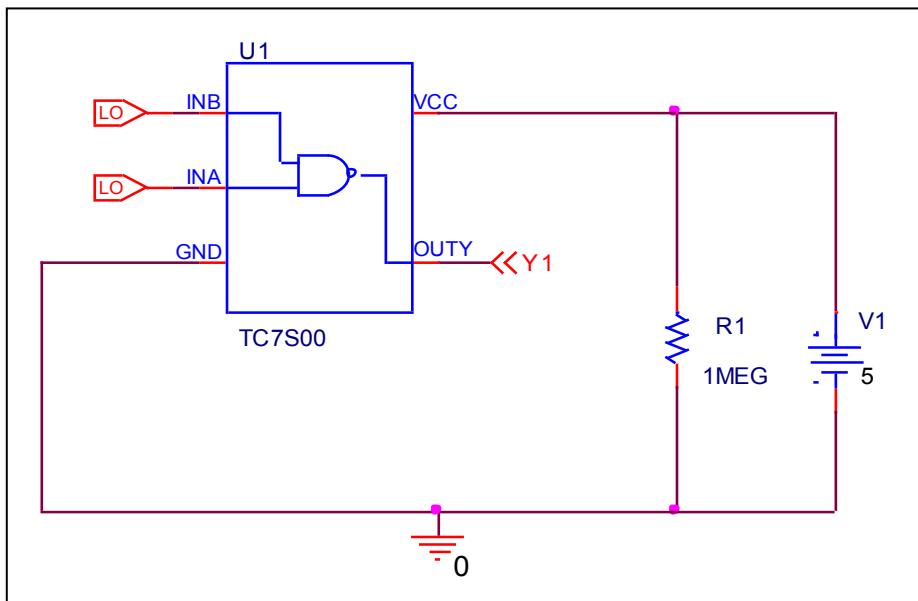
**Bee Technologies Inc.**

## Truth Table

Circuit simulation result



Evaluation circuit

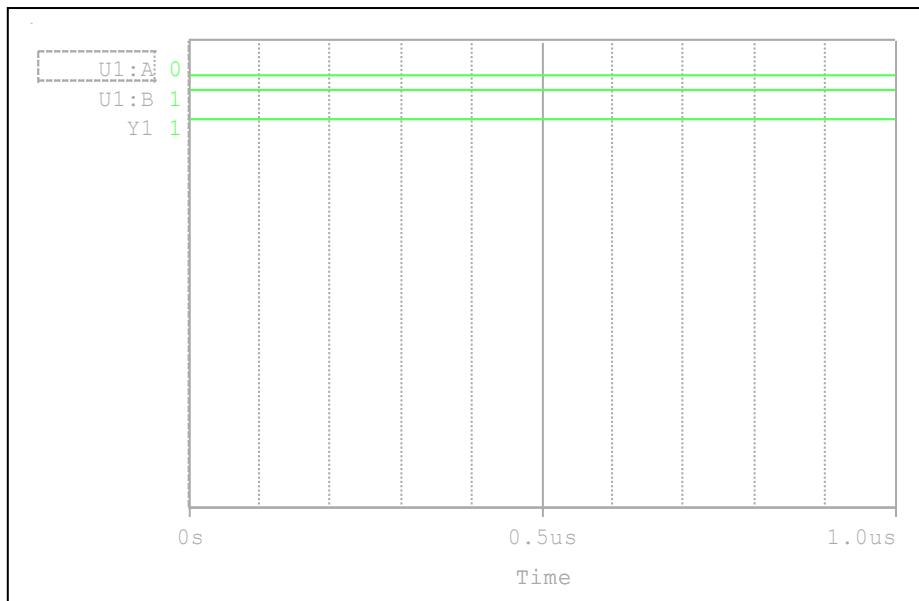


Comparison table

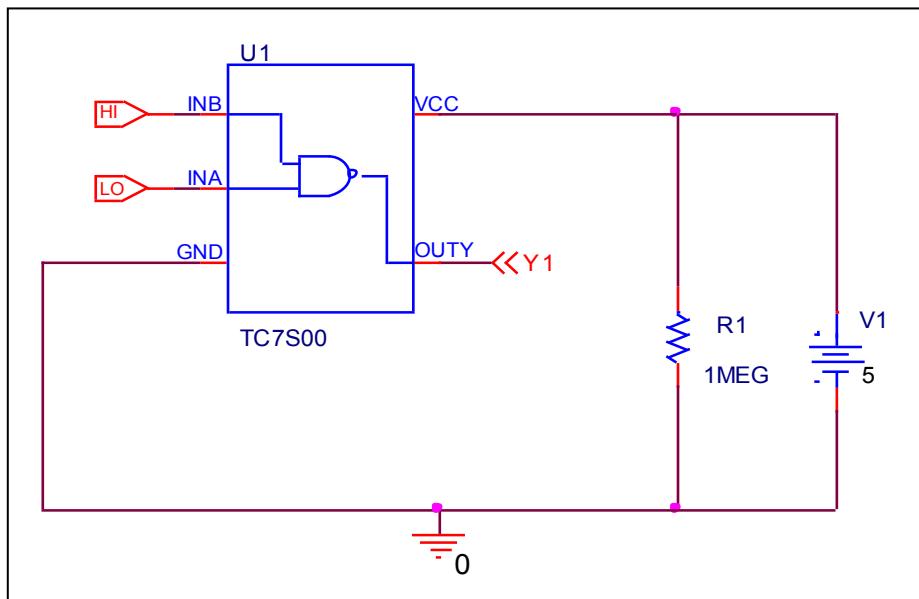
Input		Output		%Error
An	Bn	Yn (Measurement)	Yn (Simulation)	
L	L	H	H	0

## Truth Table

Circuit simulation result



Evaluation circuit

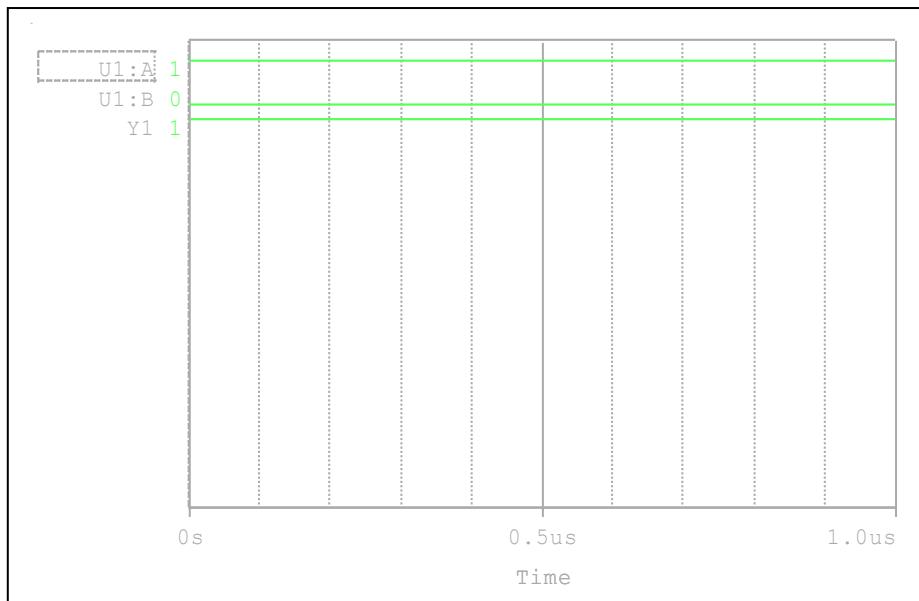


Comparison table

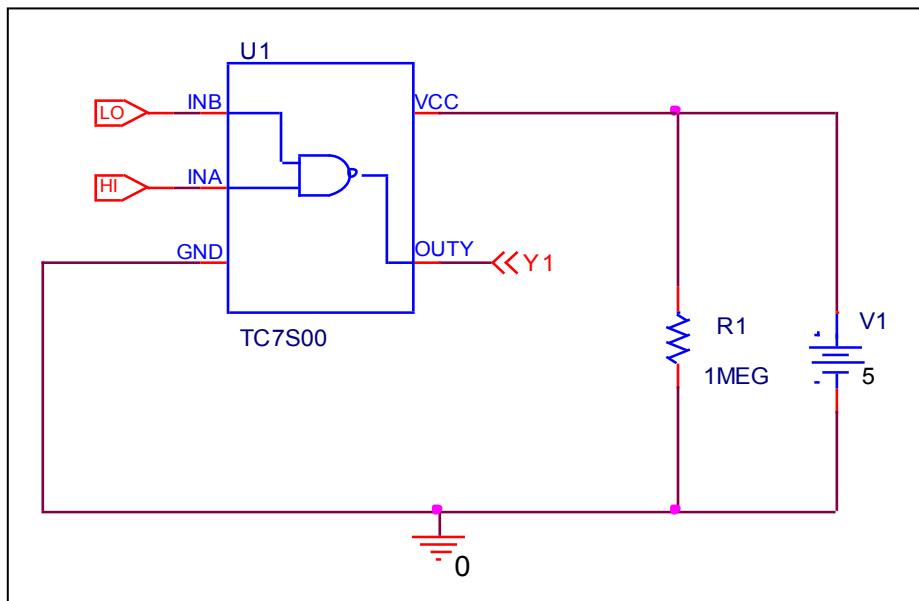
Input		Output		%Error
A <sub>n</sub>	B <sub>n</sub>	Y <sub>n</sub> (Measurement)	Y <sub>n</sub> (Simulation)	
L	H	H	H	0

## Truth Table

Circuit simulation result



Evaluation circuit

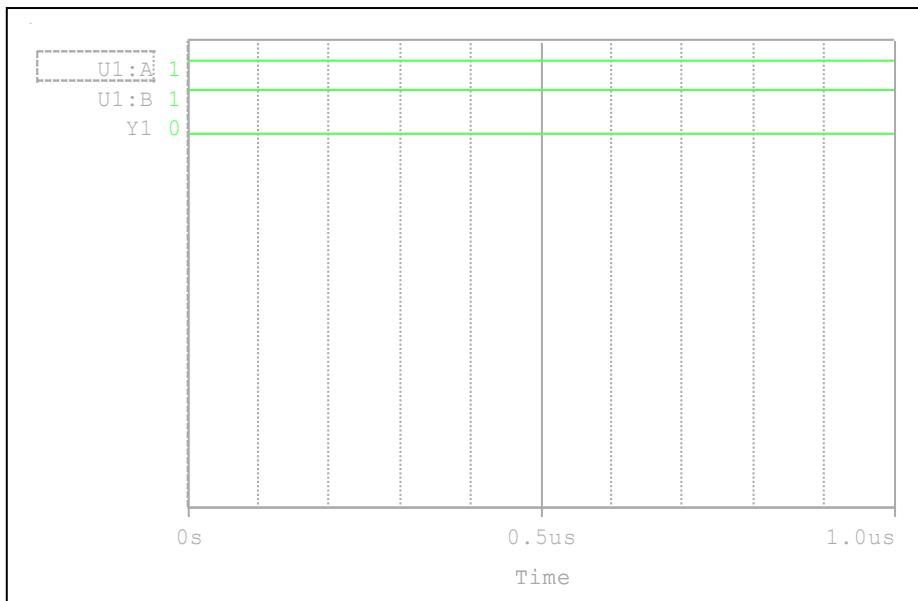


Comparison table

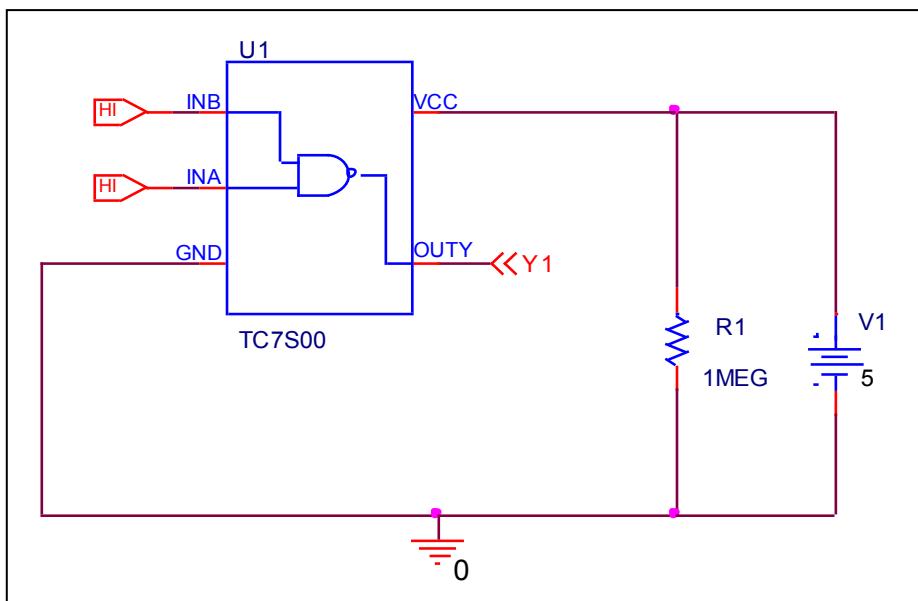
Input		Output		%Error
<b>A<sub>n</sub></b>	<b>B<sub>n</sub></b>	<b>Y<sub>n</sub> (Measurement)</b>	<b>Y<sub>n</sub> (Simulation)</b>	
<b>H</b>	<b>L</b>	<b>H</b>	<b>H</b>	<b>0</b>

## Truth Table

Circuit simulation result



Evaluation circuit

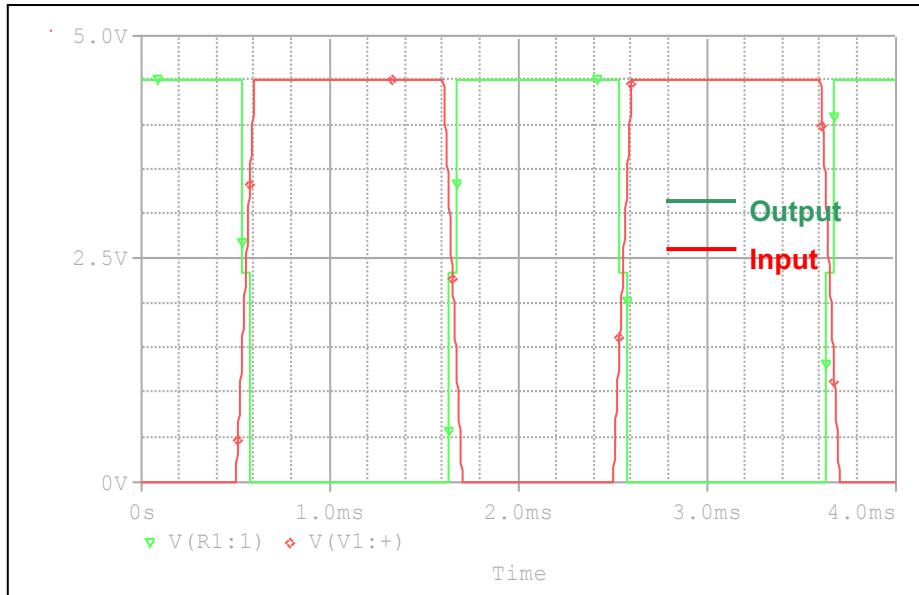


Comparison table

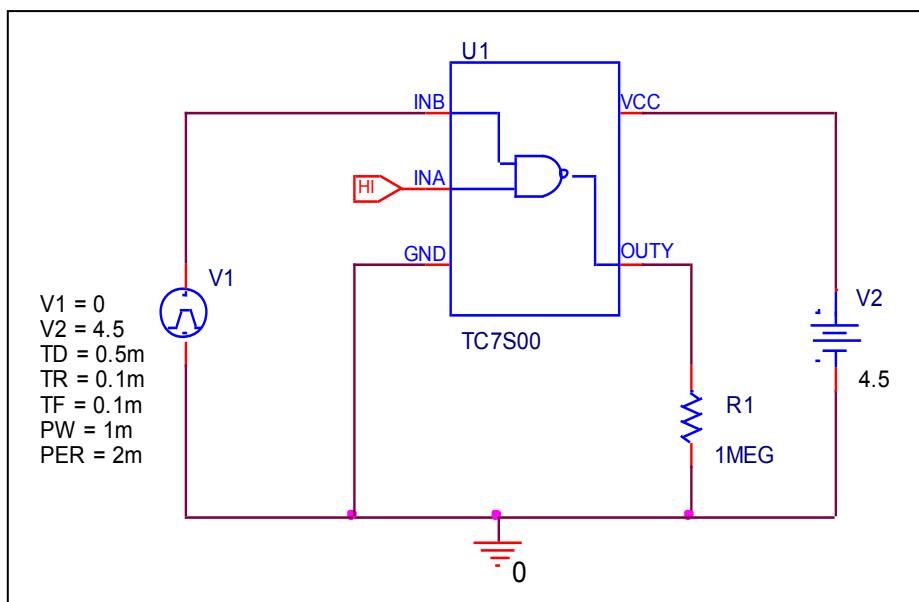
Input		Output		%Error
A <sub>n</sub>	B <sub>n</sub>	Y <sub>n</sub> (Measurement)	Y <sub>n</sub> (Simulation)	
H	H	L	L	0

## High Level and Low Level Input Voltage

Circuit simulation result



Evaluation circuit

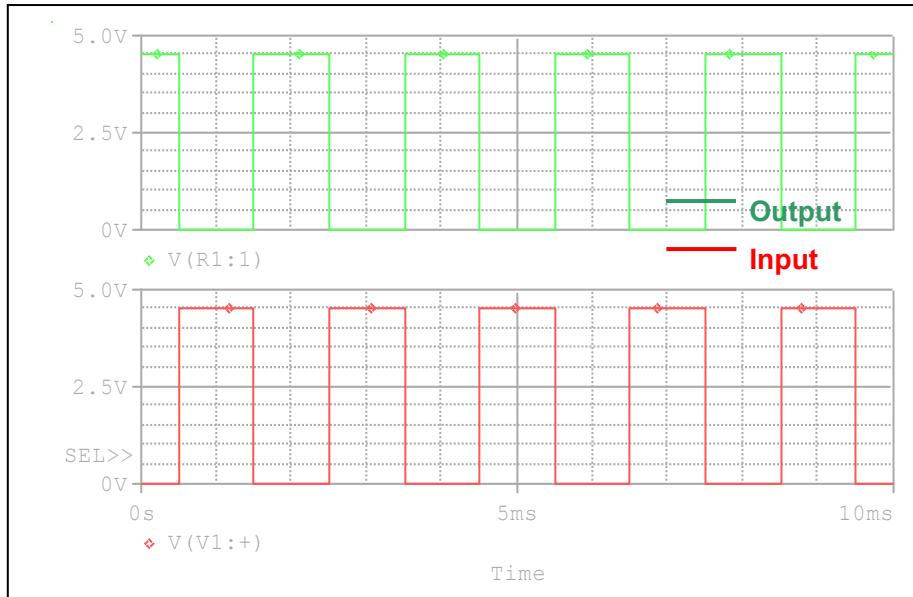


Comparison table

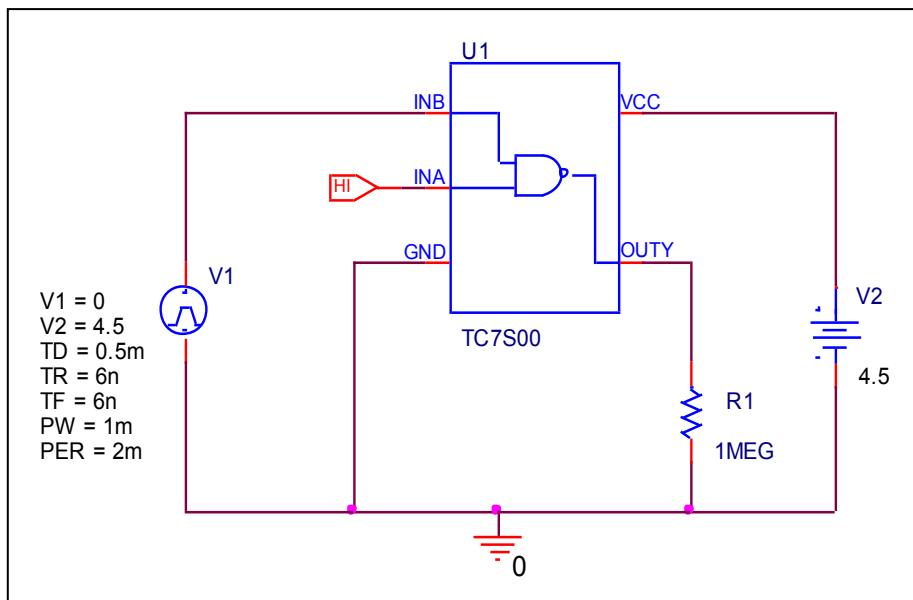
V <sub>CC</sub> = 4.5V	Measurement	Simulation	%Error
V <sub>IH</sub> (V)	3.15	3.2182	2.165
V <sub>IL</sub> (V)	1.35	1.3411	-0.659

## High Level and Low Level Output Voltage

Circuit simulation result



Evaluation circuit

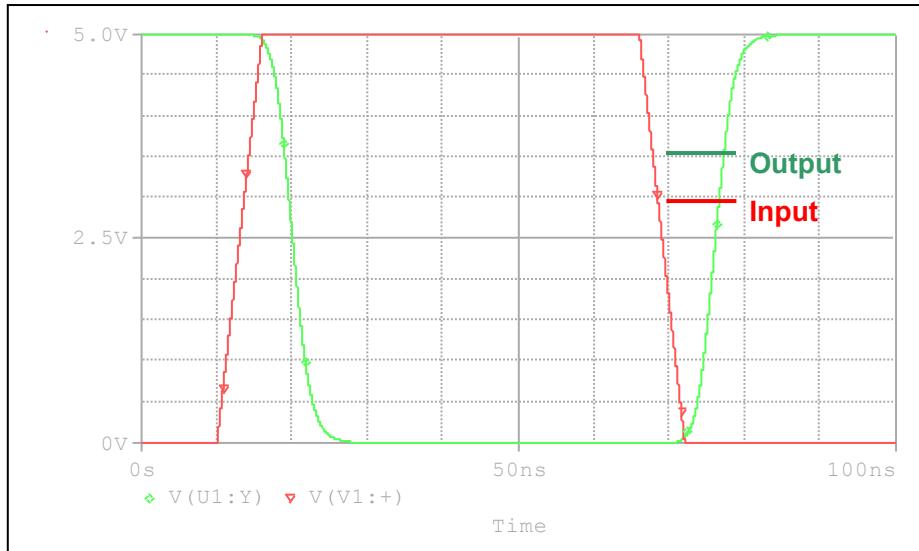


Comparison table

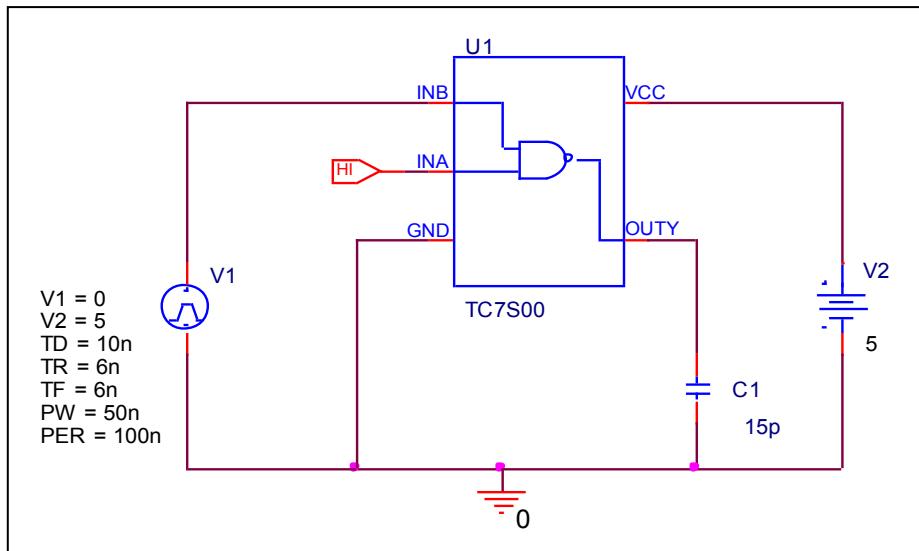
$V_{CC} = 4.5V$	Measurement	Simulation	%Error
$V_{OH}(V)$	4.5	4.4958	-0.093
$V_{OL}(V)$	0	0	0

## Propagation Delay Time

Circuit simulation result



Evaluation circuit



Comparison table

$C_L = 15\text{pF}, V_{CC} = 5\text{V}, t_r = t_f = 6\text{ns}$	Measurement	Simulation	%Error
$t_{TLH} (\text{ns})$	5	5.0012	0.024
$t_{THL} (\text{ns})$	5	5.0131	0.262
$T_{pLH} (\text{ns})$	7	7.0734	1.049
$T_{pHL} (\text{ns})$	7	7.0199	0.284