

# **Device Modeling Report**

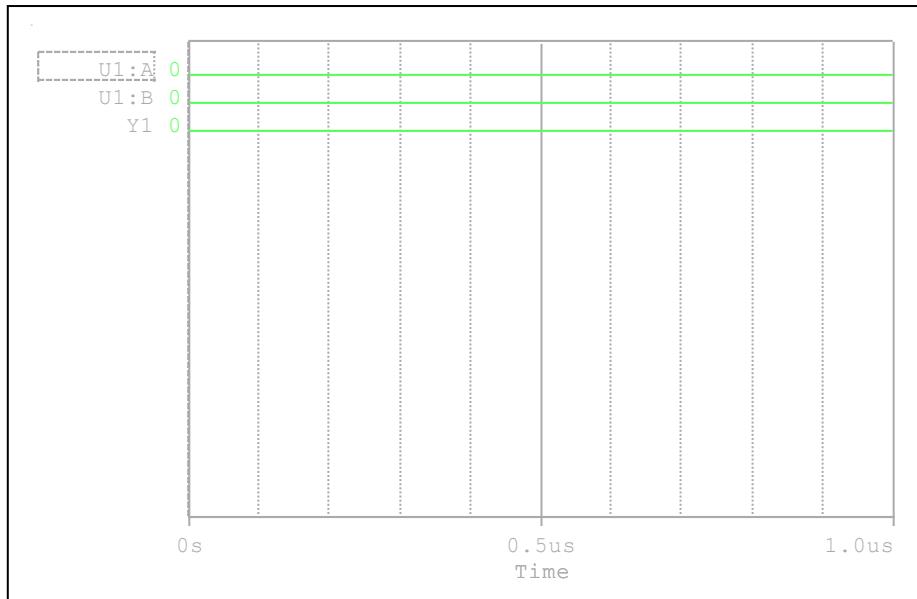
COMPONENTS : CMOS DIGITAL INTEGRATED CIRCUIT  
PART NUMBER : TC7S08F  
MANUFACTURER : TOSHIBA



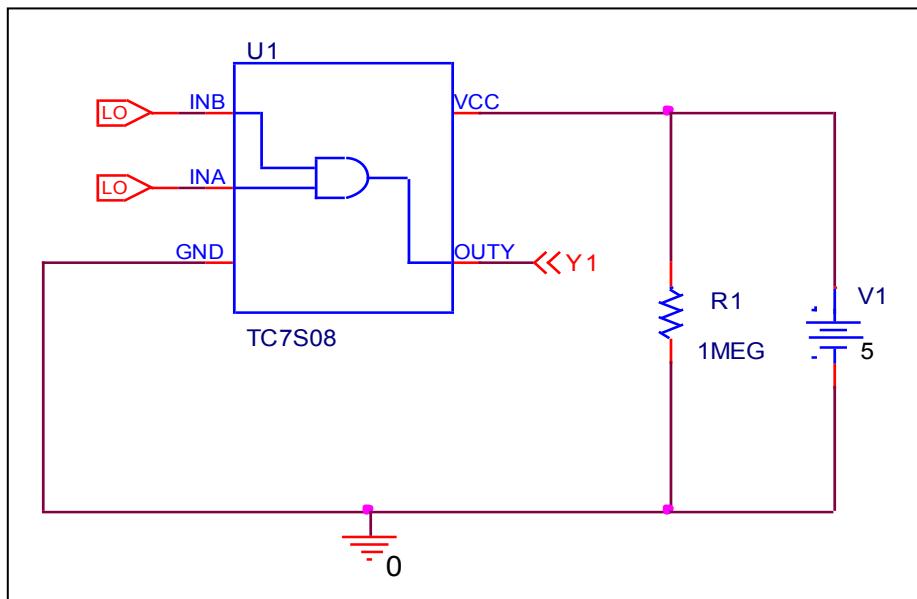
**Bee Technologies Inc.**

## Truth Table

Circuit simulation result



Evaluation circuit

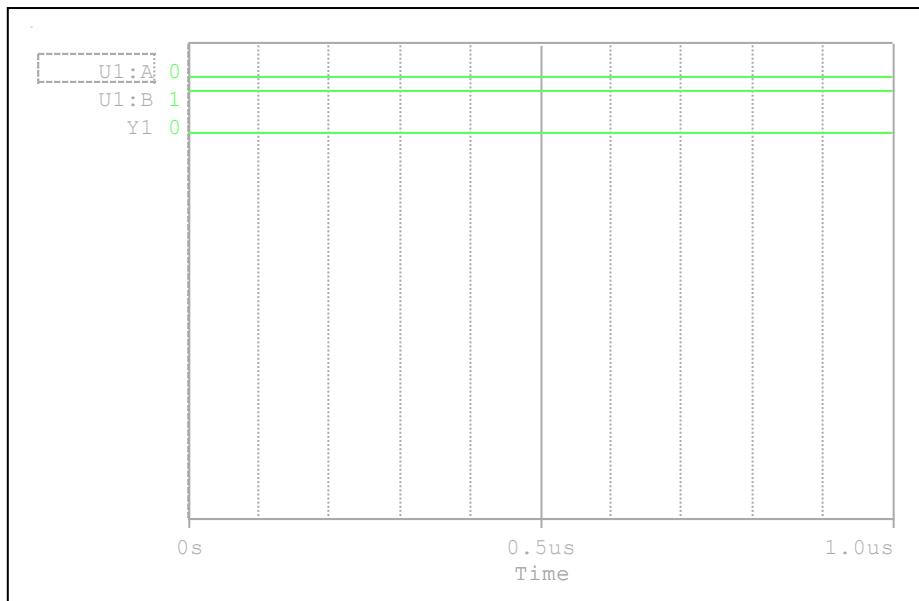


Comparison table

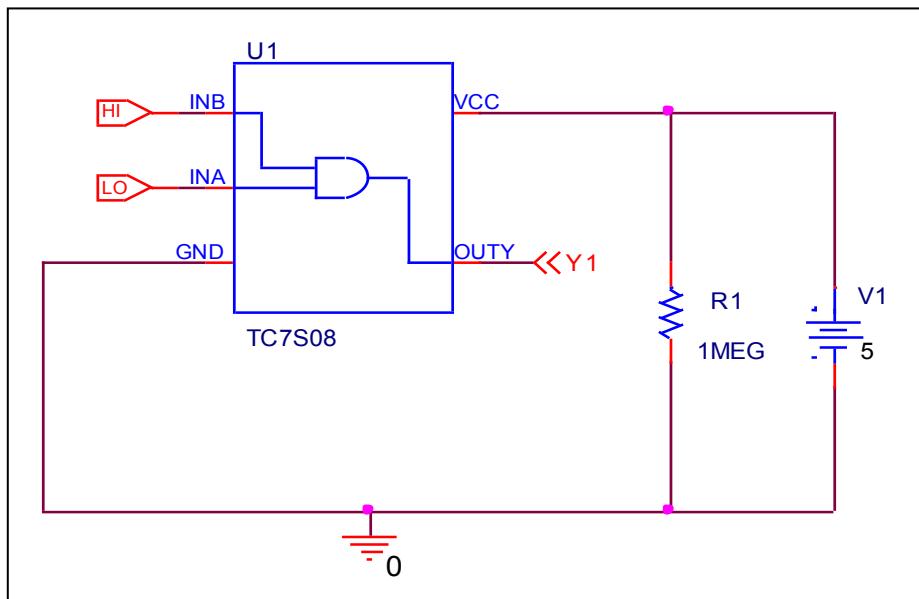
Input		Output		%Error
A <sub>n</sub>	B <sub>n</sub>	Y <sub>n</sub> (Measurement)	Y <sub>n</sub> (Simulation)	
L	L	L	L	0

## Truth Table

Circuit simulation result



Evaluation circuit

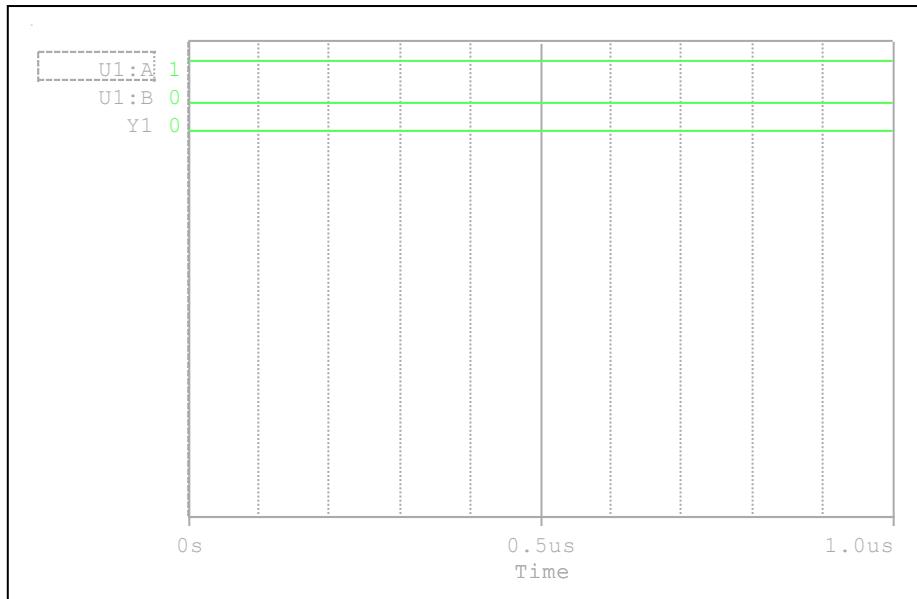


Comparison table

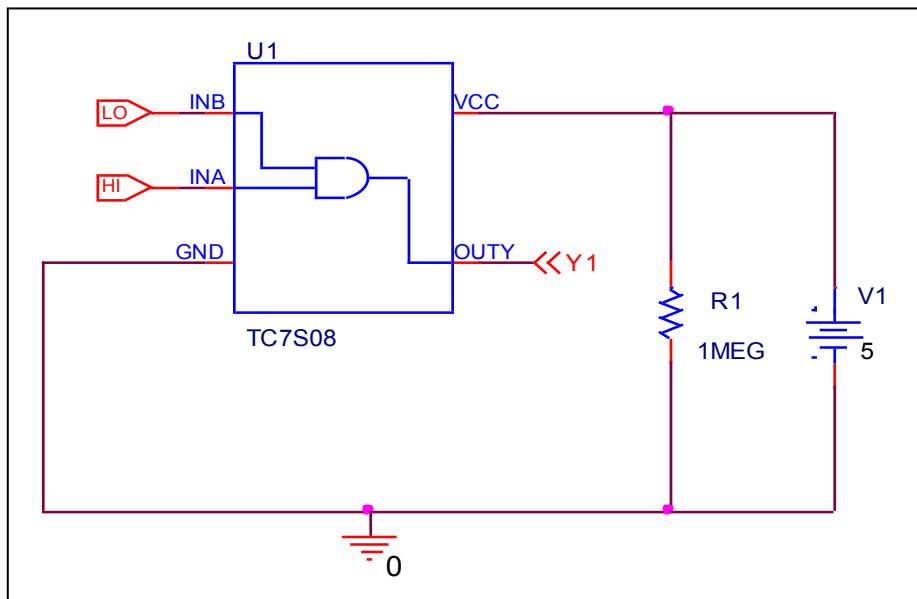
Input		Output		%Error
A <sub>n</sub>	B <sub>n</sub>	Y <sub>n</sub> (Measurement)	Y <sub>n</sub> (Simulation)	
L	H	L	L	0

## Truth Table

Circuit simulation result



Evaluation circuit

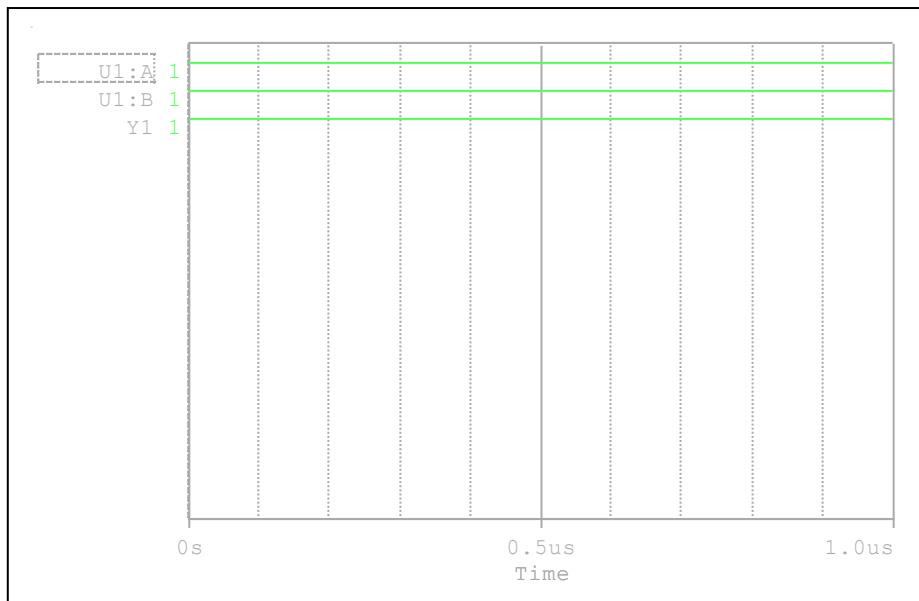


Comparison table

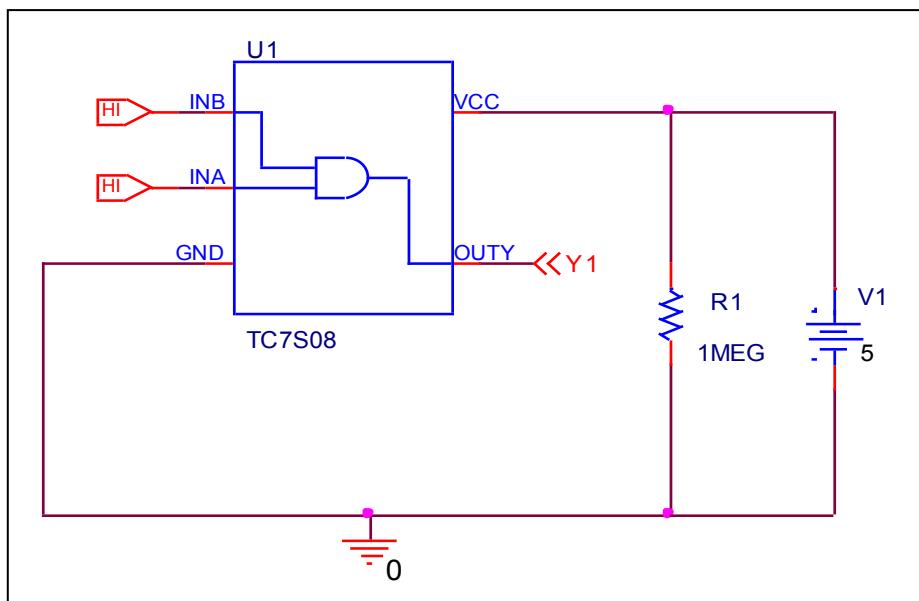
Input		Output		%Error
A <sub>n</sub>	B <sub>n</sub>	Y <sub>n</sub> (Measurement)	Y <sub>n</sub> (Simulation)	
H	L	L	L	0

## Truth Table

Circuit simulation result



Evaluation circuit

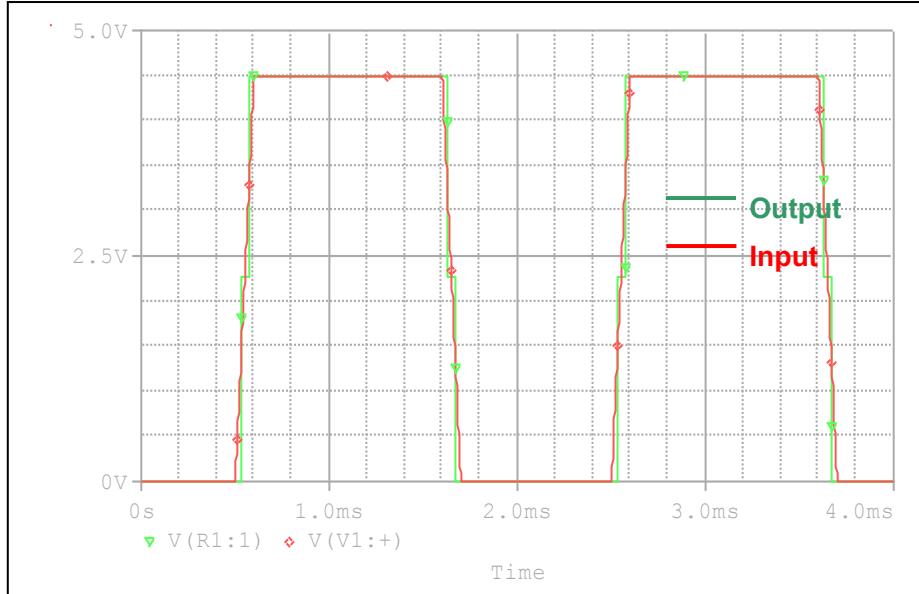


Comparison table

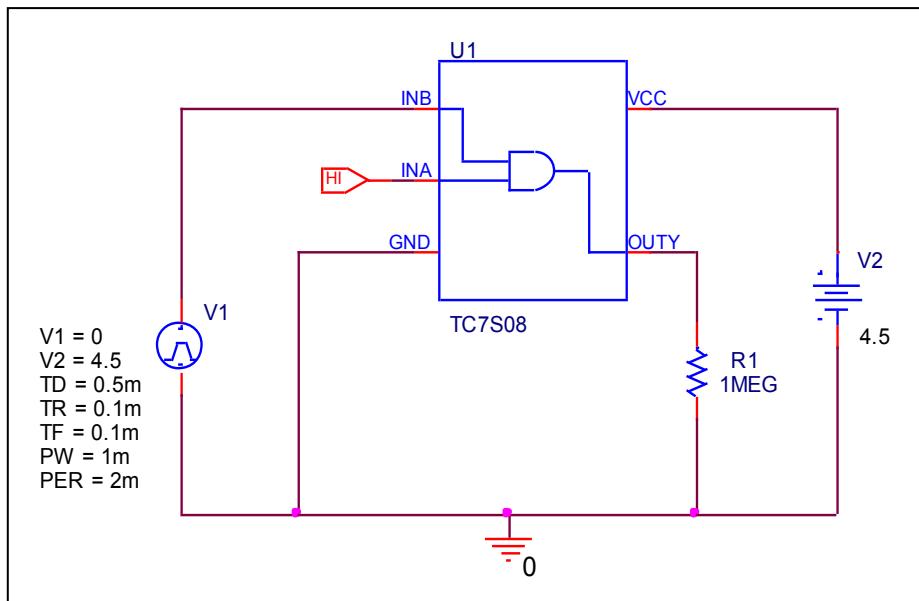
Input		Output		%Error
A <sub>n</sub>	B <sub>n</sub>	Y <sub>n</sub> (Measurement)	Y <sub>n</sub> (Simulation)	
H	H	H	H	0

## High Level and Low Level Input Voltage

Circuit simulation result



Evaluation circuit

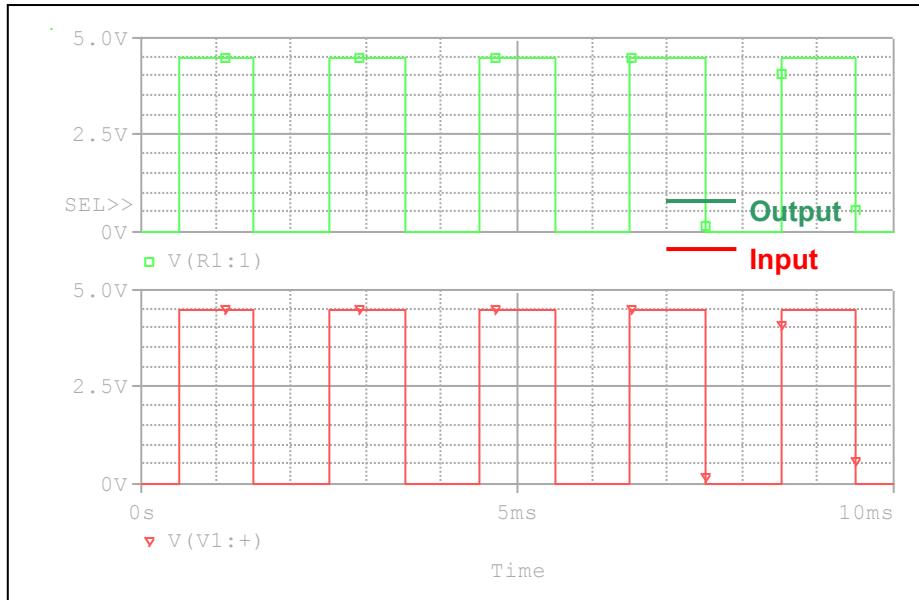


Comparison table

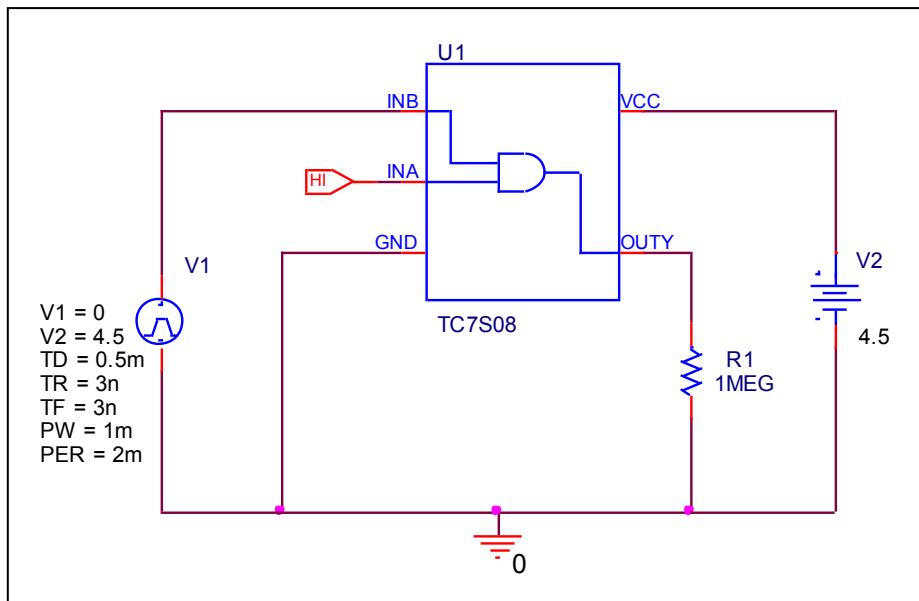
$V_{CC} = 4.5V$	Measurement	Simulation	%Error
$V_{IH} (V)$	3.15	3.218	2.159
$V_{IL} (V)$	1.35	1.3455	-0.333

## High Level and Low Level Output Voltage

Circuit simulation result



Evaluation circuit

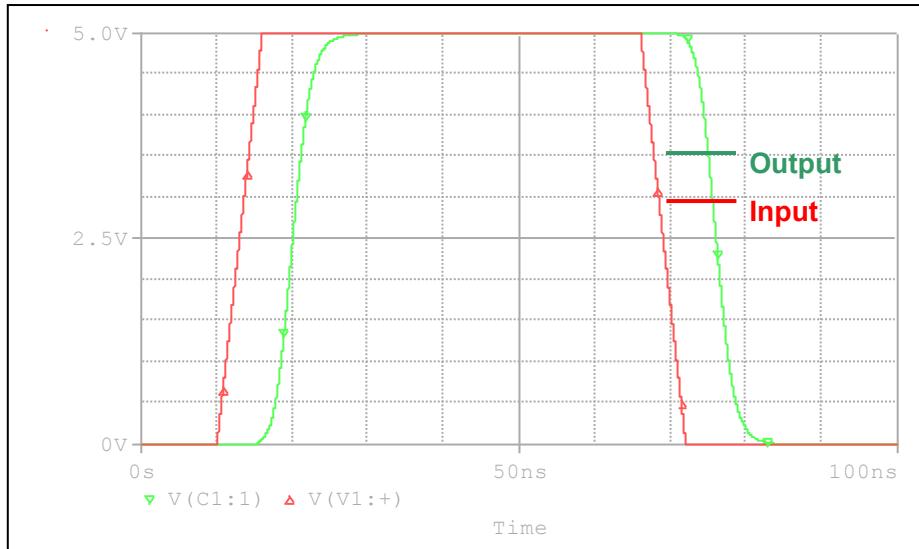


Comparison table

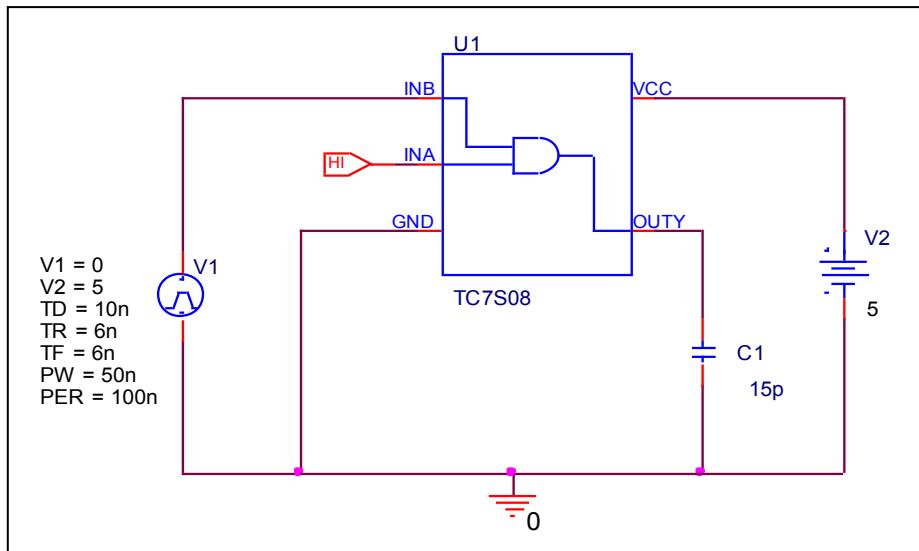
$V_{CC} = 4.5V$	Measurement	Simulation	%Error
$V_{OH}(V)$	4.5	4.4955	-0.1
$V_{OL}(V)$	0	0	0

## Propagation Delay Time

Circuit simulation result



Evaluation circuit



Comparison table

$C_L = 15\text{pF}, V_{CC} = 5\text{V}, t_r = t_f = 6\text{ns}$	Measurement	Simulation	%Error
$t_{TLH} (\text{ns})$	5	5.0236	0.472
$t_{THL} (\text{ns})$	5	5.0224	0.448
$T_{PLH} (\text{ns})$	7	7.0337	0.481
$T_{PHL} (\text{ns})$	7	7.0115	0.164