

Device Modeling Report

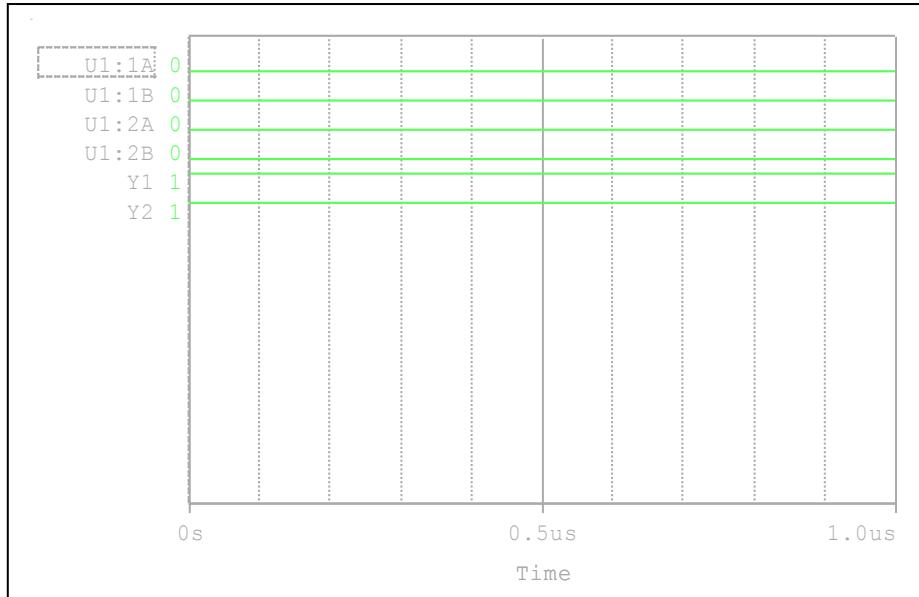
COMPONENTS : CMOS DIGITAL INTEGRATED CIRCUIT
PART NUMBER : TC7WH00FU
MANUFACTURER : TOSHIBA



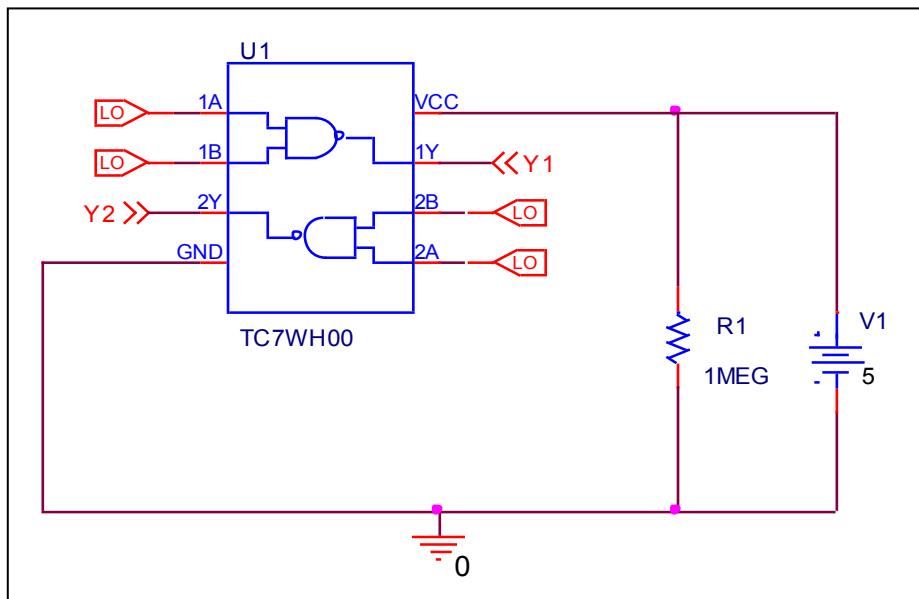
Bee Technologies Inc.

Truth Table

Circuit simulation result



Evaluation circuit

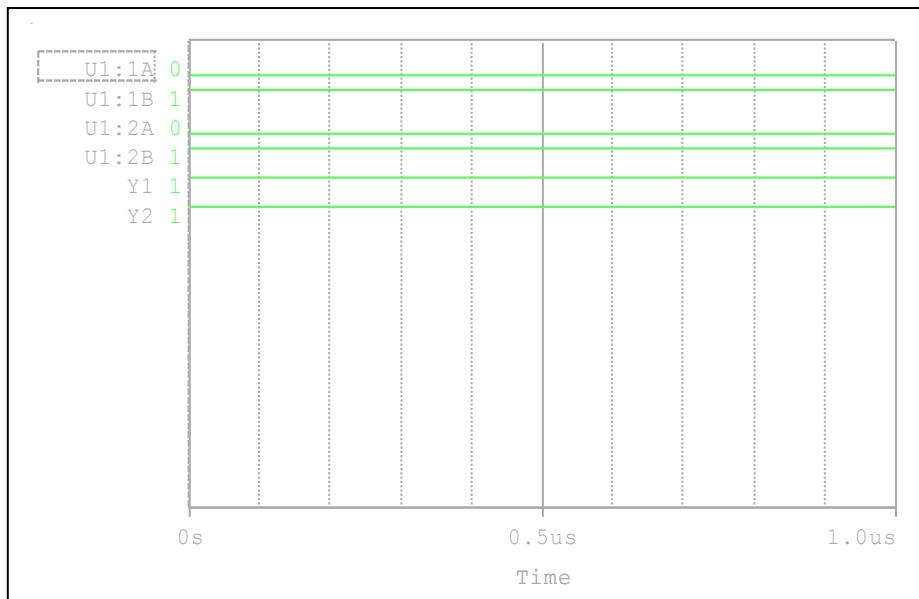


Comparison table

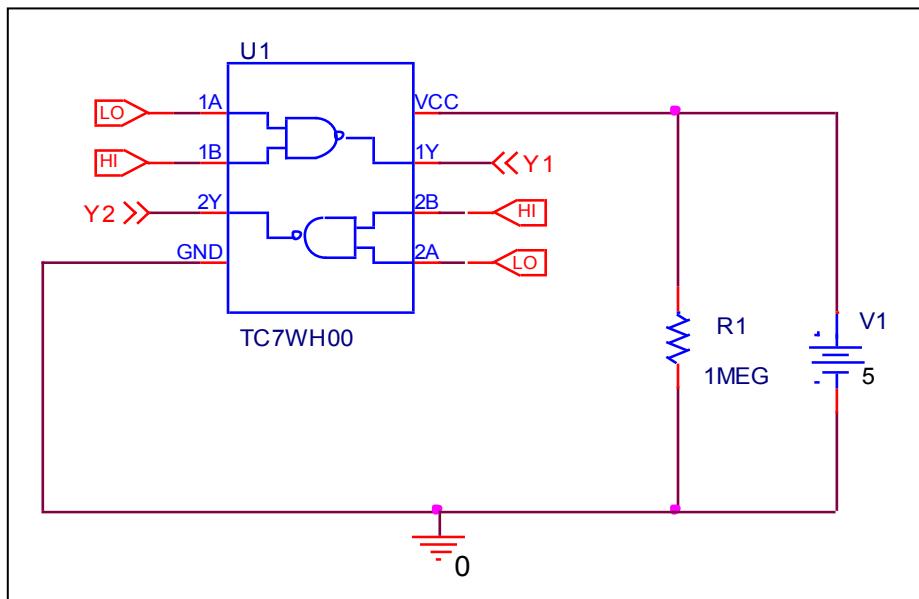
Input		Output		%Error
A _n	B _n	Y _n (Measurement)	Y _n (Simulation)	
L	L	H	H	0

Truth Table

Circuit simulation result



Evaluation circuit

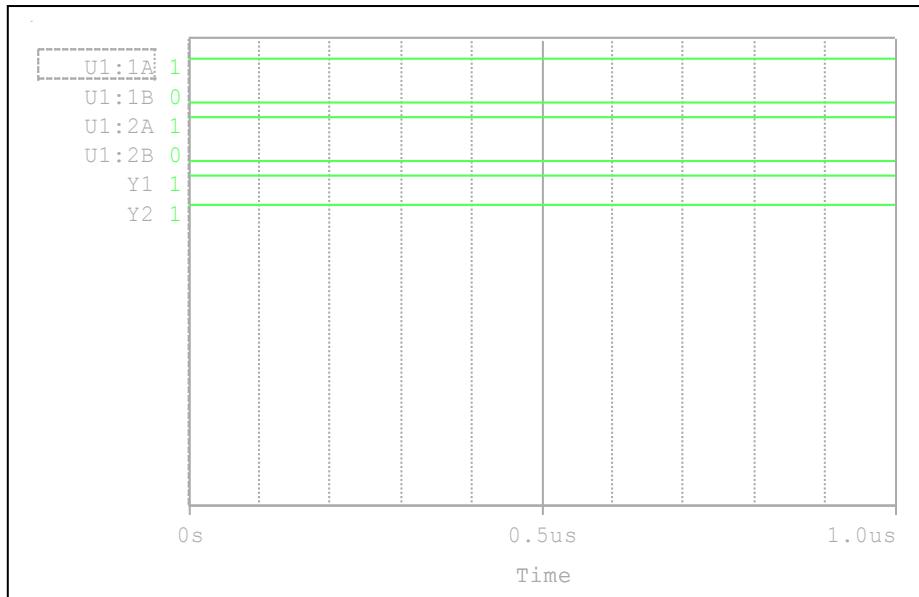


Comparison table

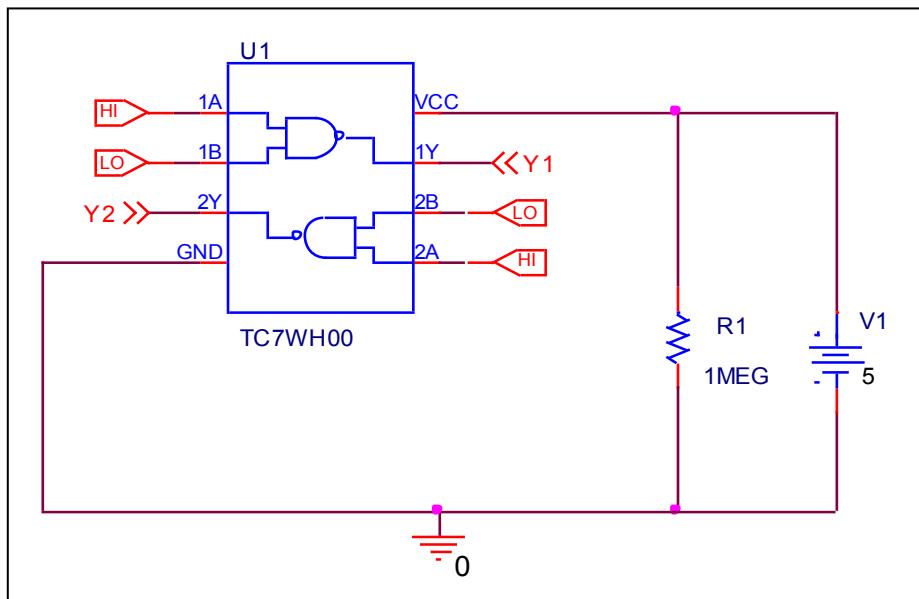
Input		Output		%Error
A _n	B _n	Y _n (Measurement)	Y _n (Simulation)	
L	H	H	H	0

Truth Table

Circuit simulation result



Evaluation circuit

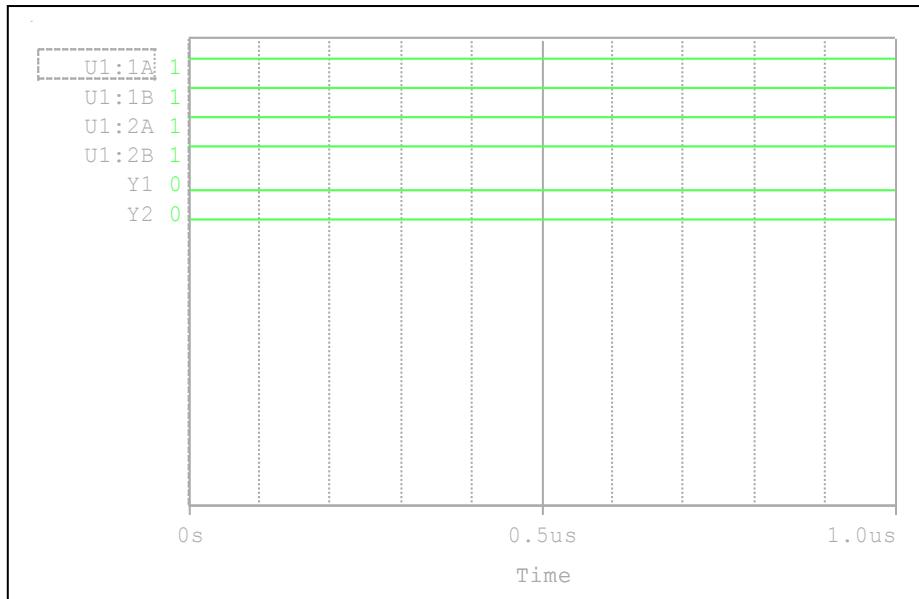


Comparison table

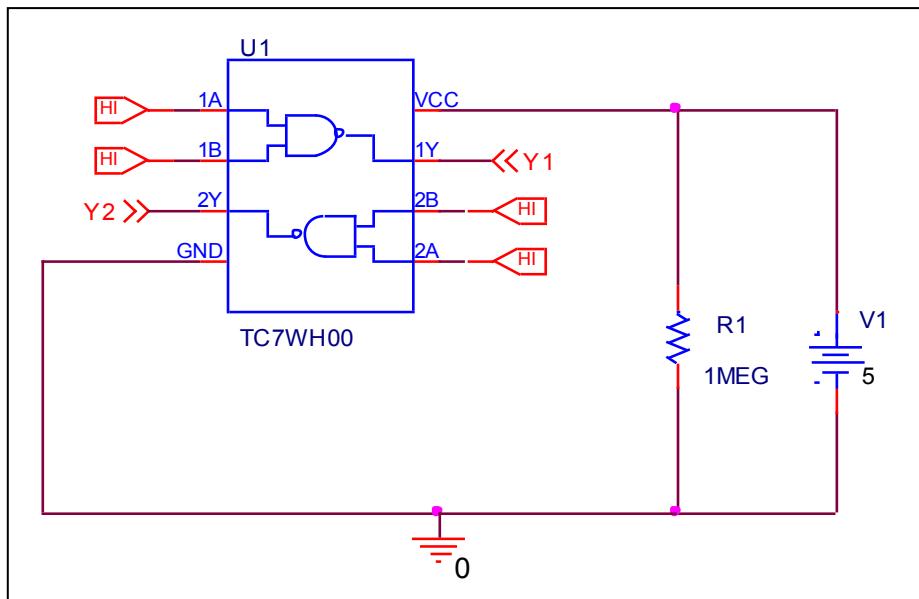
Input		Output		%Error
A _n	B _n	Y _n (Measurement)	Y _n (Simulation)	
H	L	H	H	0

Truth Table

Circuit simulation result



Evaluation circuit

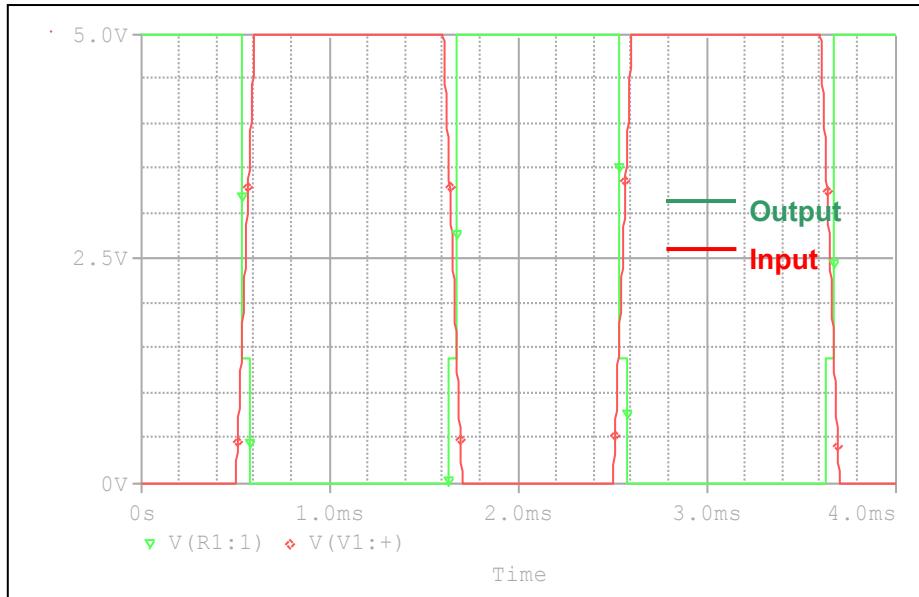


Comparison table

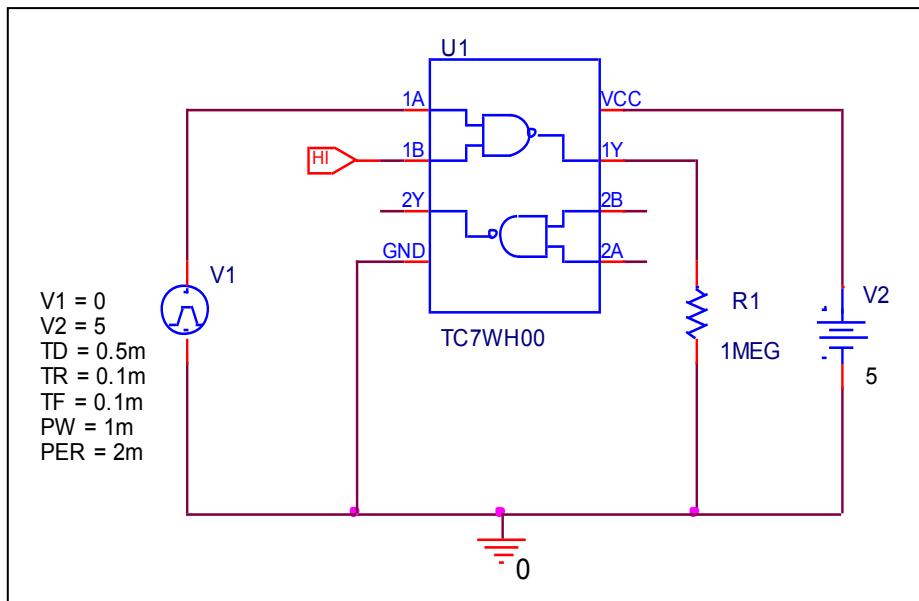
Input		Output		%Error
A _n	B _n	Y _n (Measurement)	Y _n (Simulation)	
H	H	L	L	0

High Level and Low Level Input Voltage

Circuit simulation result



Evaluation circuit

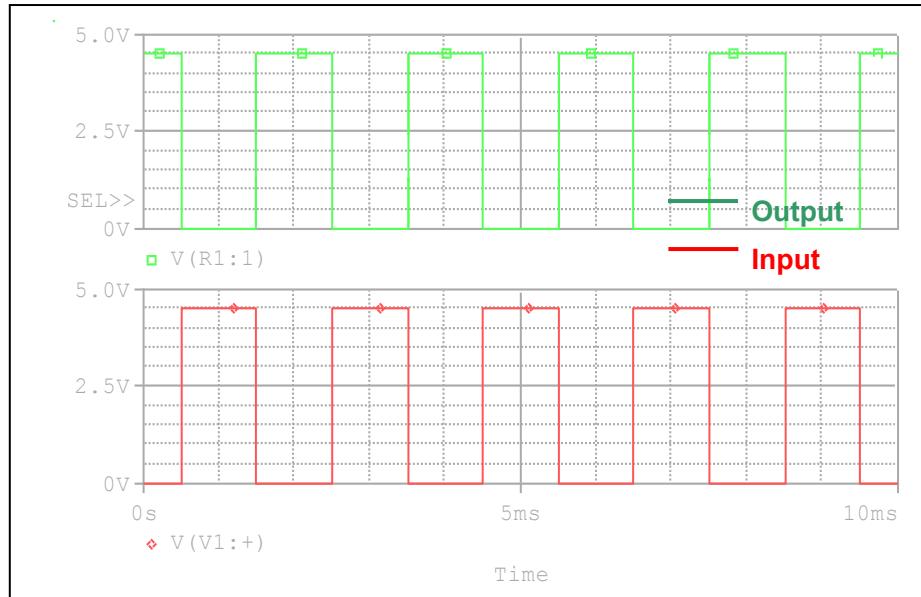


Comparison table

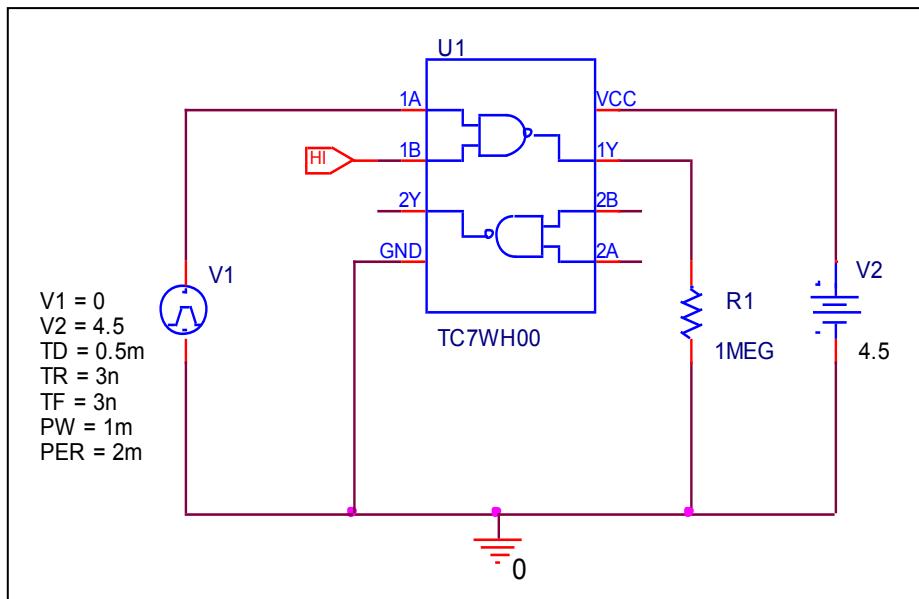
$V_{CC} = 5V$	Measurement	Simulation	%Error
$\text{Min } V_{IH} = (V_{CC} * 0.7) \text{ (V)}$	3.5	3.6252	3.577
$\text{Min } V_{IL} = (V_{CC} * 0.3) \text{ (V)}$	1.5	1.4951	-0.327

High Level and Low Level Output Voltage

Circuit simulation result



Evaluation circuit

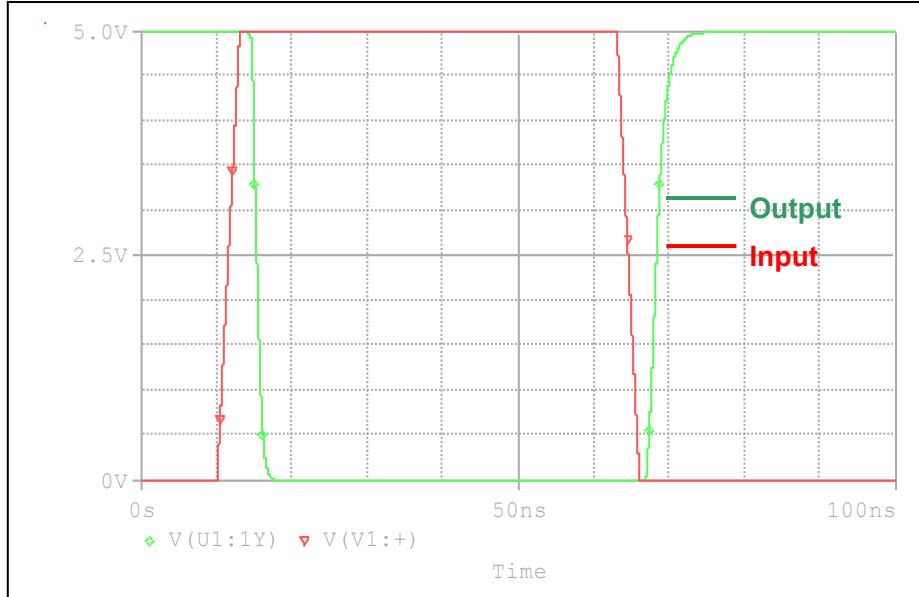


Comparison table

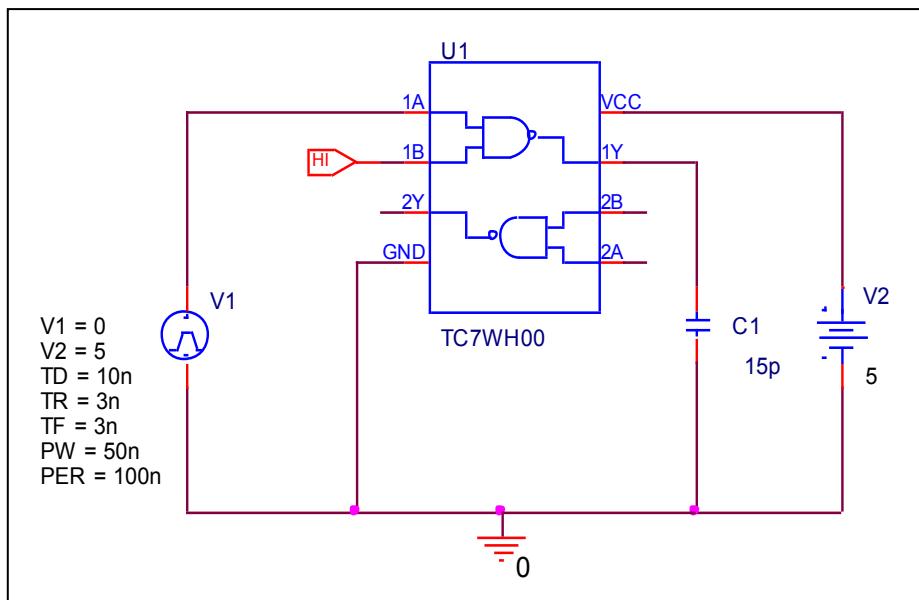
V _{CC} = 4.5V	Measurement	Simulation	%Error
V _{OH} (V)	4.5	4.4965	-0.078
V _{OL} (V)	0	0	0

Propagation Delay Time

Circuit simulation result



Evaluation circuit



Comparison table

$C_L = 15\text{pF}, V_{cc} = 5\text{V}, T_r = T_f = 3\text{ns}$	Measurement	Simulation	%Error
$t_{pLH} (\text{ns})$	3.7	3.7575	1.554
$t_{pHL} (\text{ns})$	3.7	3.7664	1.795